



Customer Information Notification

202412006I : KE1xZP100M96SF0 Datasheet Update from Rev.2 to Rev.3

Note: This notice is NXP Company
Proprietary.

Issue Date: Aug 31, 2025 **Effective Date:** Sep 01, 2025

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Change Category

- | | | | | |
|--|--|--|--|---|
| ☐ Wafer
Fab
Process | ☐ Assembly
Process | ☐ Product Marking | ☐ Test
Process | ☐ Design |
| ☐ Wafer
Fab
Materials | ☐ Assembly
Materials | ☐ Mechanical Specification | ☐ Test
Equipment | ☐ Errata |
| ☐ Wafer
Fab
Location | ☐ Assembly
Location | ☐ Packing/Shipping/Labeling | ☐ Test
Location | ☐ Electrical
spec./Test
coverage |
- ☐ Firmware ☒ Other: Datasheet

Notification Overview

Description

NXP Semiconductors announces that the KE1xZP100M96SF0 Datasheet has been updated to Rev3.

The revision history included in the updated document provides a detailed description of the change.

Change is summarized below:

KE1xZP100M96SF0_Rev3:

1. Correct the pinout diagram from 48LQFP to 64LQFP in Figure 6.

The updated KE1xZP100M96SF0 Datasheet can be found at:

<https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/general-purpose-mcus/ke-series-arm-cortex-m4-m0-plus/ke1xz-arm-cortex-m0-plus-5v-main-stream-mcu-with-nxp-touch-and-can-control:KE1xZ>

Reason

The Datasheet has been updated to provide the correct 64LQFP pinout diagram.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Data Sheet Revision

A new data sheet will be issued

Additional information

Additional documents: [view online](#)

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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NXP Quality Management Team.

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