



PCN (Product Change Notice)

- ☒ Major Change
☐ Minor Change

PCN Number: PCN_UtSTST_20251205

Affected Series: WE-STST

Affected Part Number: 74930000, 74930100

PCN Date: 2025-09-05 (YYYY-MM-DD)

Effective Date: 2025-12-05 (YYYY-MM-DD)

Change Category:

- ☐ Equipment/Location
☒ General Data
☐ Material
☐ Process
☐ Product Design
☐ Shipping/Packaging
☐ Supplier
☐ Software

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Datasheet Change:

☒ Yes ☐ No

Attachment:

☐ Yes ☒ No

DESCRIPTION OF CHANGE:

In order to enhance the product reliability, Würth Elektronik eiSos will improve the Recommended Land Pattern 2, in additional, the Recommended Stencil Design 2 will be added.

For the purpose of a datasheet information enlargement, Würth Elektronik eiSos will add the pin numbers in the test condition.

As a datasheet information amendment, Würth Elektronik eiSos will delete the test condition of the DC Resistance.

There will be no change in form, function, quality and reliability of the product.

The preliminary datasheets of the affected part numbers will be available on the website after the PCN date.

Link to the website: [WE-STST](#)

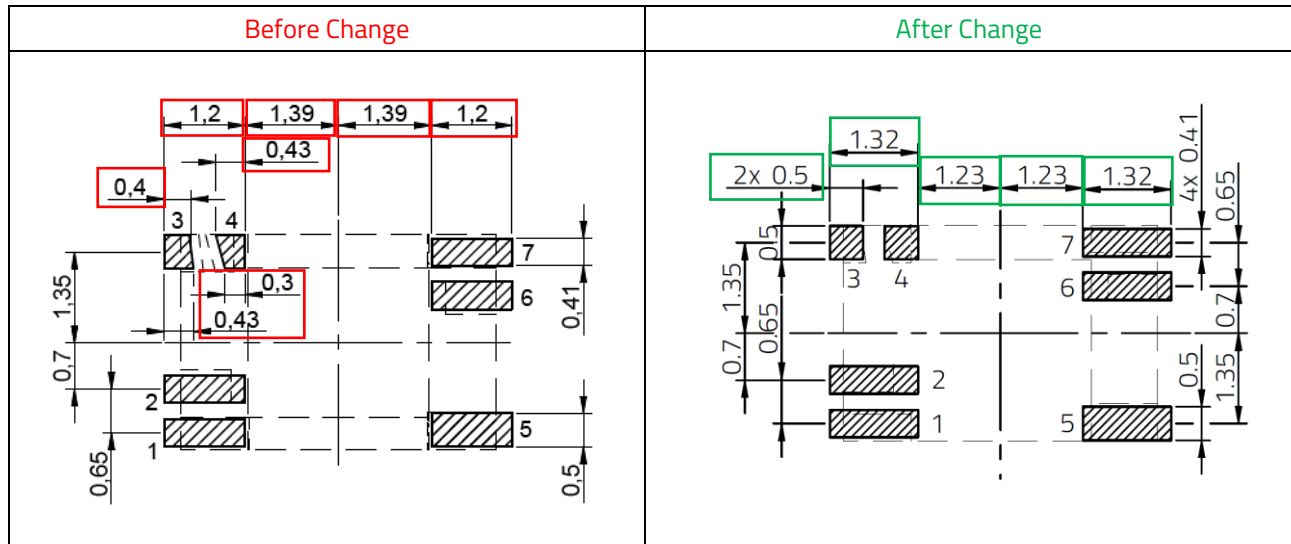
PART NUMBER, REVISION & DATASHEET:

Part Number	Revision before change	Revision after change
74930000	004.001	004.002
74930100	004.000	004.001

The links in the table above are valid until the effective date.

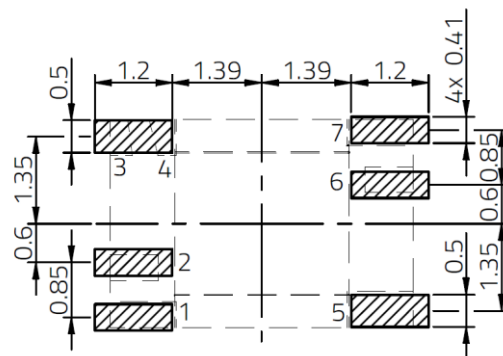
DETAILS OF CHANGE:

- 74930000 and 74930100: Change Recommended Land Pattern 2.



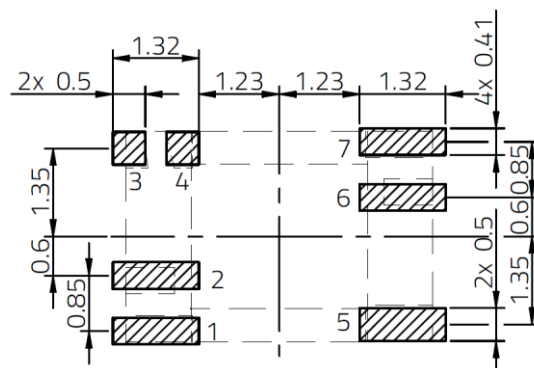
- 74930100: Add the Recommended Stencil Design.

Recommended Stencil Design: [mm]



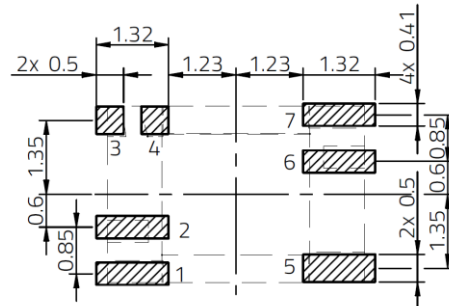
Suggested stencil thickness: 0.08mm

Recommended Stencil Design 2: [mm]



Suggested stencil thickness: 0.08mm

- 74930000: Add the Recommended Stencil Design 2.



Suggested stencil thickness: 0.08mm

- 74930000 and 74930100: Add the pin numbers, delete the test condition of the DC Resistance.

Before Change

Electrical Properties:

Properties		Test conditions	Value	Unit	Tol.
Turns Ratio	n	(1-2):(6-7), 3-4 short	1:1		±1%
DC Resistance	R _{DC}	100 kHz/ 100 mV	4.5	Ω	max.
Inductance	L	100 kHz/ 100 mV	350	μH	min.
Insertion Loss	IL	1-100 MHz	-1	dB	max.
Return Loss	RL	1-30 MHz @ 100 Ω	-18	dB	min.
Return Loss	RL	30-60 MHz @ 100 Ω	-14	dB	min.
Return Loss	RL	60-80 MHz @ 100 Ω	-12	dB	min.
Return Loss	RL	80-100 MHz @ 100 Ω	-10	dB	min.
Differential to Common Mode Rejection Ratio	DCMR	1-60 MHz	-33	dB	min.
Differential to Common Mode Rejection Ratio	DCMR	60-100 MHz	-33	dB	min.
Insulation Test Voltage	V _T	1 min.	1500	V (RMS)	min.

After Change

Electrical Properties:

Properties		Test conditions	Value	Unit	Tol.
Turns Ratio	n	(1-2):(6-7), 3-4 short	1:1		±1%
DC Resistance ¹⁾	R _{DC}		4.5	Ω	max.
Inductance ²⁾	L	100 kHz/ 100 mV	350	μH	min.
Insertion Loss	IL	1-100 MHz	-1	dB	max.
Return Loss	RL	1-30 MHz @ 100 Ω	-18	dB	min.
Return Loss	RL	30-60 MHz @ 100 Ω	-14	dB	min.
Return Loss	RL	60-80 MHz @ 100 Ω	-12	dB	min.
Return Loss	RL	80-100 MHz @ 100 Ω	-10	dB	min.
Differential to Common Mode Rejection Ratio	DCMR	1-60 MHz	-33	dB	min.
Differential to Common Mode Rejection Ratio	DCMR	60-100 MHz	-33	dB	min.
Insulation Test Voltage ³⁾	V _T	1 min.	1500	V (RMS)	min.

- ¹⁾ Pin (1-2), Pin (6-7) 3-4 short
²⁾ Pin (1-2), Pin (6-7) 3-4 short
³⁾ Pin (1-2) to (6-7)



RELIABILITY / QUALIFICATION OF CHANGE:

An additional reliability testing was performed and approved.

Test Item	Sample size	Reference	Test conditions	Acceptance
Solderability	30	IPC-A-610	Preconditions: Steam Aging 8 hrs±15min @93 °C Condition: According JEDEC J-STD-020, Tc=240~245 °C, tp=20~30 s. Electrical Test not required. Magnification 50X	Approved