

High Volume Sensor for Low Pressure Applications

Freescale Semiconductor has developed a low cost, high volume, miniature pressure sensor package which is ideal as a sub-module component or a disposable unit. The unique concept of the Chip Pak allows great flexibility in system design while allowing an economic solution for the designer. This new chip carrier package uses Freescale Semiconductor's unique sensor die with its piezoresistive technology, along with the added feature of on-chip, thin-film temperature compensation and calibration.

NOTE: Freescale Semiconductor is also offering the Chip Pak package in application-specific configurations, which will have an "SPX" prefix, followed by a four-digit number, unique to the specific customer

Features

- Low Cost
- Integrated Temperature Compensation and Calibration
- Ratiometric to Supply Voltage
- Polysulfone Case Material (Medical, Class V Approved)
- Provided in Easy-to-Use Tape and Reel

Typical Applications

- Respiratory Diagnostics
- Air Movement Control
- Controllers
- Pressure Switching

NOTE: The die and wire bonds are exposed on the front side of the Chip Pak (pressure is applied to the backside of the device). Front side die and wire protection must be provided in the customer's housing. Use caution when handling the devices during all processes.

Freescale Semiconductor's MPXC2011DT1/ MPXC2012DT1 Pressure Sensor has been designed for medical usage by combining the performance of Freescale Semiconductor's shear stress pressure sensor design and the use of biomedically approved materials. Materials with a proven history in medical situations have been chosen to provide a sensor that can be used with confidence in applications, such as invasive blood pressure monitoring. It can be sterilized using ethylene oxide. The portions of the pressure sensor that are required to be biomedically approved are the rigid housing and the gel coating.

The rigid housing is molded from a white, medical grade polysulfone that has passed extensive biological testing including: tissue culture test, rabbit implant, hemolysis, intracutaneous test in rabbits, and system toxicity, USP.

The MPXC2011DT1 contains a silicone dielectric gel which covers the silicon piezoresistive sensing element. The gel is a nontoxic, nonallergenic elastomer system which meets all USP XX Biological Testing Class V requirements. The properties of the gel allow it to transmit pressure uniformly to the diaphragm surface, while isolating the internal electrical connections from the corrosive effects of fluids, such as saline solution. The gel provides electrical isolation sufficient to withstand defibrillation testing, as specified in the proposed Association for the Advancement of Medical Instrumentation (AAMI) Standard for blood pressure transducers. A biomedically approved opaque filler in the gel prevents bright operating room lights from affecting the performance of the sensor.

The MPXC2012DT1 is a no-gel option.

**MPXC2011DT1
 MPXC2012DT1**

**PRESSURE SENSORS
 0 to 75 mm HG
 (0 TO 10 kPA)**

CHIP PAK PACKAGE



**MPXC2011DT1/MPXC2012DT1
 CASE 423A-03**

PIN NUMBER

1	GND	3	V _S
2	S+	4	S-

ORDERING INFORMATION

Device Order No.	Case No.	Device Description	Device Marking
MPXC2011DT1	423A	Chip Pak, 1/3 Gel	Date Code, Lot ID
MPXC2012DT1	423A	Chip Pak, No Gel	Date Code, Lot ID

Packaging Information	Reel Size	Tape Width	Quantity
Tape and Reel	330 mm	24 mm	1000 pc/reel

Table 1. Maximum Ratings⁽¹⁾

Rating	Symbol	Value	Unit
Maximum Pressure (Backside)	P_{max}	75	kPa
Storage Temperature	T_{stg}	-25 to +85	°C
Operating Temperature	T_A	+15 to +40	°C

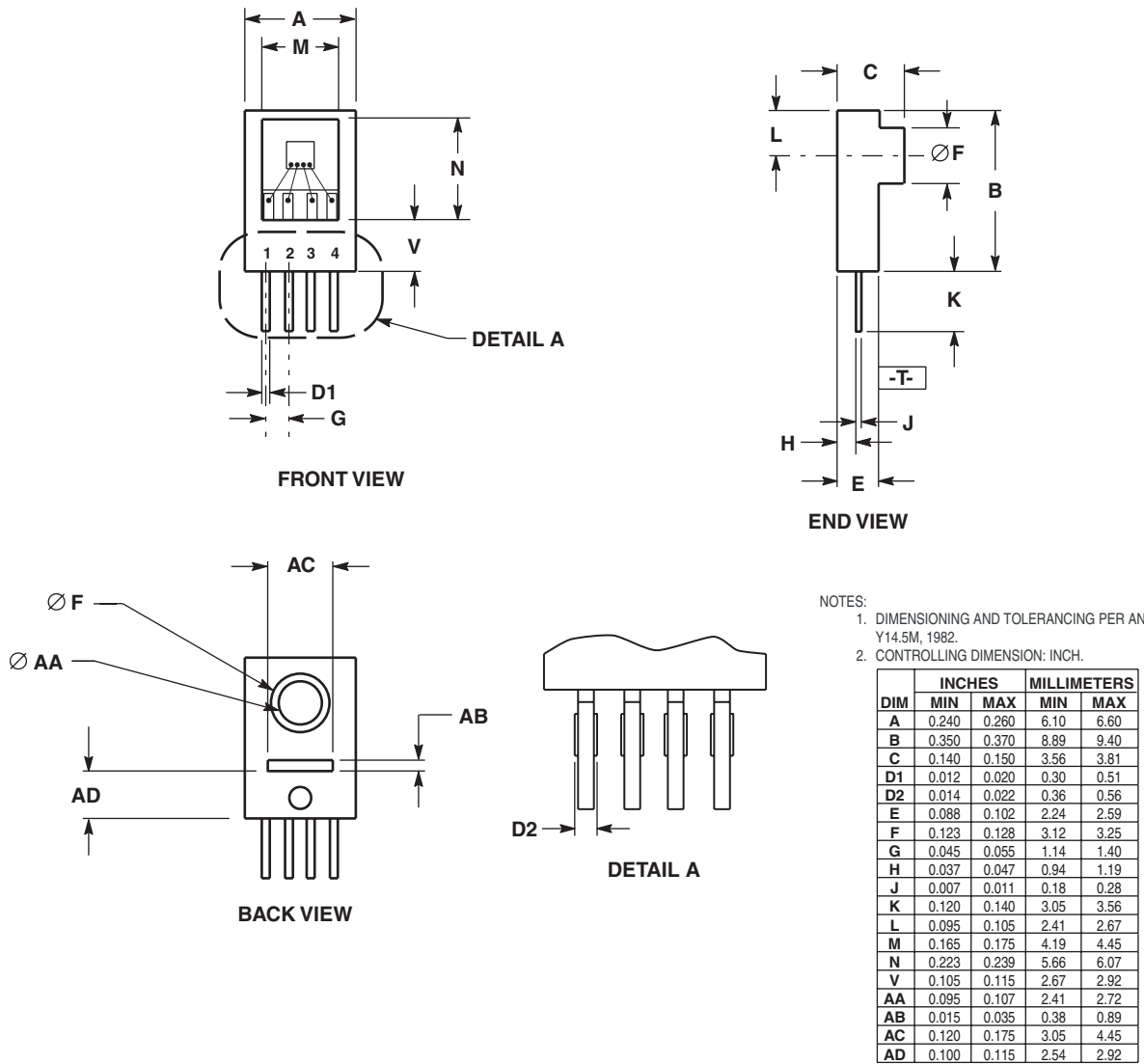
1. Exposure beyond the specified limits may cause permanent damage or degradation to the device.

Table 2. Operating Characteristics ($V_S = 10$ Vdc, $T_A = 25^\circ\text{C}$ unless otherwise noted, $P_1 > P_2$)

Characteristic	Symbol	Min	Typ	Max	Unit
Pressure Range ⁽¹⁾	P_{OP}	0	—	10	kPa
Supply Voltage ⁽²⁾	V_S	—	3	10	Vdc
Supply Current	I_o	—	6.0	—	mAdc
Full Scale Span ⁽³⁾	V_{FSS}	24	25	26	mV
Offset ⁽⁴⁾	V_{off}	-1.0	—	1.0	mV
Sensitivity	$\Delta V/\Delta P$	—	2.5	—	mV/kPa
Linearity ⁽⁵⁾	—	-1.0	—	1.0	% V_{FSS}
Pressure Hysteresis ⁽⁵⁾ (0 to 10 kPa)	—	—	± 0.1	—	% V_{FSS}
Temperature Hysteresis ⁽⁵⁾ (+15°C to +40°C)	—	—	± 0.1	—	% V_{FSS}
Temperature Effect on Full Scale Span ⁽⁵⁾	TCV_{FSS}	-1.0	—	1.0	% V_{FSS}
Temperature Effect on Offset ⁽⁵⁾	TCV_{off}	-1.0	—	1.0	mV
Input Impedance	Z_{in}	1300	—	2550	W
Output Impedance	Z_{out}	1400	—	3000	W
Response Time ⁽⁶⁾ (10% to 90%)	t_R	—	1.0	—	ms
Warm-Up	—	—	20	v	ms
Offset Stability ⁽⁷⁾	—	—	± 0.5	—	% V_{FSS}

- 1.0 kPa (kiloPascal) equals 0.145 psi.
- Device is ratiometric within this specified excitation range. Operating the device above the specified excitation range may induce additional error due to device self-heating.
- Full Scale Span (V_{FSS}) is defined as the algebraic difference between the output voltage at full rated pressure and the output voltage at the minimum rated pressure.
- Offset (V_{off}) is defined as the output voltage at the minimum rated pressure.
- Accuracy (error budget) consists of the following:
 - Linearity: Output deviation from a straight line relationship with pressure, using end point method, over the specified pressure range.
 - Temperature Hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is cycled to and from the minimum or maximum operating temperature points, with zero differential pressure applied.
 - Pressure Hysteresis: Output deviation at any pressure within the specified range, when this pressure is cycled to and from the minimum or maximum rated pressure, at 25°C.
 - TcSpan: Output deviation at full rated pressure over the temperature range of 0 to 85°C, relative to 25°C.
 - TcOffset: Output deviation with minimum rated pressure applied, over the temperature range of 0 to 85°C, relative to 25°C.
- Response Time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.
- Offset stability is the product's output deviation when subjected to 1000 hours of Pulsed Pressure, Temperature Cycling with Bias Test.

PACKAGE DIMENSIONS



CASE 423A-03 ISSUE C CHIP PAK PACKAGE

How to Reach Us:

Home Page:

www.freescale.com

Web Support:

<http://www.freescale.com/support>

USA/Europe or Locations Not Listed:

Freescale Semiconductor, Inc.
Technical Information Center, EL516
2100 East Elliot Road
Tempe, Arizona 85284
+1-800-521-6274 or +1-480-768-2130
www.freescale.com/support

Europe, Middle East, and Africa:

Freescale Halbleiter Deutschland GmbH
Technical Information Center
Schatzbogen 7
81829 Muenchen, Germany
+44 1296 380 456 (English)
+46 8 52200080 (English)
+49 89 92103 559 (German)
+33 1 69 35 48 48 (French)
www.freescale.com/support

Japan:

Freescale Semiconductor Japan Ltd.
Headquarters
ARCO Tower 15F
1-8-1, Shimo-Meguro, Meguro-ku,
Tokyo 153-0064
Japan
0120 191014 or +81 3 5437 9125
support.japan@freescale.com

Asia/Pacific:

Freescale Semiconductor Hong Kong Ltd.
Technical Information Center
2 Dai King Street
Tai Po Industrial Estate
Tai Po, N.T., Hong Kong
+800 2666 8080
support.asia@freescale.com

For Literature Requests Only:

Freescale Semiconductor Literature Distribution Center
P.O. Box 5405
Denver, Colorado 80217
1-800-441-2447 or 303-675-2140
Fax: 303-675-2150
LDCForFreescaleSemiconductor@hibbertgroup.com

Information in this document is provided solely to enable system and software implementers to use Freescale Semiconductor products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits or integrated circuits based on the information in this document.

Freescale Semiconductor reserves the right to make changes without further notice to any products herein. Freescale Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in Freescale Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals", must be validated for each customer application by customer's technical experts. Freescale Semiconductor does not convey any license under its patent rights nor the rights of others. Freescale Semiconductor products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Freescale Semiconductor product could create a situation where personal injury or death may occur. Should Buyer purchase or use Freescale Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold Freescale Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Freescale Semiconductor was negligent regarding the design or manufacture of the part.

Freescale™ and the Freescale logo are trademarks of Freescale Semiconductor, Inc. All other product or service names are the property of their respective owners.

© Freescale Semiconductor, Inc. 2007. All rights reserved.

