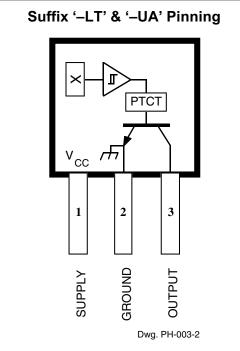
3240

PRELIMINARY INFORMATION C

(subject to change without notice) June 30, 2000



Pinning is shown viewed from branded side.

ABSOLUTE MAXIMUM RATINGS at $T_A = +25^{\circ}C$

Supply Voltage, V _{CC} 26.5 V
Reverse Battery Voltage, V _{RCC} 30 V
Magnetic Flux Density, B Unlimited
Output OFF Voltage, V _{OUT} 26 V
Continuous Output Current,
I _{OUT} Internally Limited
Reverse Output Current, I _{OUT} 50 mA
Package Power Dissipation, P _D . See Graph
Junction Temperature, T_J +170°C
Operating Temperature Range, T _A
Suffix 'E-'40°C to +85°C
Suffix 'L-'40°C to +150°C
Storage Temperature Range,
T _S 65°C to +170°C

CHOPPER-STABILIZED, PRECISION HALL-EFFECT SWITCH

The A3240-- Hall-effect switch is an extremely temperature-stable and stress-resistant sensor especially suited for operation over extended temperature ranges to $+150^{\circ}$ C. Superior high-temperature performance is made possible through dynamic offset cancellation, which reduces the residual offset voltage normally caused by device overmolding, temperature dependencies, and thermal stress.

The device includes on a single silicon chip a voltage regulator, Hall-voltage generator, small-signal amplifier, chopper stabilization, Schmitt trigger, and a short-circuit protected open-collector output to sink up to 25 mA. A south pole of sufficient strength will turn the output ON. An on-board regulator permits operation with supply voltages of 4.2 to 24 volts.

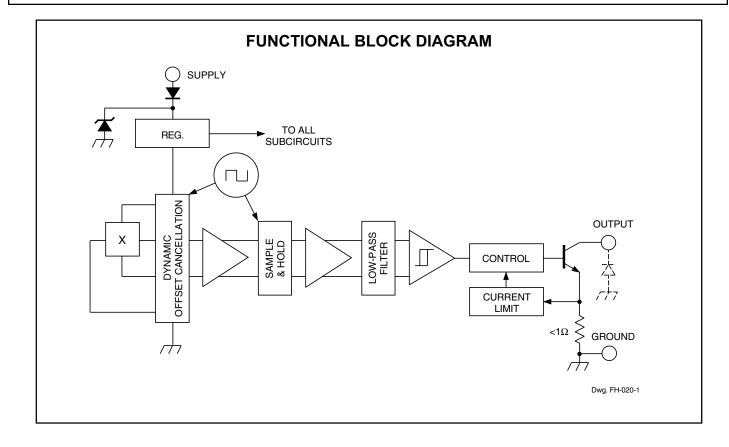
The first character of the part number suffix determines the device operating temperature range; suffix 'E–' is for -40°C to +85°C and 'L–' is -40°C to +150°C. Three package styles provide a magnetically optimized package for most applications. Suffix '–LH' is a modified SOT-23 surface-mount package, '–LT' is a miniature SOT-89/TO-243AA transistor package for surface-mount applications; while suffix '–UA' is a three-lead ultra-mini-SIP for through-hole mounting.

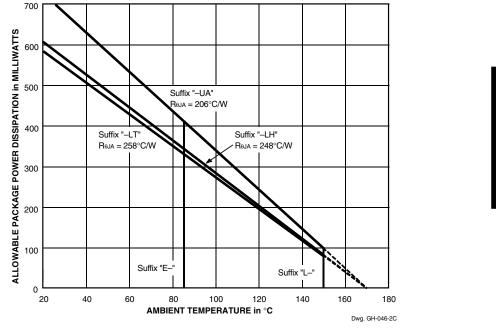
FEATURES

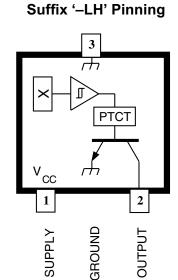
- Resistant to Physical Stress
- Superior Temperature Stability
- Output Short-Circuit Protection
- Operation From Unregulated Supply
- Reverse Battery Protection
- Solid-State Reliability
- Small Size

Always order by complete part number: the prefix 'A' + the basic fourdigit part number + a suffix to indicate operating temperature range + a suffix to indicate package style, e.g., [A3240ELH].









Dwg. PH-003-4



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				Limits		
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Supply Voltage Range	V _{cc}	Operating, T _J < 170°C ¹	4.2	_	24	V
Output Leakage Current	I _{OFF}	V _{OUT} = 24 V, B < B _{RP}	-	_	10	μA
Output Saturation Voltage	V _{OUT(SAT)}	I_{OUT} = 20 mA, B > B _{OP}	-	185	500	mV
Output Current Limit	I _{OM}	B > B _{OP}	30	_	50	mA
Power-On Time	t _{po}	V _{CC} > 4.2 V	-	_	50	μs
Chopping Frequency	f _C		-	340	_	kHz
Output Rise Time	t _r	R _L = 820 Ω, C _L = 20 pF	-	0.2	2.0	μs
Output Fall Time	t _f	R _L = 820 Ω, C _L = 20 pF	-	0.1	2.0	μs
Supply Current	I _{cc}	B < B _{RP} , V _{CC} = 12 V	_	3.0	6.0	mA
		B > B _{OP} , V _{CC} = 12 V	-	4.0	6.0	mA
Reverse Battery Current	I _{cc}	V _{RCC} = -30 V	-	_	-5.0	mA
Zener Voltage	$V_{Z} + V_{D}$	I _{CC} = 15 mA, T _A = 25°C	28	32	37	V
Zener Impedance	z _z + z _D	I _{CC} = 15 mA, T _A = 25°C	_	50	_	Ω

ELECTRICAL CHARACTERISTICS over operating temperature range.

NOTES: 1. Maximum voltage must be adjusted for power dissipation and junction temperature.

2. B_{OP} = operate point (output turns ON); B_{RP} = release point (output turns OFF).

3. Typical Data is at $T_A = +25^{\circ}C$ and $V_{CC} = 12$ V and is for design information only.

MAGNETIC CHARACTERISTICS over operating supply voltage and temperature ranges.

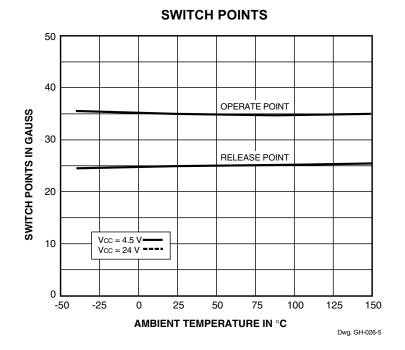
			Limits			
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Operate Point	B _{OP}		_	35	50	G
Release Point	B _{RP}		5.0	25	_	G
Hysteresis	B _{hys}	B _{OP} - B _{RP}	_	10	_	G

NOTES: 1. Complete part number includes a suffix to identify operating temperature range (E or L) and

package type (LH, LT, or UA).

2. Typical Data is at $T_A = +25^{\circ}C$ and $V_{CC} = 12$ V and is for design information only.

TYPICAL OPERATING CHARACTERISTICS as a function of temperature



OUTPUT SATURATION VOLTAGE SUPPLY CURRENT 300 6.0 Іоит = 20 mA Vcc = 12 V Vcc = 12 V 5.0 SATURATION VOLTAGE IN mV SUPPLY CURRENT IN mA 200 4.0 OUTPUT ON, B > BOP 100 3.0 OUTPUT OFF, B < BRP 0 2.0 -25 0 25 75 100 150 -50 50 125 -50 -25 25 75 100 0 50 125 150 AMBIENT TEMPERATURE IN °C AMBIENT TEMPERATURE IN °C

Dwg. GH-029-4

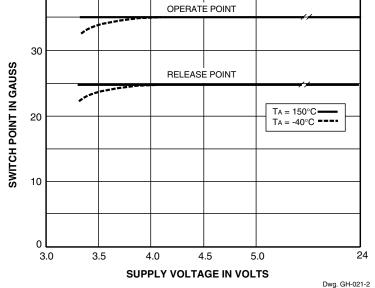
* Complete part number includes a suffix denoting operating temperature range (E or L) and package type (LH, LT, or UA).



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TYPICAL OPERATING CHARACTERISTICS as a function of supply voltage

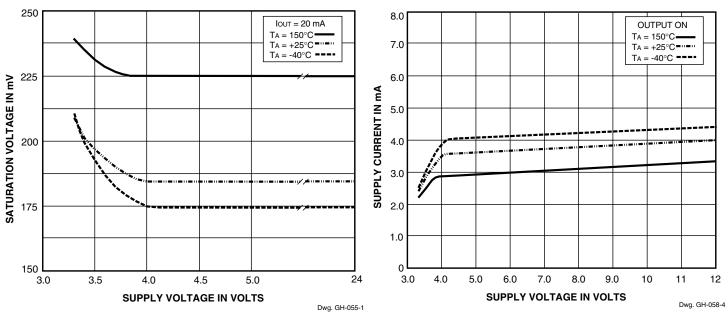
OPERATE POINT



OUTPUT SATURATION VOLTAGE

40

SUPPLY CURRENT

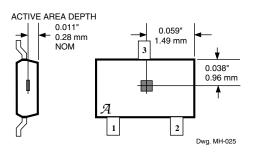


* Complete part number includes a suffix denoting operating temperature range (E or L) and package type (LH, LT, or UA).

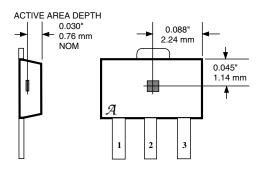
SENSOR LOCATIONS

(±0.005" [0.13 mm] die placement)

Package Designator 'LH'

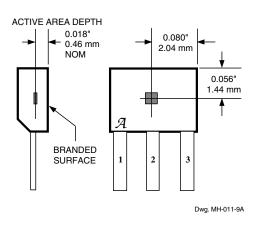


Package Designator 'LT'



Dwg. MH-008-8

Package Designators 'UA' and 'UA-TL'



Although sensor location is accurate to three sigma for a particular design, product improvements may result in small changes to sensor location.



CRITERIA FOR DEVICE QUALIFICATION

All Allegro sensors are subjected to stringent qualification requirements prior to being released to production. To become qualified, except for the destructive ESD tests, no failures are permitted.

Qualification Test	Test Method and Test Conditions	Test Length	Samples	Comments
Biased Humidity (HAST)	T _A = 130°C, RH = 85%	50 hrs	77	$V_{CC} = V_{OUT} = 5 V$
High-Temperature Operating Life (HTOL)	JESD22-A108, T _A = 150°C, T _J ≤ 170°C	408 hrs	77	V _{CC} = 24 V, V _{OUT} = 20 V
Accelerated HTOL	T _A = 175°C, T _J = 190°C	504 hrs	77	V _{CC} = 24 V, V _{OUT} = 20 V
Autoclave, Unbiased	JESD22-A102, Condition C, T _A = 121°C, 15 psig	96 hrs	77	
High-Temperature (Bake) Storage Life	MIL-STD-883, Method 1008, T _A = 170°C	1000 hrs	77	
Temperature Cycle	MIL-STD-883, Method 1010, -65°C to +150°C	500 cycles	77	
Latch-Up	_	Pre/Post Reading	6	
Electro-Thermally Induced Gate Leakage	_	Pre/Post Reading	6	
ESD, Human Body Model	CDF-AEC-Q100-002	Pre/Post Reading	x per test	Test to failure, All leads > TBD
Electrical Distributions	Per Specification	_	30	

FUNCTIONAL DESCRIPTION

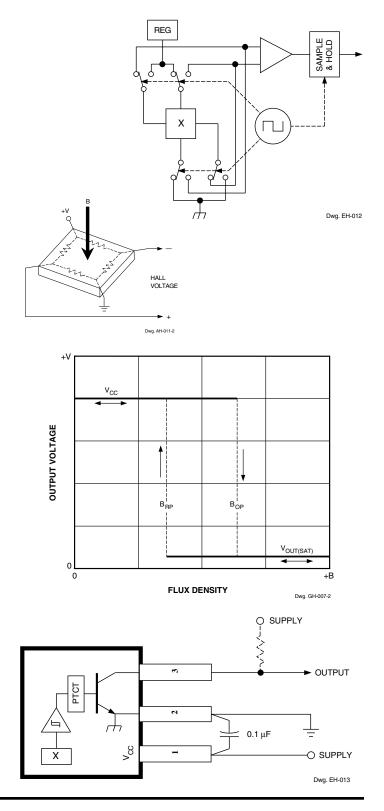
Chopper-Stabilized Technique. These devices use a proprietary dynamic offset cancellation technique, with an internal high-frequency clock to reduce the residual offset voltage of the Hall element that is normally caused by device overmolding, temperature dependencies, and thermal stress. This technique produces devices that have an extremely stable quiescent Hall output voltage, are immune to thermal stress, and have precise recoverability after temperature cycling. This technique will also slightly degrade the device output repeatability.

The Hall element can be considered as a resistor array similar to a Wheatstone bridge. A large portion of the offset is a result of the mismatching of these resistors. The chopperstabilizing technique cancels the mismatching of the resistors by changing the direction of the current flowing through the Hall plate and Hall voltage measurement taps, while maintaining the Hall-voltage signal that is induced by the external magnetic flux. The signal is, then, captured by a sample-andhold circuit.

Operation. The output of these devices switches low (turns ON) when a magnetic field (south pole) perpendicular to the Hall sensor exceeds the operate point threshold (B_{OP}). After turn-ON, the output is capable of sinking 25 mA and the output voltage is $V_{OUT(SAT)}$. When the magnetic field is reduced below the release point (B_{RP}), the device output goes high (turns OFF). The difference in the magnetic operate and release points is the hysteresis (B_{hys}) of the device. This built-in hysteresis allows clean switching of the output even in the presence of external mechanical vibration and electrical noise.

Applications. It is strongly recommended that an external bypass capacitor be connected (in close proximity to the Hall sensor) between the supply and ground of the device to reduce both external noise and noise generated by the chopper-stabilization technique.

The simplest form of magnet that will operate these devices is a bar magnet with the south pole towards the branded surface of the device. Many other methods of operation are possible. Extensive applications information on magnets and Hall-effect sensors is also available in the *Allegro Electronic Data Book* AMS-702 or *Application Note* 27701, or at www.allegromicro.com





PACKAGE DESIGNATOR 'LH'

(fits SC-74A solder-pad layout)

0.0079

0.0050

0.020 MIN

Dwg. MA-010-3B in

0° TO 8°

Dimensions in Millimeters (controlling dimensions)

Dimensions in Inches (for reference only)

0.118

0.106

0.037

0.043

0.035

0.049

0.035

BSC

0.122

0.114

3

2

0.020

0.012

H

0.083

0.073

0.022

REF

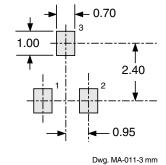
0.006

0.000

3.10 2.90 0.50 0.20 0.30 0.127 2.10 З 3.00 0.25 мім 1.85 2.70 2 1 4 日 0.55 0° то 8° REF 0.95 BSC 1.10 0.90 0.15 1.25 0.00 0.90

Dwg. MA-010-3B mm

- 0.028 3 0.039 0.094 0.037 Dwg. MA-011-3 in

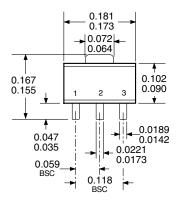


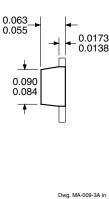
NOTES: 1. Tolerances on package height and width represent allowable mold offsets. Dimensions given are measured at the widest point (parting line).

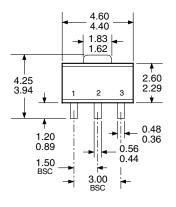
- Exact body and lead configuration at vendor's option within limits shown. 2.
- 3. Height does not include mold gate flash.
- Where no tolerance is specified, dimension is nominal. 4.

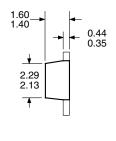
PACKAGE DESIGNATOR 'LT' (SOT-89/TO-243AA)

Dimensions in Inches (for reference only) Dimensions in Millimeters (controlling dimensions)

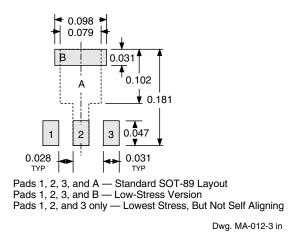


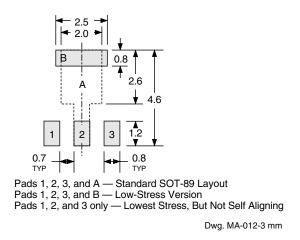






Dwg. MA-009-3A mm



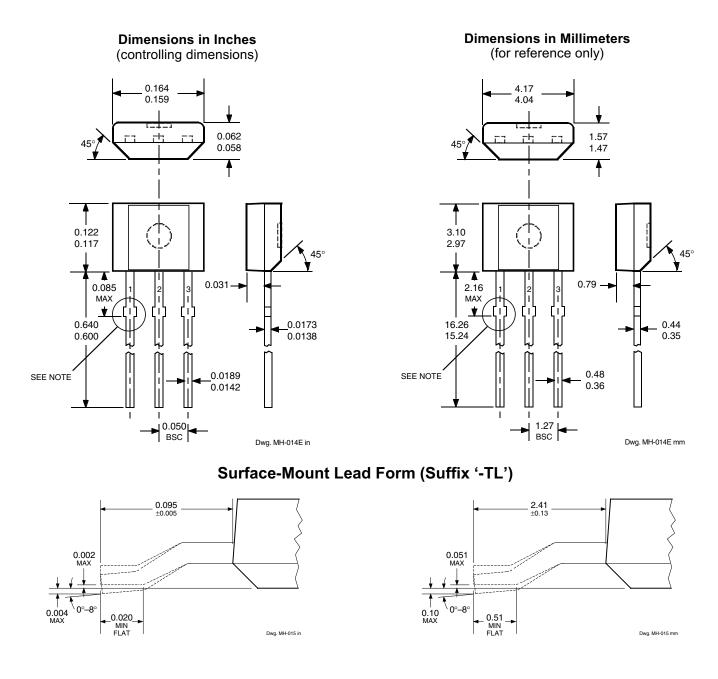


NOTE: Exact body and lead configuration at vendor's option within limits shown.



10

PACKAGE DESIGNATOR 'UA'



NOTES: 1. Tolerances on package height and width represent allowable mold offsets. Dimensions given are measured at the widest point (parting line).

- 2. Exact body and lead configuration at vendor's option within limits shown.
- 3. Height does not include mold gate flash.
- 4. Recommended minimum PWB hole diameter to clear transition area is 0.035" (0.89 mm).
- 5. Where no tolerance is specified, dimension is nominal.

> The products described herein are manufactured under one or more of the following U.S. patents: 5,045,920; 5,264,783; 5,442,283; 5,389,889; 5,581,179; 5,517,112; 5,619,137; 5,621,319; 5,650,719; 5,686,894; 5,694,038; 5,729,130; 5,917,320; and other patents pending.

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