Small Outline Optoisolators

Transistor Output

These devices consist of a gallium arsenide infrared emitting diode optically coupled to a monolithic silicon phototransistor detector, in a surface mountable, small outline, plastic package. They are ideally suited for high density applications, and eliminate the need for through-the-board mounting.

- Convenient Plastic SOIC-8 Surface Mountable Package Style
- Standard SOIC-8 Footprint, with 0.050" Lead Spacing
- Shipped in Tape and Reel, which Conforms to EIA Standard RS481A
- Compatible with Dual Wave, Vapor Phase and IR Reflow Soldering
- High Input-Output Isolation of 3000 Vac (rms) Guaranteed
- UL Recognized Tile #E54915

Ordering Information:

- To obtain MOC211, 212 and 213 in Tape and Reel, add R2 suffix to device numbers: R2 = 2500 units on 13" reel
- To obtain MOC211, 212 and 213 in quantities of 50 (shipped in sleeves) No Suffix

Marking Information:

- MOC211 = 211
- MOC212 = 212
- MOC213 = 213

Applications:

- General Purpose Switching Circuits
- Interfacing and coupling systems of different potentials and impedances
- Regulation Feedback Circuits
- Monitor and Detection Circuits

MAXIMUM RATINGS (T_A = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit		
INPUT LED					
Forward Current — Continuous	lF	60	mA		
Forward Current — Peak (PW = 100 μs, 120 pps)	I _F (pk)	1.0	Α		
Reverse Voltage	٧R	6.0	V		
LED Power Dissipation @ T _A = 25°C Derate above 25°C	PD	90 0.8	mW mW/°C		
OUTPUT TRANSISTOR					

Collector–Emitter Voltage	VCEO	30	V
Collector–Base Voltage	VCBO	70	V
Emitter–Collector Voltage	VECO	7.0	V
Collector Current — Continuous	IC	150	mA
Detector Power Dissipation @ T _A = 25°C Derate above 25°C	PD	150 1.76	mW mW/°C

Preferred devices are Motorola recommended choices for future use and best overall value.

MOC211 [CTR = 20% Min]

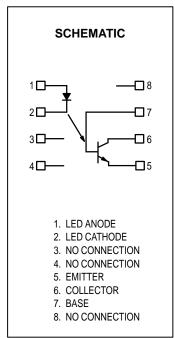
MOC212 [CTR = 50% Min]

[CTR = 100% Min]

Motorola Preferred Devices

SMALL OUTLINE OPTOISOLATORS TRANSISTOR OUTPUT







MOC211 MOC212 MOC213

MAXIMUM RATINGS — continued ($T_A = 25^{\circ}C$ unless otherwise noted)

Rating

TOTAL DEVICE					
Input–Output Isolation Voltage ^(1,2) (60 Hz, 1.0 sec. duration)		VISO	30	3000	
Total Device Power Dissipation @ T _A = 25°C Derate above 25°C Ambient Operating Temperature Range ⁽³⁾ Storage Temperature Range ⁽³⁾		PD		250 2.94 -45 to +100 -45 to +125	
		T _A	–45 to		
		T _{stg}	–45 to		
Lead Soldering Temperature (1/16" from case, 10 sec. duration)		_	26	260	
ELECTRICAL CHARACTERISTICS (T _A = 25°C unless other	wise noted) ⁽⁴⁾	•			
Characteristic	Symbol	Min	Typ ⁽⁴⁾	Max	Unit
INPUT LED					
Forward Voltage (I _F = 10 mA)	VF	_	1.15	1.5	V
Reverse Leakage Current (V _R = 6.0 V)	IR	_	0.1	100	μΑ
Capacitance	С	_	18	_	pF
OUTPUT TRANSISTOR					_
Collector–Emitter Dark Current (V _{CE} = 10 V, T _A = 25°C)	I _{CEO} 1	_	1.0	50	nA
$(V_{CE} = 10 \text{ V}, T_{A} = 100^{\circ}\text{C})$	I _{CEO} 2	_	1.0		μΑ
Collector–Emitter Breakdown Voltage (I _C = 100 μA)	V(BR)CEO	30	90		V
Emitter–Collector Breakdown Voltage (I _E = 100 μA)	V(BR)ECO	7.0	7.8		V
Collector–Emitter Capacitance (f = 1.0 MHz, V _{CE} = 0)	C _{CE}	_	7.0		pF
COUPLED					
Output Collector Current MOC211 (IF = 10 mA, V _{CE} = 10 V) MOC212 MOC213	2	2.0 (20) 5.0 (50) 10 (100)	6.5 (65) 9.0 (90) 14 (140)		mA (%)
Collector–Emitter Saturation Voltage (I _C = 2.0 mA, I _F = 10 mA)	V _{CE(sat)}	_	0.15	0.4	V
Turn–On Time ($I_C = 2.0 \text{ mA}$, $V_{CC} = 10 \text{ V}$, $R_L = 100 \Omega$)	ton	_	7.5		μs
Turn–Off Time ($I_C = 2.0 \text{ mA}$, $V_{CC} = 10 \text{ V}$, $R_L = 100 \Omega$)	t _{off}	_	5.7	_	μs
Rise Time (I _C = 2.0 mA, V_{CC} = 10 V, R_L = 100 Ω)	t _r	_	3.2	_	μs
Fall Time (I _C = 2.0 mA, V _{CC} = 10 V, R _L = 100 Ω)	t _f	_	4.7		μs
Input–Output Isolation Voltage (f = 60 Hz, t = 1.0 sec.)(1,2)	V _{ISO}	3000	_	_	Vac(rms)
Isolation Resistance (V _I _O = 500 V) ⁽²⁾	RISO	10 ¹¹	_	_	Ω
Isolation Capacitance $(V_{I-O} = 0, f = 1.0 \text{ MHz})^{(2)}$ C _{ISO}		_	0.2	_	pF

Symbol

Value

Unit

- 1. Input-Output Isolation Voltage, V_{ISO}, is an internal device dielectric breakdown rating.
- 2. For this test, pins 1 and 2 are common, and pins 5, 6 and 7 are common.
- 3. Refer to Quality and Reliability Section in Opto Data Book for information on test conditions.
- 4. Always design to the specified minimum/maximum electrical limits (where applicable).
- 5. Current Transfer Ratio (CTR) = $I_C/I_F \times 100\%$.

TYPICAL CHARACTERISTICS

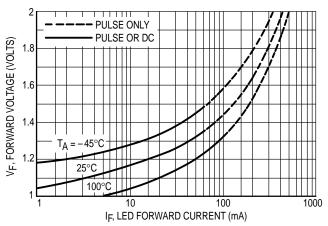


Figure 1. LED Forward Voltage versus Forward Current

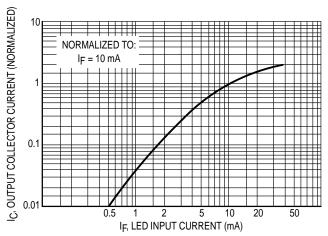


Figure 2. Output Current versus Input Current

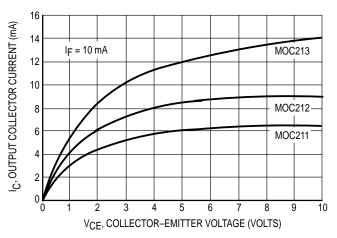


Figure 3. Output Current versus Collector–Emitter Voltage

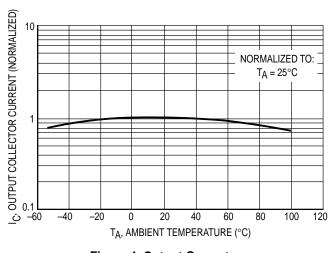


Figure 4. Output Current versus Ambient Temperature

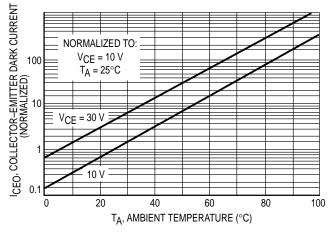


Figure 5. Dark Current versus Ambient Temperature

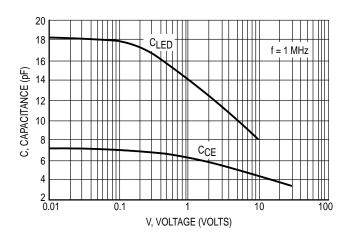
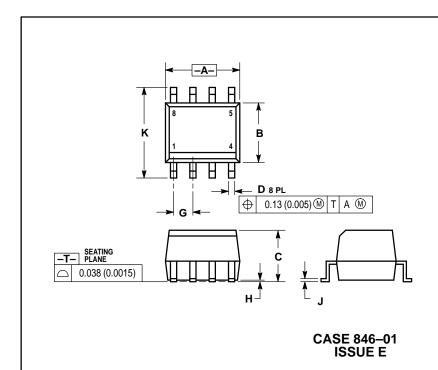


Figure 6. Capacitance versus Voltage

PACKAGE DIMENSIONS



NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.182	0.202	4.63	5.13	
В	0.144	0.164	3.66	4.16	
С	0.123	0.143	3.13	3.63	
D	0.011	0.021	0.28	0.53	
G	0.050 BSC		1.27 BSC		
Н	0.003	0.008	0.08	0.20	
J	0.006	0.010	0.16	0.25	
K	0.224	0.244	5.69	6 19	

STYLE 1: PIN 1. ANODE

2. CATHODE 3. NC

5. EMITTER 6. COLLECTOR

7. BASE

B. NC

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