

Features:

- Plastic package.
- For surface mounted applications.
- Low zener impedance.
- Low regulation factor.
- High temperature soldering guaranteed: 260 °C/10 seconds at terminals.
- Standard voltage tolerance is 10 %, suffix A \pm 5 %.

Mechanical Data:

Case	: JEDEC DO-214AC moulded plastic over passivated junction.
Terminals	: Solder plated, solderable per MIL-STD- 750, Method 2026.
Lead	: Pure tin plated lead free, solderable per MIL-STD-202, Method 2025.
Polarity	: Colour band denotes positive cathode end.
Mounting position	: Any.
Weight	: 0.002 ounces, 64mg.

Absolute Maximum Ratings ($T_{amb} = 25^{\circ}\text{C}$, unless otherwise specified)

Parameter	Test condition	Symbol	Value	Units
Power dissipation	$T_L = 75^{\circ}\text{C}$	P_{tot}	1.0	W

Maximum Thermal Resistance ($T_{amb} = 25^{\circ}\text{C}$, unless otherwise specified)

Parameter	Test condition	Symbol	Value	Units
Maximum junction temperature	-	T_j	150	°C
Storage temperature range	-	T_S	-65 to +150	

Electrical Characteristics

Device Marking Code	Nominal Zener Voltage	Test Current	Maximum Dynamic Impedance Resistance			Maximum DC Reverse Leakage Current		Maximum Surge Current	Part Number	
	$V_Z^{(1)}$ at I_{ZT} (V)	I_{ZT} (mA)	Z_{ZT} at I_{ZT} (Ω)	Z_{ZK} at I_{ZK} (Ω)	I_{ZK} (mA)	I_R (μ A)	V_R (V)	$I_{RM}^{(2)}$ (mApk)		
6P8	6.8	37	3.5	700	1	10	4	660	SML4736 SML4736A	
7P5	7.5	34	4		0.5		5	605	SML4737 SML4737A	
8P2	8.2	31	4.5				6	550	SML4738 SML4738A	
9P1	9.1	28	5				7	500	SML4739 SML4739A	
10	10		7				7.6	454	SML4740 SML4740A	
12	12	21	9				5	9.1	380	SML4742 SML4742A
13	13	19	10					9.9	344	SML4743 SML4743A
15	15	17	14					11.4	305	SML4744 SML4744A
16	16	15.5	16					12.2	285	SML4745 SML4745A
18	18	14	20					13.7	250	SML4746 SML4746A
20	20	12.5	22	15.2		225		SML4747 SML4747A		
22	22	11.5	23	16.7	205	SML4748 SML4748A				
24	24	10.5	25	18.0	190	SML4749 SML4749A				
27	27	9.5	35	20.6	170	SML4750 SML4750A				
30	30	8.5	40	1000	22.8	150		SML4751 SML4751A		

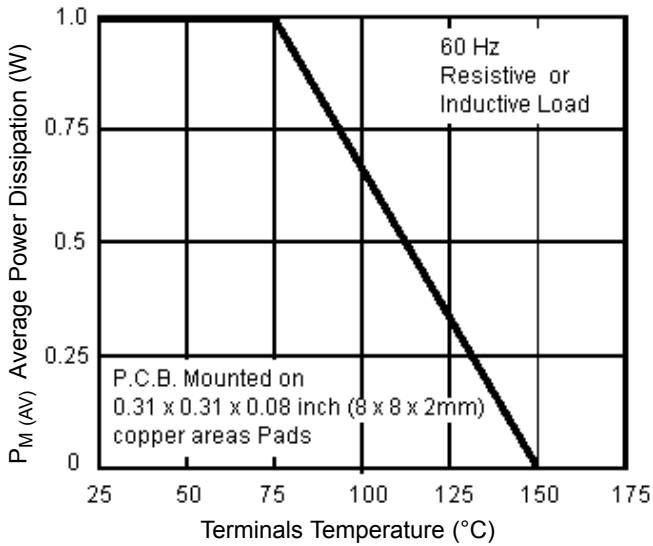
Device Marking Code	Nominal Zener Voltage	Test Current	Maximum Dynamic Impedance Resistance			Maximum DC Reverse Leakage Current		Maximum Surge Current	Part Number								
	$V_Z^{1)}$ at I_{ZT} (V)	I_{ZT} (mA)	Z_{ZT} at I_{ZT} (Ω)	Z_{ZK} at I_{ZK} (Ω)	I_{ZK} (mA)	I_R (μ A)	V_R (V)	$I_{RM}^{2)}$ (mApk)									
33	33	7.5	45	1000	0.25	5	25.1	135	SML4752								
									SML4752A								
36	36	7	50						SML4753								
				SML4753A													
39	39	6.5	60	SML4754													
				SML4754A													
43	43	6	70	1500					0.25	5	32.7	110	SML4755				
													SML4755A				
47	47	5.5	80										SML4756				
				SML4756A													
51	51	5	95	2000	0.25	5	38.8	90					SML4757				
													SML4757A				
56	56	4.5	110										SML4758				
				SML4758A													
68	68	3.7	150	2000									0.25	5	51.7	65	SML4760
																	SML4760A
75	75	3.3	175						SML4761								
				SML4761A													

1) Based on DC- Measurement at Thermal Equilibrium.

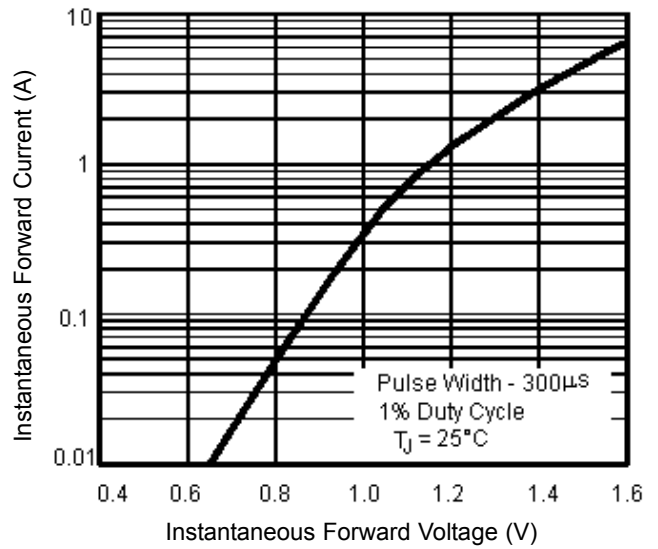
2) Surge Current is a Non-Repetitive, 8.3ms Pulse Width Square Wave or Equivalent Sine-Wave Superimposed on I_{ZT} per JEDEC Method.

Typical Characteristics ($T_{amb} = 25^{\circ}\text{C}$ unless otherwise specified)

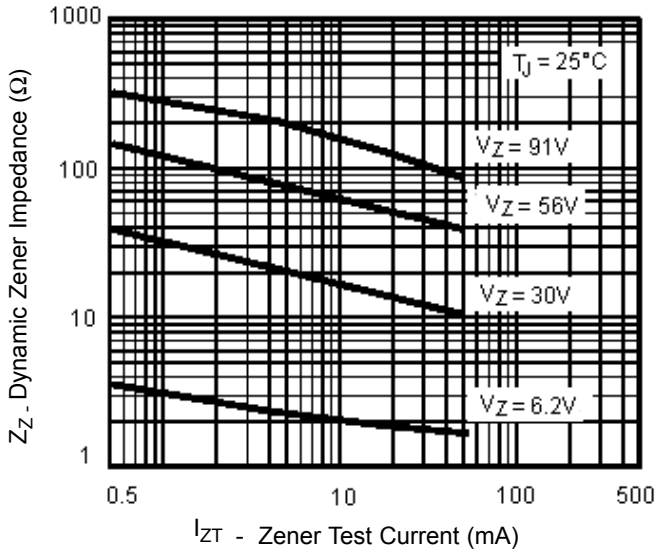
Maximum Continuous Power Dissipation



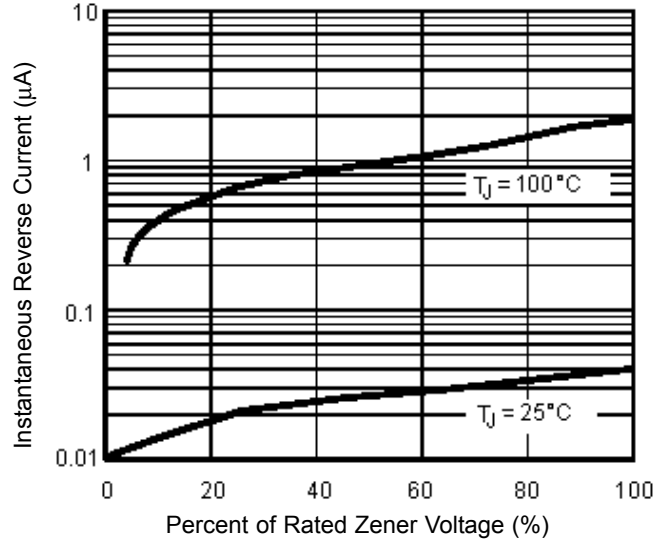
Typical Instantaneous Forward Characteristics for SML4763



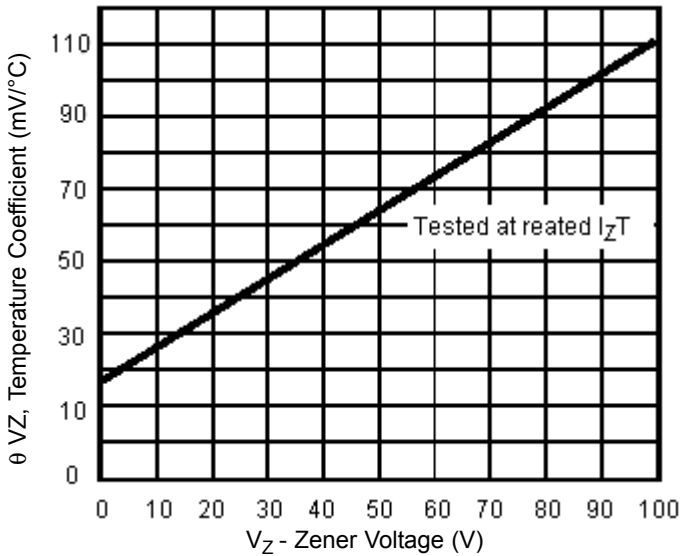
Typical Zener Impedance



Typical Reverse Characteristics



Typical Temperature Coefficients



Notes:

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