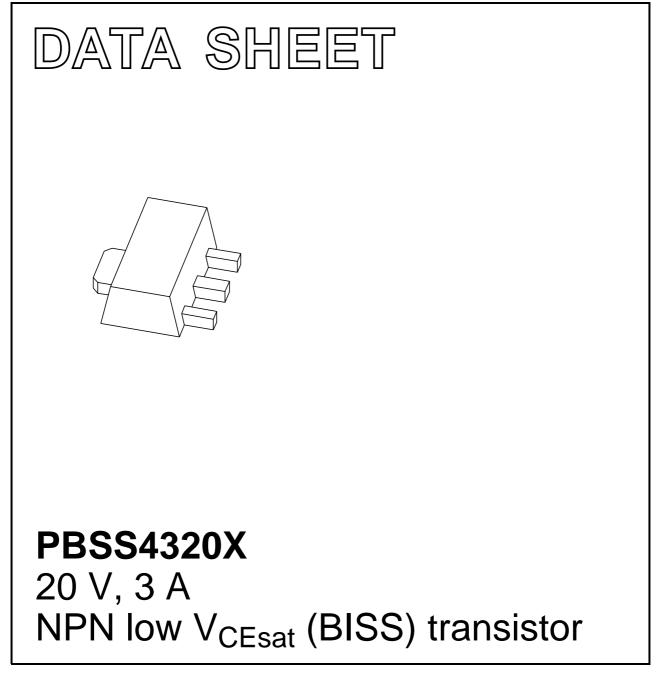
DISCRETE SEMICONDUCTORS



Product data sheet Supersedes data of 2003 Dec 15 2004 Nov 03



PBSS4320X

20 V, 3 A NPN low V_{CEsat} (BISS) transistor

FEATURES

- SOT89 (SC-62) package
- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability: I_C and I_{CM}
- Higher efficiency leading to less heat generation
- Reduced printed-circuit board requirements.

APPLICATIONS

- Power management
 - DC/DC converters
 - Supply line switching
 - Battery charger
 - LCD backlighting.
- Peripheral drivers
 - Driver in low supply voltage applications (e.g. lamps and LEDs).
 - Inductive load driver (e.g. relays, buzzers and motors).

DESCRIPTION

NPN low V_{CEsat} transistor in a SOT89 plastic package. PNP complement: PBSS5320X.

MARKING

TYPE NUMBER	MARKING CODE		
PBSS4320X	S44		

ORDERING INFORMATION

TYPE NUMBER	PACKAGE			
NAME		DESCRIPTION	VERSION	
PBSS4320X	SC-62 plastic surface mounted package; collector pad for get transfer; 3 leads		SOT89	

QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	UNIT
V _{CEO}	collector-emitter voltage 20		V
I _C	collector current (DC) 3		А
I _{CM}	peak collector current 5		А
R _{CEsat}	equivalent on-resistance 105 n		mΩ

PINNING

PIN	DESCRIPTION	
1	emitter	
2	collector	
3	base	

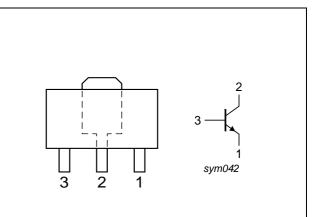


Fig.1 Simplified outline (SOT89) and symbol.

PBSS4320X

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	20	V
V _{CEO}	collector-emitter voltage	open base	_	20	V
V _{EBO}	emitter-base voltage	open collector	_	5	V
I _C	collector current (DC)	note 4	-	3	А
I _{CM}	peak collector current	limited by T _{j(max)}	-	5	А
I _B	base current (DC)		_	0.5	A
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$			
		note 1	_	550	mW
		note 2	_	1	W
		note 3	_	1.4	W
		note 4	_	1.6	W
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-65	+150	°C

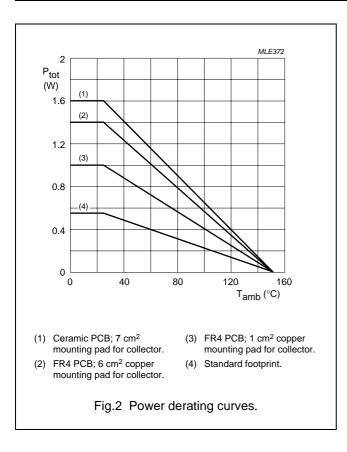
Notes

1. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; standard footprint.

2. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; mounting pad for collector 1 cm².

3. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; mounting pad for collector 6 cm².

4. Device mounted on a ceramic printed-circuit board 7 cm², single-sided copper, tin-plated.



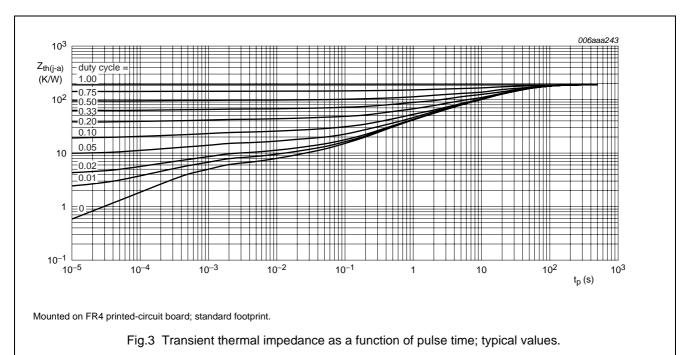
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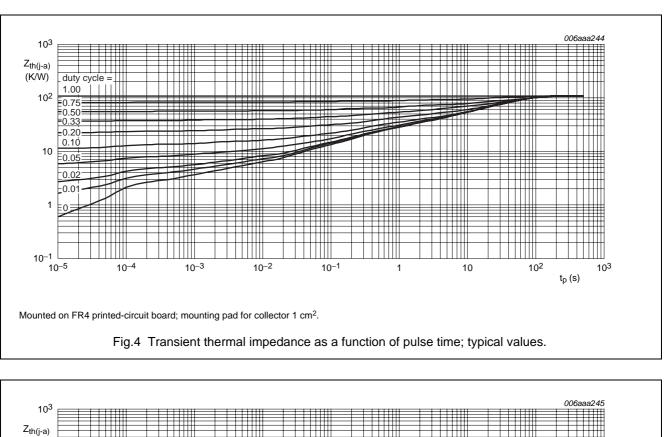
THERMAL CHARACTERISTICS

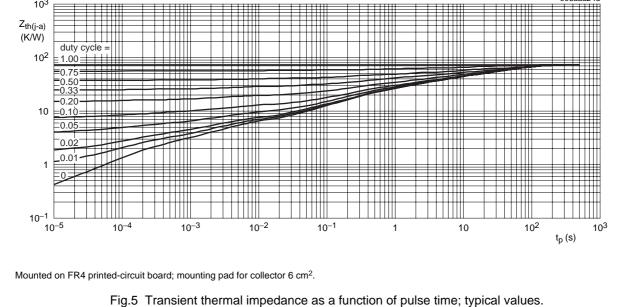
SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	in free air		
		note 1	225	K/W
		note 2	125	K/W
		note 3	90	K/W
		note 4	80	K/W
R _{th(j-s)}	thermal resistance from junction to soldering point		16	K/W

Notes

- 1. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; standard footprint.
- 2. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; mounting pad for collector 1 cm².
- 3. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; mounting pad for collector 6 cm².
- 4. Device mounted on a ceramic printed-circuit board 7 cm², single-sided copper, tin-plated.







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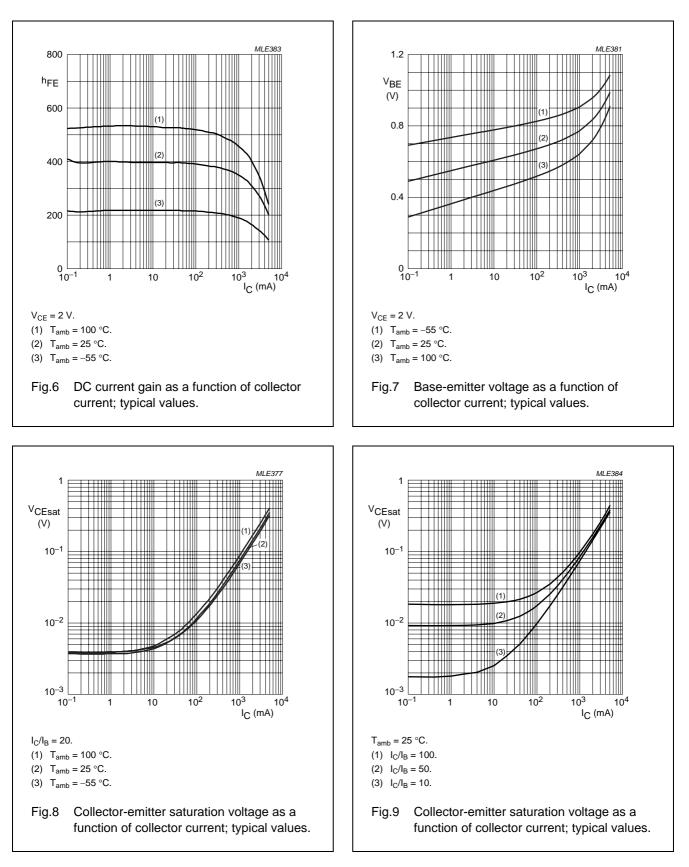
CHARACTERISTICS

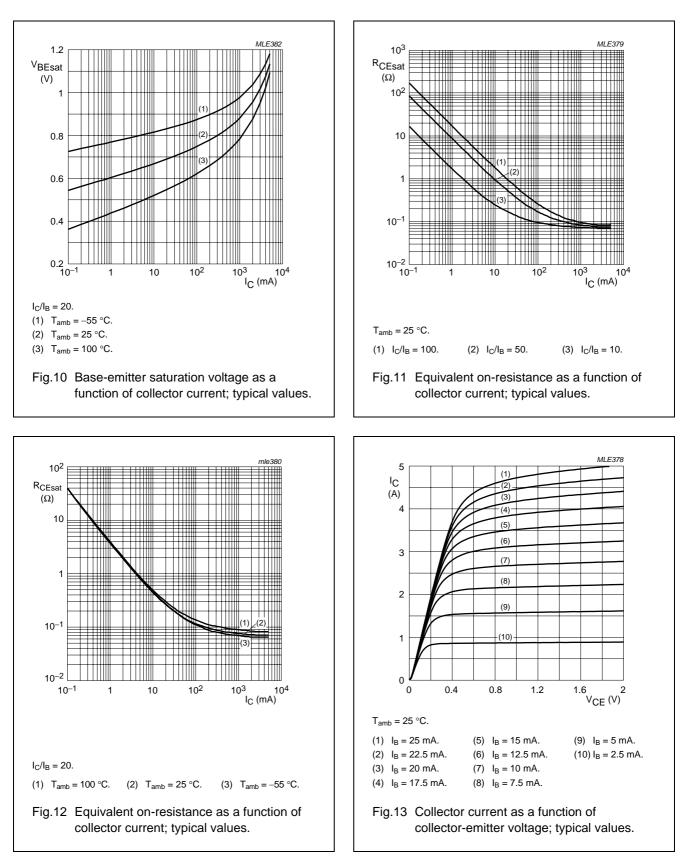
 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	$V_{CB} = 20 \text{ V}; \text{ I}_{E} = 0 \text{ A}$	-	_	100	nA
		$V_{CB} = 20 \text{ V}; \text{ I}_{E} = 0 \text{ A}; \text{ T}_{j} = 150 ^{\circ}\text{C}$	-	-	50	μA
I _{CES}	collector-emitter cut-off current	V _{CE} = 20 V; V _{BE} = 0 V	-	-	100	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A	-	-	100	nA
h _{FE}	DC current gain	$V_{CE} = 2 V$				
		I _C = 0.1 A	220	-	-	
		I _C = 0.5 A	220	-	-	
		I _C = 1 A; note 1	220	_	_	
		I _C = 2 A; note 1	200	_	_	
		I _C = 3 A; note 1	150	_	-	
V _{CEsat}	collector-emitter saturation	I _C = 0.5 A; I _B = 50 mA	_	_	70	mV
	voltage	I _C = 1 A; I _B = 50 mA	_	_	120	mV
		I _C = 2 A; I _B = 100 mA	_	_	240	mV
		I _C = 3 A; I _B = 300 mA; note 1	_	_	310	mV
R _{CEsat}	equivalent on-resistance	I _C = 3 A; I _B = 300 mA; note 1	_	85	105	mΩ
V _{BEsat}	base-emitter saturation voltage	I _C = 2 A; I _B = 100 mA	_	1.1	_	V
		I _C = 3 A; I _B = 300 mA; note 1	_	_	1.2	V
V _{BEon}	base-emitter turn-on voltage	V _{CE} = 2 V; I _C = 1 A	1.1	_	_	V
f _T	transition frequency	$I_{C} = 100 \text{ mA}; V_{CE} = 5 \text{ V}; f = 100 \text{ MHz}$	100	—	-	MHz
Cc	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = i_e = 0 \text{ A}; f = 1 \text{ MHz}$	-	_	35	pF

Note

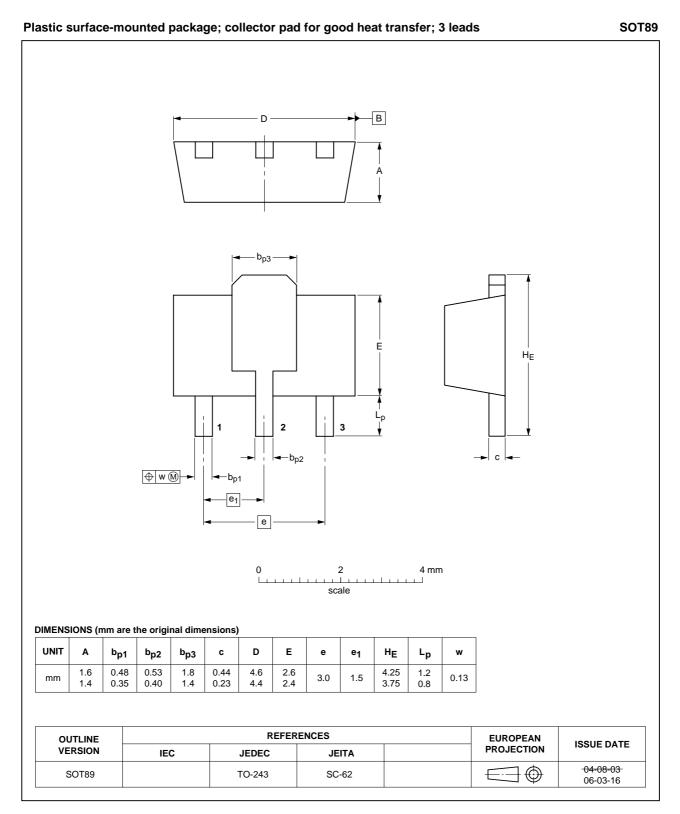
1. Pulse test: $t_p \leq 300~\mu\text{s};~\delta \leq 0.02.$





PBSS4320X

PACKAGE OUTLINE



PBSS4320X

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

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