

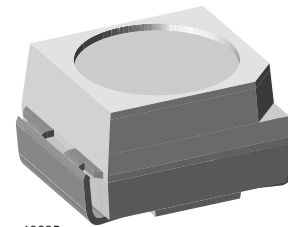
High Intensity SMD LED

Description

These new devices have been designed to meet the increasing demand for surface mounting technology. This device is used for outdoor or for low power applications.

The package of the TLMD310. is the PLCC-2 (equivalent to a size B tantalum capacitor).

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled up with clear epoxy.



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Features

- SMD LEDs with exceptional brightness
- Luminous intensity categorized
- Compatible with automatic placement equipment
- EIA and ICE standard package
- Compatible with infrared, vapor phase and wave solder processes according to CECC
- Available in 8 mm tape
- Low profile package
- Non-diffused lens: excellent for coupling to light pipes and backlighting
- Low power consumption
- Luminous intensity ratio in one packaging unit
 $I_{Vmax}/I_{Vmin} \leq 2.0$, optional ≤ 1.6
- Lead-free device

Applications

Automotive:
Backlighting in dashboards and switches

Telecommunication:
Indicator and backlighting in telephone and fax
Indicator and backlight for audio and video equipment
Indicator and backlight for battery driven equipment
Small indicator for outdoor applications
Indicator and backlight in office equipment
Flat backlight for LCDs, switches and symbols
General use

Parts Table

Part	Color, Luminous Intensity	Angle of Half Intensity ($\pm\phi$)	Technology
TLMD3100	Red, $I_V > 20$ mcd (typ.)	60 °	GaAlAs on GaAs
TLMD3101	Red, $I_V = (16 \text{ to } 50)$ mcd	60 °	GaAlAs on GaAs
TLMD3105	Red, $I_V = (10 \text{ to } 32)$ mcd	60 °	GaAlAs on GaAs

Absolute Maximum Ratings

$T_{amb} = 25$ °C, unless otherwise specified

TLMD310.

Parameter	Test condition	Symbol	Value	Unit
Reverse voltage		V_R	6	V
DC Forward current		I_F	30	mA
Surge forward current	$t_p \leq 10$ μ s	I_{FSM}	0.5	A
Power dissipation	$T_{amb} \leq 60$ °C	P_V	100	mW

Parameter	Test condition	Symbol	Value	Unit
Junction temperature		T_j	100	°C
Operating temperature range		T_{amb}	- 40 to + 100	°C
Storage temperature range		T_{stg}	- 55 to + 100	°C
Soldering temperature	$t \leq 5$ s	T_{sd}	260	°C
Thermal resistance junction/ambient	mounted on PC board (pad size > 16 mm ²)	R_{thJA}	400	K/W

Optical and Electrical Characteristics

$T_{amb} = 25$ °C, unless otherwise specified

Red

TLMD310.

Parameter	Test condition	Part	Symbol	Min	Typ.	Max	Unit
Luminous intensity ²⁾	$I_F = 10$ mA	TLMD3100	I_V	10	20		mcd
		TLMD3101	I_V	16		50	mcd
Luminous intensity ¹⁾	$I_F = 10$ mA	TLMD3105	I_V	10		32	mcd
Luminous intensity	$I_F = 1$ mA		I_V		2		mcd
Dominant wavelength	$I_F = 10$ mA		λ_d		648		nm
Peak wavelength	$I_F = 10$ mA		λ_p		650		nm
Angle of half intensity	$I_F = 10$ mA		ϕ		± 60		deg
Forward voltage	$I_F = 20$ mA		V_F		1.8	2.2	V
Reverse voltage	$I_R = 10$ μ A		V_R	6	15		V
Junction capacitance	$V_R = 0$, $f = 1$ MHz		C_j		15		pF

¹⁾ in one Packing Unit $I_{Vmax}/I_{Vmin} \leq 1.6$

²⁾ in one Packing Unit $I_{Vmax}/I_{Vmin} \leq 2.0$

Typical Characteristics ($T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)

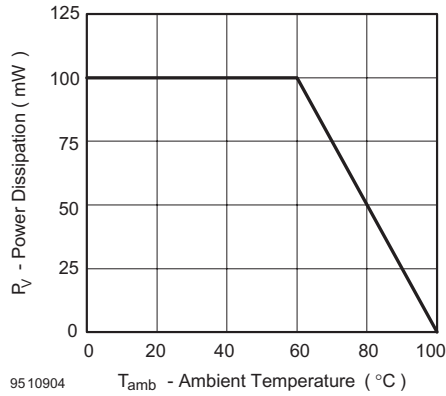


Figure 1. Power Dissipation vs. Ambient Temperature

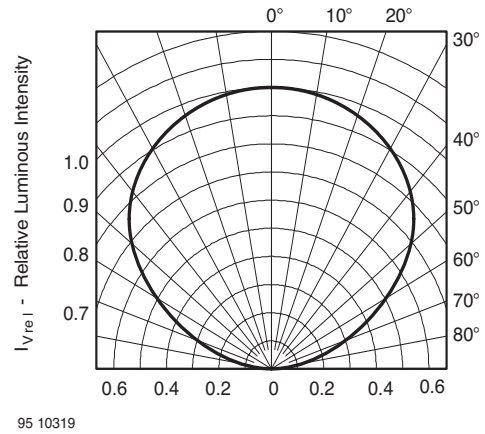


Figure 4. Rel. Luminous Intensity vs. Angular Displacement

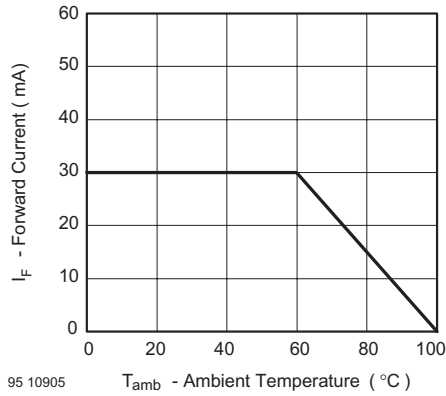


Figure 2. Forward Current vs. Ambient Temperature for InGaN

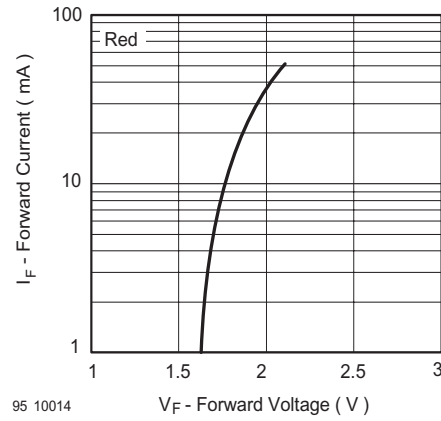


Figure 5.

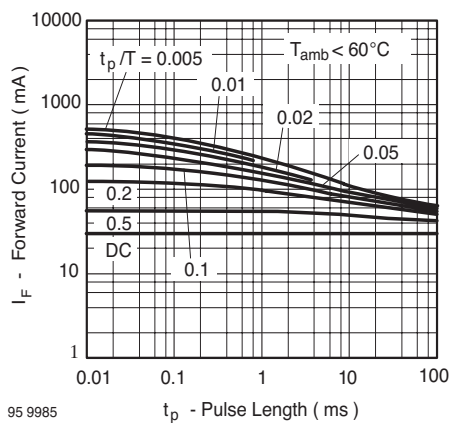


Figure 3. Pulse Forward Current vs. Pulse Duration

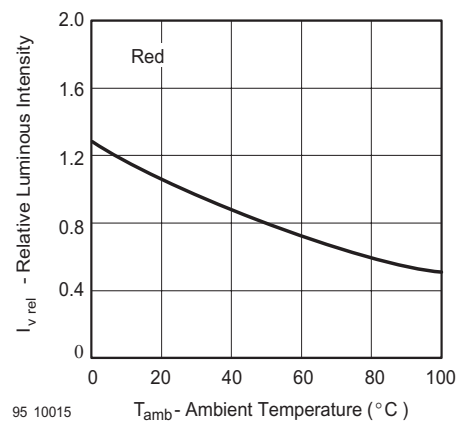


Figure 6. Rel. Luminous Intensity vs. Ambient Temperature

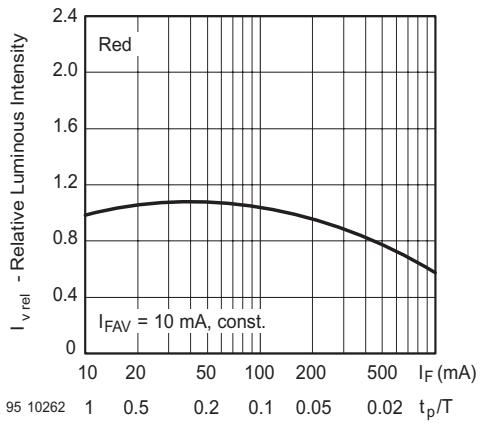


Figure 7. Rel. Lumin. Intensity vs. Forw. Current/Duty Cycle

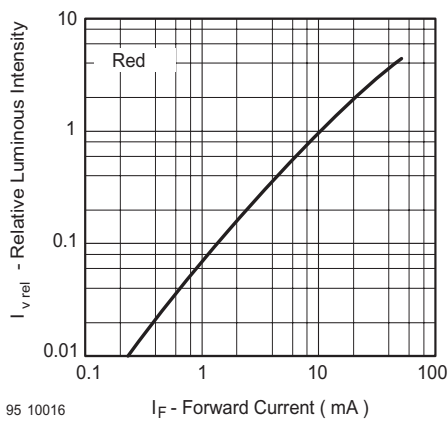


Figure 8. Relative Luminous Intensity vs. Forward Current

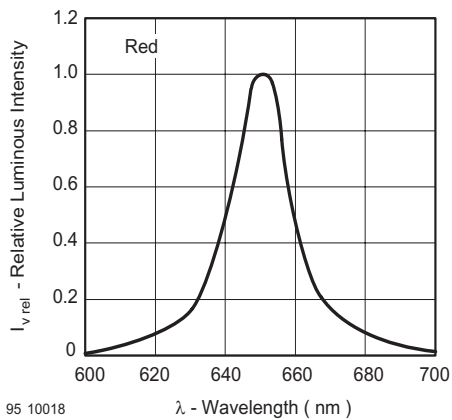
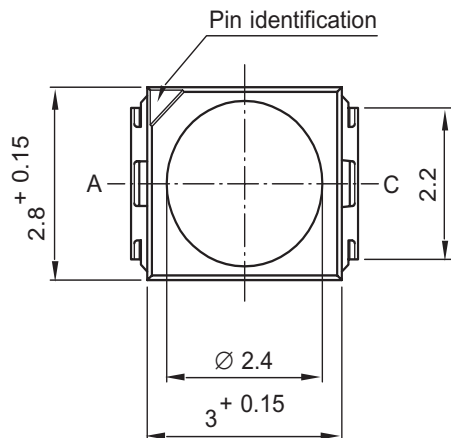
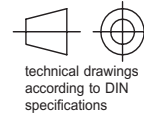
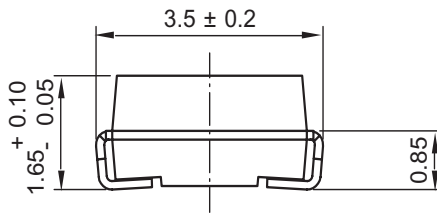
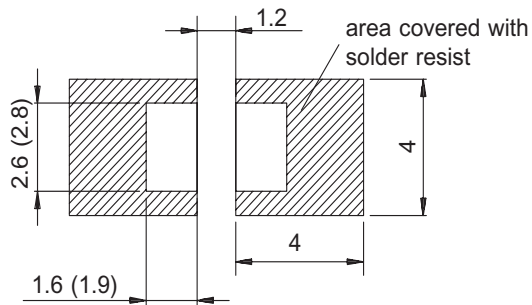


Figure 9. Relative Intensity vs. Wavelength

Package Dimensions in mm



Mounting Pad Layout



Dimensions: IR and Vaporphase
(Wave Soldering)

Drawing-No. : 6.541-5025.01-4
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Ozone Depleting Substances Policy Statement

It is the policy of **Vishay Semiconductor GmbH** to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

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Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany
Telephone: 49 (0)7131 67 2831, Fax number: 49 (0)7131 67 2423



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