## **FOX**Electronics



## Farnell Order Code: 1641030 FOX MODEL: FXO-HC536R-12.288 FOX Part Number: 767-12.288-8 HCMOS, CERAMIC, SMD, OSCILLATOR

## **Specifications**

Parameters			
Frequency	12.288 MHz		
Frequency Stability <sup>1</sup>	±25 PPM		
Temperature Range			
Operating (T <sub>OPR</sub> )	-40°C ~ +85°C		
Storage (T <sub>STG</sub> )	-55°C ~ +125⁰C		
Supply Voltage (V <sub>DD</sub> )	3.3V ± 5%		
Input Current (I <sub>DD</sub> )	32 mA Max		
Output Voltage (VoL)	10% V <sub>DD</sub> Max		
(V <sub>ОН</sub> )	90% V <sub>DD</sub> Min		
Output Symmetry (50% V <sub>p-p</sub> Level)	45% ~ 55%		
Rise Time (10% ~ 90% V <sub>p-p</sub> ) (TR)	3 nS Max		
Fall Time (90% ~ 10% V <sub>p-p</sub> ) (TF)	3 nS Max		
Output Load (HCMOS)	15 pF Max		
Startup Time (T <sub>s</sub> )	10 ms Max		
Output Enable / Disable Time <sup>2</sup>	100 nS Max		
Phase Jitter (12kHz ~ 20MHz)	< 1.1pS Typ.		
Maximum Soldering Temp / Time	260°C / 10 Seconds		
Moisture Sensitivity Level (MSL)	1		
Termination Finish	Au		

<sup>1</sup> Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration.

<sup>2</sup>An internal pullup resistor from pin 1 to pin 4 allows active output if pin 1 is left open.

ENABLE/DISABLE FUNCTION <sup>2</sup>				
Pin 1	OUTPUT (Pin 3)			
OPEN <sup>2</sup>	ACTIVE			
'1' Level VIH ≥ 70%V <sub>DD</sub>	ACTIVE			
'0' Level VIL $\leq 30\%V_{DD}$	High Z			

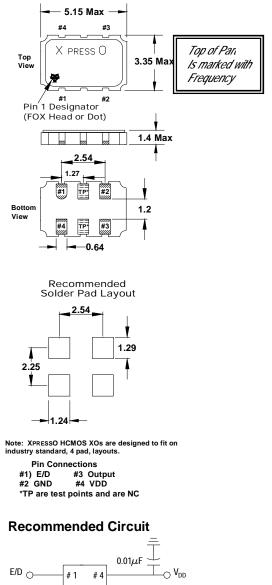
**NOTE**: A 0.01 $\mu$ F bypass capacitor should be placed between V<sub>DD</sub> (Pin4) and GND (Pin 2) to minimize power supply line noise. **RoHS Compliance Status: Compliant** 



Drawing is for reference to critical specifications defined by size measurements. Certain non-critical visual attributes, such as side castellations,

reference pin shape, etc. may vary





NC

GND

NC

#2 #3

OUT

HCMOS LOAD



Reference / Comments	Title / Description: Product Specifications				
	Drawing Number: DWG-4548-15		Rev: 1	Size: A	
	Part Number: 767-12.288-8			Cage: 61429	
	Draftsperson: AR Approved: BN		N	Date: 08/25/2008	