



# **Approval Sheet**

for

# **Anti-Explosion Fusible Resistors**

# **FAE** series

±5%

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Rev.	Description	Issue Date	Drawn	Approved
00	issue new spec.	May 27, 2009	Lynn Chen	Ken Hsu

Description Anti-Explosion Fusible Resistors			
Series	FAE	Rev.	00





#### 1. PRODUCT:

ANTI-EXPLOSION FUSIBLE RESISTORS (the resistor is coated with blue lacquer)

# 2. PART NUMBER:

Part number of the anti-explosion fusible resistor is identified by the name, power, tolerance, packing, temperature coefficient, special type and resistance value.

Example:

FAE	100	J	Т	-	52-	20R	
(1)	(2)	(3)	(4)	(5)	(6)	(7)	
Series	Power	Resistance	Packing	Temperature	Special	Resistance	
Name	Rating	Tolerance	Style	Coefficient	Type	Value	
				of Resistance			

(1) Style: FAE SERIES

(2) Power Rating: 100=1W

(3) Tolerance: J=±5%

(4) Packaging Type: R=Paper Taping Reel

T=Tape on Box Packing

B=Bulk Packing

(5) Temperature Coefficient : ±300ppm/°C

(6) Special Type: 52-=52.4mm

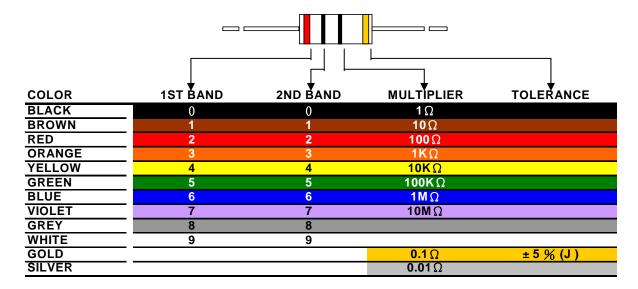
(7) Resistance Value : E24 Series

Example: 1R \ 10R \ 100R......





# 3. BAND-CODE:

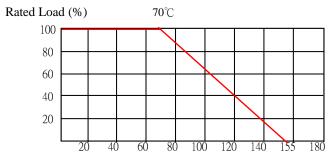


# 4. ELECTRICAL CHARACTERISTICS

STYLE	FAE100
Power Rating at 70 °C	1 W
Maximum Working Voltage	350V
Maximum Overload Voltage	600V
Dielectric Withstanding Voltage	500V
Operating Temp. Range	- 55 °C to + 155 °C
Pulse Load Capability	Up to 2 KV
Resistance Range	$0R1\Omega$ to $100R\Omega$
Temperature Coefficient	±300 ppm /°C

\* Below or over this resistance on request.

#### 5. DERATING CURVE

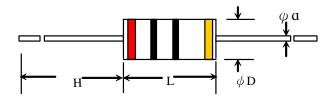


Ambient Temperature (°C)





#### 6. DIMENSIONS



STYLE	DIMENSION				
Normal	L	$\phid$			
FAE100	9.5±0.5	5.5±0.5	26±2.0	0.6±0.05	

## 7. ENVIRONMENTAL CHARACTERISTICS

#### (1) Fusing characteristics

Fusing Voltage =  $\sqrt{X * Power Rating \times Resistance Value}$ x = 16 or 25

 $R < 2.0\Omega$  Fusing time within 30 seconds at 25 times of rated power

 $R>2.2\Omega$  Fusing time within 30 seconds at 16 times of rated power

Fusing residual resistive value at least 100 times rated resistance

#### (2) Short Time Over Load Test

At 2.5 times of the rated voltage. (If the voltage exceeds the maximum load voltage, the maximum load voltage will be used as the rated voltage) applied for 5 seconds, the resistor should be free from defects after the resistor is released from load for about 30 minutes

Short Time Overload Voltage =  $2.5*\sqrt{\text{Power Rating} \times \text{Resistance Value}}$ 

The change of the resistance value should be within  $\pm 2.0 \% + 0.05 \Omega$ 

#### (3) Dielectric Withstanding Voltage

The resistor is placed on the metal V Block. Apply a Table I dielectric withstanding between the terminals connected together with the block for about 60 seconds.

The resistor shall be able to withstand without breakdown or flashover.

### (4) Temperature Coefficient Test

Test of resistors above room temperature  $100^{\circ}\text{C} \pm 2^{\circ}\text{C}$  (Testing Temperature  $115^{\circ}\text{C}$  to  $130^{\circ}\text{C}$ ) at the constant temperature silicon plate for over 5 minutes. Then measure the resistance value.

The Temperature Coefficient is calculated by the following equation and its value should be within the range of requested.

Resistor Temperature Coefficient = 
$$\frac{R - R_0}{R_0} \times \frac{1}{t - t_0} \times 10^6$$

**R** = Resistance value under the testing temperature

 $R_0$  = Resistance value at the room temperature

t = The testing temperature

**t<sub>o</sub>** = Room temperature





#### (5) Insulation Resistance

Apply test terminal on lead and resistor body. The test resistance should be high than 1,00M ohm.

#### (6) Solderability

Immerse the specimen into the solder pot at 260  $\pm$  5 °C for 5  $\pm$  0.5 seconds. At least 95% solder coverage on the termination.

#### (7) Resistance to Solvent

The specimen into the appropriate solvent of IPA condition of ultrasonic machine for 1 minute. The specimen is no deterioration of coatings and color code.

#### (8) Terminal Strength

Direct Load – Resistors shall be held by one terminal and the load shall be gradually applied in the direction of the longitudinal axis of the resistor unit the applied load reacheds 5 pounds  $^{\circ}$  The load shall be held for 10 seconds. The load of weight shall be  $\geq$  2.5 kg (24.5N).

#### (9) Pulse Overload

Apply 4 times of rated voltage to the specimen at the 1 second on and 25 seconds off cycle, subjected to voltage application cycles specified in 10,000 time  $^{\circ}$ 

The change of the resistance value shall be within  $\pm$  2.0% + 0.05  $\Omega$ 

#### (10)Load Life in Humidity

Place the specimen in a test chamber at 40  $\pm$  2 °C and 90 ~ 95 % relative humidity. Apply the rated voltage to the specimen at the 1.5 hours on and 0.5 hour off cycle. The total length of test is 1,000 hours The change of the resistance value shall be within  $\pm$  5 %  $\pm$  0.05  $\Omega$ 

#### (11)Load Life Test

Placed in the constant temperature chamber of  $70 \pm 3$  °C the resistor shall be connected to the lead wire at the point of 25mm. Length with each terminal, the resistors shall be arranged not much effected mutually by the temperature of the resistors and the excessive ventilation shall not be performed, for 90 minutes on and 30 minutes off under this condition the rated D.C. voltage is applied continuously for 1000+48/-0 hours then left at no-load for 1hour, measured at this time the resistance value  $\circ$ 

The change of the resistance value shall be within  $\pm$  5 % + 0.05  $\Omega$ .

There shall be no remarkable change in the appearance and the color code shall be legible after the test.

# (12)Temperature Cycling Test

The temperature cycle shown in the following table shall be repeated 5 times consecutively. The measurement of the resistance value is done before the first cycle and after ending the fifth cycle, leaving in the room temperature for about 1 hour  $^{\circ}$ 

#### **Temperature Cycling Conditions:**

Step	Temperature(°C)	Time (minute)
1	-55 ± 3	30
2	25 ± 3	2~3
3	155 ± 3	30
4	25 ± 3	2~3

The change of the resistance value shall be within  $\pm$  2.0 % + 0.05  $\Omega$ 

After the test the resistor shall be free from the electrical or mechanical damage.





#### (13)Resistance to Soldering Heat

The terminal lead shall be dipped into the solder pot at 350  $\pm$  10 °C for 3  $\pm$  0.5 seconds up to 2 ~ 2.5 mm. The change of the resistance value shall be within  $\pm$  1.0 % + 0.05  $\Omega$ 

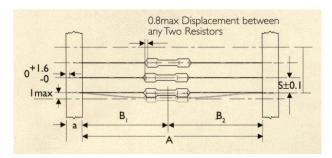
## (14)Overload Flame Retardant

At 4 times of the rated voltage ( If the voltage exceeds the maximum load voltage, the maximum load voltage will be used as the rated voltage ) applied for 1 minute.

Overload Test Voltage =  $4*\sqrt{Power\ Rating \times Resistance\ Value}$ The resistor shall show no evidence of flaming arcing.

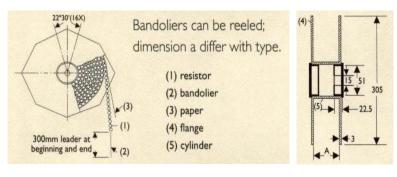
#### 8. PACKING METHODS

Bandolier for Axial leads



STYLE		DIMENI	ONS		Unit: : mm
Normal	Α	Α	B1-B2	S(spacing)	T (max. deviation of spacing)
FAE100	6 ± 0.5	52.4 ± 1.5	1.2	5	1 mm per 10 spacing 0.5 mm per 5 spacing

#### 9. TAPE ON REEL PACKING

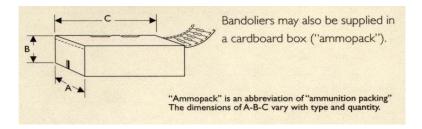


STYLE	TAPE ON REEL		
Normal	ACROSS FLANGE(A) Qty per reel		
FAE100	95	2,000	





#### 10. TAPE ON BOX PACKING



STYLE	Standard Lead Length			Short Lead Length			Qty per box
Normal	W ( A )	H(B)	L(C)	W(A)	H(B)	L(C)	
FAE100	103	78	260	81	91	260	1,000

#### 11. Plant Address

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