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ADSP-BF537 STAMP Board Support Package (BSP)

Product Description

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The BF537 STAMP Board Support Package (BSP) provides developers with a cost effective environment to develop embedded systems with the Blackfin Processor. The STAMP BSP has been specifically designed to support the development and porting of open source uClinux applications and includes the full complement of memory along with serial and network interfaces.

The BSP includes an ADSP-BF537 STAMP board and a CD with a recent copy of the Blackfin open source development tools and uClinux Kernel, documentation, board schematics, gerbers, and layout files. There are several Analog Devices daughter cards available for use with the STAMP board. Three separate SPI ADC daughter cards are available - AD7476, AD7476A, and AD7940 - each one providing an analog input interface. In addition, an AD1836 daughter card is available that provides an audio interface. Look out for additional daughter cards in the future with various other interfaces such as video and analog input/output.

For the most recent versions of tools and documentation, visit the uClinux for Blackfin web site. This web site is the central repository for all Blackfin open source projects. Several projects based on the STAMP board and daughter cards are posted here including: a networked audio media node, a networked oscilloscope, and a port of Linphone using the Speex voice codec.

The BF537 STAMP BSP will have you up and running with uClinux on the Blackfin right out of the box.

Features

- Linux OS based on uClinux 2.6.x, GPL license-compliant, complete kernel and driver source code
- GNU gcc (C/C++) support with gdb/kgdb debug capabilities over Ethernet and JTAG
- Das U-Boot bootloader
- Complete Linux IP stack including standard protocols
- Device drivers for on board peripherals

Technical Specs

- ADSP-BF537 500 MHz Blackfin® Processor
  - IEEE 802.3-Compliant 10/100 Ethernet MAC
  - Controller Area Network (CAN) 2.0B Interface
- 64 Mbytes SDRAM
- 4 Mbytes Flash Memory
- RS232 serial interface
- I/O connectors for Blackfin peripherals (SPI, two-wire interface, IrDA™, SPORT0 and SPORT1, Timers, PPI (general purpose parallel high speed interface, glueless TFT flat panel))
- JTAG interface for debug and FLASH programming
- On-Board Power

SUPPORTED TECHNOLOGIES

- Networking/Ethernet
- OS testing with Linux Test Project (LTP)

APPLICATIONS AREAS

- VoIP, Network Communications and control

AVAILABILITY

Now

Purchasing Information:

This Product is not available to purchase directly from Analog Devices. For pricing, availability and to purchase, contact your local Analog Devices distributor.