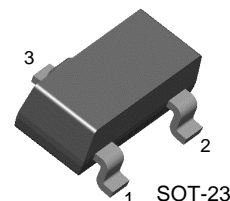


BCX70J

General Purpose Transistor



SOT-23
1. Base 2. Emitter 3. Collector

NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings $T_a=25^\circ\text{C}$ unless otherwise noted

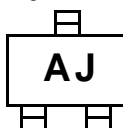
Symbol	Parameter	Value	Units
V_{CBO}	Collector-Base Voltage	45	V
V_{CEO}	Collector-Emitter Voltage	45	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current	200	mA
P_C	Collector Power Dissipation	350	mW
T_{STG}	Storage Temperature	-55 ~ 150	$^\circ\text{C}$

• Refer to KST3904 for graphs

Electrical Characteristics $T_a=25^\circ\text{C}$ unless otherwise noted

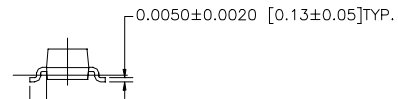
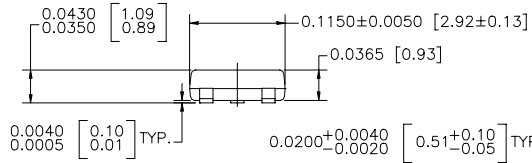
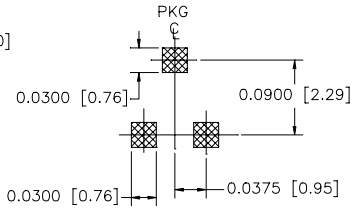
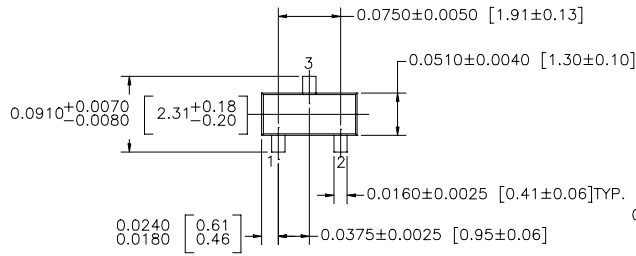
Symbol	Parameter	Test Condition	Min.	Max.	Units
BV_{CEO}	Collector-Emitter Breakdown Voltage	$I_C=2.0\text{mA}$, $I_B=0$	45		V
BV_{EBO}	Emitter-Base Breakdown Voltage	$I_E=1.0\mu\text{A}$, $I_C=0$	5		V
I_{CES}	Collector Cut-off Current	$V_{CE}=32\text{V}$, $V_{BE}=0$		20	nA
I_{EBO}	Emitter Cut-off Current	$V_{EB}=4\text{V}$, $I_C=0$		20	nA
h_{FE}	DC Current Gain	$V_{CE}=5\text{V}$, $I_C=10\mu\text{A}$ $V_{CE}=5\text{V}$, $I_C=2.0\text{mA}$ $V_{CE}=1\text{V}$, $I_C=50\text{mA}$	40 250 90	460	
$V_{CE}(\text{sat})$	Collector-Emitter Saturation Voltage	$I_C=10\text{mA}$, $I_B=0.25\text{mA}$ $I_C=50\text{mA}$, $I_B=1.25\text{mA}$		0.35 0.55	V
$V_{BE}(\text{sat})$	Base-Emitter Saturation Voltage	$I_C=10\text{mA}$, $I_B=0.25\text{mA}$ $I_C=50\text{mA}$, $I_B=1.25\text{mA}$	0.6 0.7	0.85 1.05	V
$V_{BE}(\text{on})$	Base-Emitter On Voltage	$I_C=2.0\text{mA}$, $V_{CE}=5\text{V}$	0.55	0.75	V
f_T	Current Gain Bandwidth Product	$I_C=10\text{mA}$, $V_{CE}=5\text{V}$, $f=100\text{MHz}$	125		MHz
C_{ob}	Output Capacitance	$V_{CB}=10\text{V}$, $I_E=0$, $f=1\text{MHz}$		4.5	pF
NF	Noise Figure	$V_{CE}=5\text{V}$, $I_C=0.2\text{mA}$ $R_S=2\text{K}\Omega$, $f=1\text{KHz}$		6	dB
t_{ON}	Turn On Time	$I_C=10\text{mA}$, $I_{B1}=1.0\text{mA}$		150	ns
t_{OFF}	Turn Off Time	$V_{BB}=3.6\text{V}$, $I_{B2}=1.0\text{mA}$ $R_1=R_2=5\text{K}\Omega$, $R_L=990\Omega$		800	ns

Marking



Package Dimensions

SOT-23



CONTROLLING DIMENSION IS INCH
VALUES IN [] ARE MILLIMETERS

SOT 23, 3 LEADS LOW PROFILE

NOTE : UNLESS OTHERWISE SPECIFIED

1. STANDARD LEAD FINISH 150 MICROINCHES / 3.81 MICROMETERS
MINIMUM TIN / LEAD (SOLDER) ON ALLOY 42
2. REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE G, DATED JUL 1993

Dimensions in Millimeters

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification

Product Status

Definition of Terms