

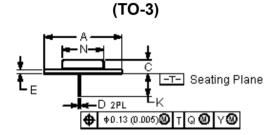


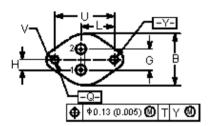
NPN silicon transistors.

Fast switching speeds and high current capacity ideally suit these parts for use in switching regulators, inverters, wide-band amplifiers and power oscillators in industrial and commercial applications.

Features:

- High speed t_f = 0.5μs (maximum).
- High current I_C (maximum) = 30 amperes.
- Low saturation V_{CE} (sat) = 2.5V (maximum) at I_{C} = 20 amperes.
- · Pb-free package.





Style 1: Pin 1. Base 2. Emitter Collector (Case)

Dimensions	Minimum Maximum		
А	1.550 (39.37) Reference		
В	-	1.050 (26.67)	
С	0.250 (6.35)	0.335 (8.51)	
D	0.038 (0.97)	0.043 (1.09)	
Е	0.055 (1.40)	0.070 (1.77)	
G	0.430 (10.92) BSC		
Н	0.215 (5.46) BSC		
К	0.440 (11.18)	0.480 (12.19)	
L	0.665 (16.89) BSC		
N	-	0.830 (21.08)	
Q	0.151 (3.84)	0.165 (4.19)	
U	1.187 (30.15) BSC		
V	0.131 (3.33)	0.188 (4.77)	
V		0.188 (4.77)	

Dimensions : Inches (Millimetres)

20 Ampere NPN Silicon Power Transistors 90 Volts - 140 Watts



(TO-3) Case 1-07 Style 1



Maximum Ratings (Note 1)

Rating	Symbol	Value	Unit	
Collector-Emitter Voltage	V _{CEO}	90	V dc	
Collector-Base Voltage	V _{CBO}	450		
Collector-Emitter Voltage	V _{CEV}	- 150		
Emitter-Base Voltage	V _{EBO}	7		
Collector Current-Continuous -Peak (Note 2)	I _C	20 30	A dc	
Base Current-Continuous	I _B	5		
Total Power Dissipation at T _C = 25°C Derate above 25°C	P _D	140 0.8	W W/°C	
Operating and Storage Junction Temperature Range	T _J , T _{Stg}	-65 to +200	°C	

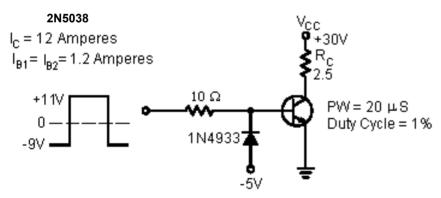
Thermal Characteristics

Characteristic	Symbol	Maximum	Unit
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	1.25	°C/W

Stresses exceeding maximum ratings may damage the device. Maximum ratings are stress ratings only. Functional operation above the recommended operating conditions is not implied. Extended exposure to stresses above the recommended operating conditions may affect device reliability.

- 1. Indicates JEDEC registered data.
- 2. Pulse test: pulse width ≤10ms, duty cycle ≤50%.

Switching Time Test Circuit





Electrical Characteristics (T_C = 25°C unless otherwise noted) (Note 3)

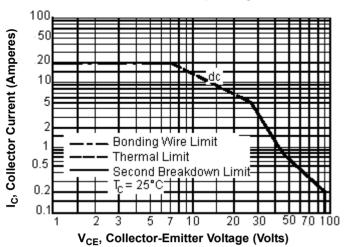
Characteristic	Symbol	Minimum	Maximum	Unit	
Off Characteristics					
Collector-Emitter Sustaining Voltage (Note 4) $(I_C = 200 \text{mA dc}, I_B = 0)$	V _{CEO (sus)}	90	-	V dc	
Collector Cut off Current $(V_{CE} = 140V \text{ dc}, V_{BE \text{ (off)}} = 1.5V)$ $(V_{CE} = 100V \text{ dc}, V_{BE \text{ (off)}} = 1.5V \text{ dc}, T_{C} = 150^{\circ}\text{C})$	I _{CEX}	-	50 10	mA dc	
Emitter Cut off Current $(V_{EB} = 5V \text{ dc}, I_C = 0)$ $(V_{EB} = 7V \text{ dc}, I_C = 0)$	I _{EBO}	-	5 50		
On Characteristics (Note 4)			1		
DC Current Gain (I _C = 12A dc, V _{CE} = 5V dc)	h _{FE}	20	100	-	
Collector-Emitter Saturation Voltage $(I_C = 20A \text{ dc}, I_B = 5A \text{ dc})$	V _{CE (sat)}	-	2.5	V. I.	
Base-Emitter Saturation Voltage $(I_C = 20A \text{ dc}, I_B = 5A \text{ dc})$	V _{BE (sat)}	-	3.3 V dc		
Dynamic Characteristics					
Magnitude of Common-Emitter Small-Signal Short-Circle Current Transfer Ratio (I_C = 2A dc, V_{CE} = 10V dc, f = 50		12	-	-	
Switching Characteristics					
Resistive Load					
Rise Time (V _{CC} = 30V dc)	t _r	-	0.5		
Storage Time $(I_C = 12A \text{ dc}, I_{B1} = I_{B2} = 1.2A \text{ dc})$	dc) t _s	-	1.5	μs	

^{3.} Indicates JEDEC Registered Data.

^{4.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.



Forward Bias Safe Operating Area



There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate I_{C} - V_{CE} limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

Second breakdown pulse limits are valid for duty cycles to 10%. At high case temperatures, thermal limitations may reduce the power that can be handled to values less than the limitations imposed by second breakdown.

Part Number Table

Description	Part Number
Transistor, NPN, TO-3	2N5038

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