





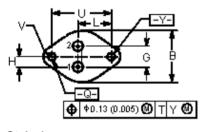
Silicon power transistors.

The MJ15023 powerBaseTM power transistors designed for high power audio, disk head positioners and other linear applications.

Features:

- High safe operating area (100% tested) 2A at 80V.
- High DC current gain = h_{FE} = 15 (minimum) at I_C = 8A dc.
- Pb-free packages.

(TO-3)



Style 1:

Pin 1. Base 2. Emitter

Collector (Case)

Dimensions	Minimum	Maximum	
A	1.550 (39.37) Reference		
В	-	1.050 (26.67)	
С	0.250 (6.35)	0.335 (8.51)	
D	0.038 (0.97)	0.043 (1.09)	
E	0.055 (1.40)	0.070 (1.77)	
G	0.430 (10.92) BSC		
Н	0.215 (5.46) BSC		
К	0.440 (11.18)	0.480 (12.19)	
L	0.665 (16.89) BSC		
Ν	-	0.830 (21.08)	
Q	0.151 (3.84) 0.165 (4.		
U	1.187 (30.15) BSC		
V	0.131 (3.33)	0.188 (4.77)	
Dimensions : Inches (Millimetres			

16 Amperes Silicon Power Transistors 200 - 250 Volts, 250 Watts



(TO-3) Case 1-07 Style 1

multicomp

http://www.farnell.com http://www.newark.com http://www.cpc.co.uk

Maximum Ratings

Rating	Symbol	Value	Unit	
Collector-Emitter Voltage MJ15023	V _{CEO}	200		
Collector-Base Voltage MJ15023	V _{CBO}	350		
Emitter-Base Voltage	V do V _{EBO} 5		v úc	
Collector-Emitter Voltage	V _{CEX}	400		
Collector Current-Continuous -Peak (Note 1)	Ic	c 16 30 A dc		
Base Current-Continuous	Ι _Β	5		
Total Power Dissipation at T _C = 25°C Derate above 25°C	P _D	250 1.43	W W/°C	
Operating and Storage Junction Temperature Range	T _{J,} T _{stg}	-65 to +200	°C	

Thermal Characteristics

Characteristics	Symbol	Maximum	Unit
Thermal Resistance, Junction-to-Case	$R_{ extsf{ heta}JC}$	0.70	°C/W

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. Pulse Test: Pulse Width = 5ms, Duty Cycle $\leq 10\%$.

Electrical Characteristics (T_c = 25°C unless otherwise noted)

Characteristic		Symbol	Minimum	Maximum	Unit
Off Characteristics		1	1		
Collector-Emitter Sustaining Voltage (Note 2 $(I_C = 100 \text{mA dc}, I_B = 0)$) MJ15023	V _{CEO (sus)}	200	-	_
Collector Cut off Current (V_{CE} = 200V dc, $V_{BE (off)}$ = 1.5V dc)	MJ15023	I _{CEX}	-	250	
Collector Cut off Current (V_{CE} = 150V dc, I_B = 0)	MJ15023	I _{CEO}	-		μA dc
Emitter Cut off Current (V_{CE} = 5V dc, I_B = 0)		I _{EBO}	-	500	
Second Breakdown			1		
Second Breakdown Collector Current with Base Forward Biased ($V_{CE} = 50V \text{ dc}, t = 0.5s$ (Non-repetitive) ($V_{CE} = 80V \text{ dc}, t = 0.5s$ (Non-repetitive)		I _{S/b}	5 2	-	A dc

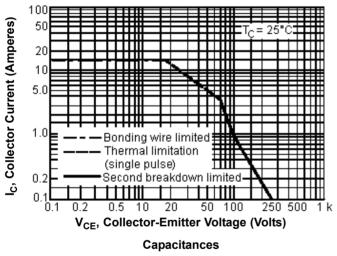


Electrical Characteristics (T_c = 25°C unless otherwise noted)

On Characteristic				
DC Current Gain ($I_C = 8A dc, V_{CE} = 4V dc$) ($I_C = 16A dc, V_{CE} = 4V dc$)	h _{FE}	15 5	60 -	-
Collector-Emitter Saturation Voltage ($I_C = 8A dc, I_B = 0.8A dc$) ($I_C = 16A dc, I_B = 3.2A dc$)	V _{CE (sat)}	-	1.4 4.0	V dc
Base-Emitter On Voltage (I _C = 8A dc, V _{CE} = 4V dc)	V _{BE (on)}	-	2.2	
Dynamic Characteristics				
Current-Gain Bandwidth Product (I _C = 1A dc, V _{CE} = 10V dc, f _{test} = 1MHz)	f _T	4	-	MHz
Output Capacitance (V _{CB} = 10V dc, I _E = 0, f _{test} = 1MHz)	C _{ob}	-	600	pF

1. Pulse Test: Pulse Width = 300μ s, Duty Cycle $\leq 2\%$.

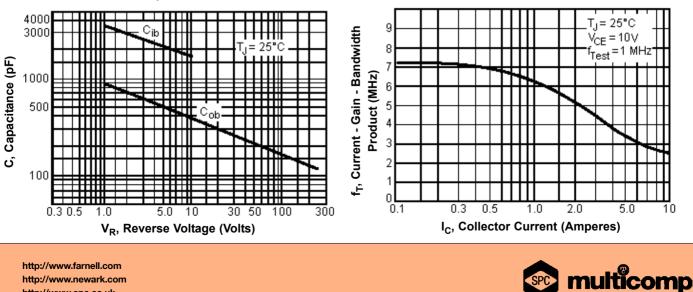
Active Region DC Safe Operating Area



http://www.cpc.co.uk

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data is based on $T_{J (pk)} = 200^{\circ}C$; T_C is variable depending on conditions. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.



Current - Gain - Bandwidth Product



200

100

50

20

10

5.0

2.0

0.2

h_{FE}, DC Current Gain



DC Current Gain

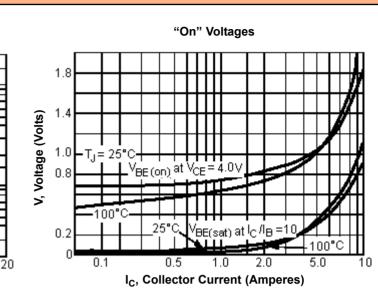
T₁=100°C

= 25

 $V_{CE} = 4.0 V$

10

5.0



Part Number Table

0.5

Description	Part Number		
Transistor, PNP, TO-3	MJ15023		

2.0

I_C, Collector Current (Amperes)

1.0

Disclaimer This data sheet and its contents (the "Information") belong to the Premier Famell Group (the "Group") or are licensed to it. No licence is granted for the use of it other than for information purposes in connection with the products to which it relates. No licence of any intellectual property rights is granted. The Information is subject to change without notice and replaces all data sheets previously supplied. The Information supplied is believed to be accurate but the Group assumes no responsibility for its accuracy or completeness, any error in or omission from it or for any use made of it. Users of this data sheets previously for its accuracy or completeness, any error in or omission from it or for any use made of it. Users of this data resulting from any reliance on the Information or use of it (including liability resulting from negligence or where the Group was aware of the possibility of such loss or damage arising) is excluded. This will not operate to limit or restrict the Group's liability for death or personal injury resulting from its negligence. SPC Multicomp is the registered trademark of the Group. © Premier Farnell plc 2008.

http://www.farnell.com http://www.newark.com http://www.cpc.co.uk

