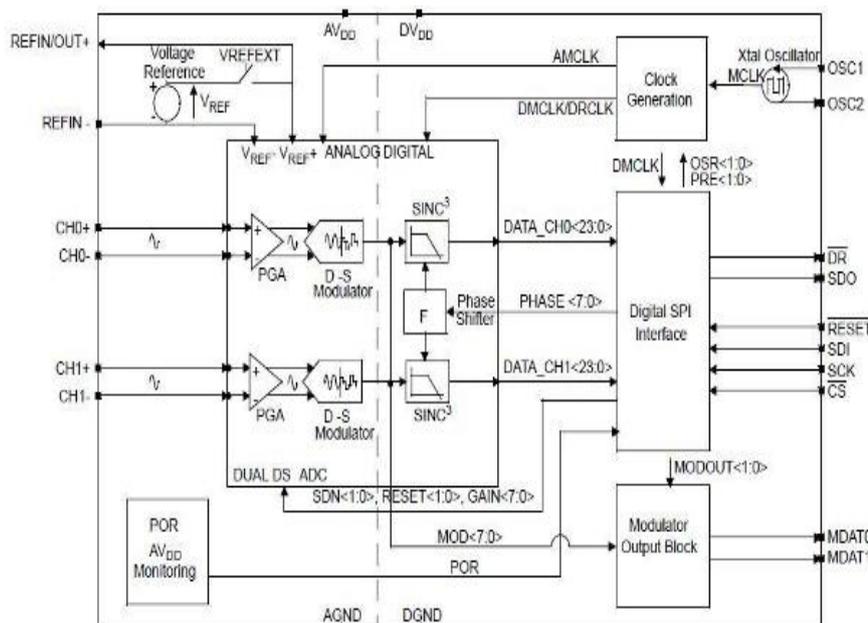




MCP3901 Two Channel Analog Front End

General Description:

The MCP3901 dual channel analog front end features high accuracy 16/24-bit delta-sigma A/D converters, internal programmable gain amplifiers, an internal voltage reference, and phase-delay compensation with up to 91 dB SINAD. Communication with the MCP3901 is done via the SPI interface enabling control of the PGAs, oversampling ratio for control of the output data rate (up to 64 ksp/s), resolution, dithering, shutdown and other communication features.



Key Features:

- Two simultaneously sampled 16/24-bit resolution delta-sigma A/D converters
- 91 dB SINAD, -104 dBc THD, 109 dB SFDR
- Programmable data rate up to 64 ksp/s
- Internal voltage reference with low drift of 12 ppm/°C
- Internal PGAs on each channel with gain of 32 V/V
- Phase delay compensation between channels with 1 μs resolution
- Low power consumption (14mW at 5V)

Applications:

- Energy Metering & Power Measurement
- Automotive
- Portable Instrumentation
- Medical and Power Monitoring

Related Products Information:

Mfr Part #	Farnell #	Newark #	Description
MCP3901A0-E/SS	1814889	73R8373	Two Channel Analog Front End, SSOP, 20pin
MCP3901A0T-E/SS	-	73R8374	Two Channel Analog Front End, SSOP, 20pin Tape on reel of 1