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SHARP

MOBILE LIQUID CRYSTAL DISPLAY GROUP SHARP CORPORATION SPECIFICATION

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APPLICABLE GROUP

MOBILE LIQUID CRYSTAL DISPLAY

GROUP

DEVICE SPECIFICATION FOR

TFT-LCD module

 $MODEL\ No.\ LQ064V3DG02$

CUSTOMER'S APPROVAL

DATE

BY

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SHARP CORPORATION

RECORDS OF REVISION

MODEL No: LQ064V3DG02

SPEC No : LA1-05005A

	NO.	PAGE	SUMMARY	NOTE
2004.1.21		-	-	1st Issue
2005.7.1	A	3	· Corresponding connector is revised. CF9A→DF9A,CF9B→DF9B,CF9C→DF9C	2nd Issue
			Vcc function is added.	
			Power supply in →Power supply in(Supply the same voltage to Pin No.28 and No.29)	
		4	· Operating temperature(Ambiemt) is deleted.	•
		10	· Contrast ratio is changed. Min 150→250 , TYP 250→350	
·		16	· Specification of the front shield case opening	. *
			area is added.	
				· =
				· · ·
				
		-		
				
		-		
				

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1 Applicable TFT-LCD module This specification applies to the color TFT-LCD module, LQ064V3DG02

2 Overview

This module is a color active matrix transmissive LCD module incorporating amorphous silicon TFT (Thin Film Transistor).

It is composed of a color TFT-LCD panel, driver ICs, control circuits and power supply circuitry and a backlight unit. Graphics and texts can be displayed on a 640 x RGB x 480 dots panel with 262,144 colors by feeding 18 bit data signal (6bit/each of R,G,B), 4(four) timing signals, +3.3V or +5V DC power supply for TFT-LCD and AC power supply for backlight.

(Note: Backlight-driving DC/AC inverter is not built in this module.)

- Fine images with stripe aligned 307,200 pixels on 6.4 inch diagonal screen
- · Color display capability of 262,144 colors with 18 bit data signal(6 bits for each RGB)
- · Adapting a wide viewing angle technology [best viewing angle: 12 o'clock direction]
- · High contrast, thanks to active matrix drive system
- · AG(Anti Glare) polarizing filter
- Light and slim compact module achieved by COG assemble technology (6 o'clock direction is also available by the function to flip the screen horizontally or vertically)
- · Natural coloring reproducibility by employing normally-white-mode, which has good nature in coloring
- · Image inversion both horizontally and vertically

3 Mechanical Specifications

Items	specifications	Unit
Display size (Diagonal)	16.32 (6.4")	Cm
Active display area	130.56 (H) x 97.92 (V)	Mm
Pixel format	640(H) x RGB x 480(V)	dot
	(1 pixel=R+G+B dots)	_
Pixel pitch	0.204 (H) x 0.204(V)	mm
Pixel configuration	R,G,B vertical stripe	+A
LCD mode	Normally white/Twisted Nematic	_
Dimension *	161.3 (W) x 117 (H) x 12(D)	min
Mass	280 (max)	g

^{*.} Protrusion such as backlight harness and positioning boss are not included.

Fig.1 shows dimensions of the module.

4 Input Signal Assignment

.1 TFT-LCD Panel driving section

1 31
2 30
CN1 pin arrangement from module surface
(Transparent view)

Employed connector: DF9BA31P-1V(32) (HIROSE ELECTRIC)
Corresponding connector: DF9 -31S-1V(32), DF9A-31S-1V(22)
,DF9B-31S-1V(32), DF9C-31S-1V(22) (HIROSE ELECTRIC)

Pin No.	Symbol	Function	Polarity
1	GND	_	
2	CK	Clock signal for sampling each datum	
3	Hsync	Horizontal Sync signal	Negative
4 .	Vsync	Vertical Sync signal	Negative
5 .	GND		,
6	R0	RED Data signal (LSB)	
7	R1	RED Data signal	
8	R2	RED Data signal	
9	R3	RED Data signal	
10	R4	RED Data signal	
11	R5	RED Data signal (MSB)	
12	GND		
13	G0	GREEN data signal (LSB)	
14	G1	GREEN data signal	
15	G2	GREEN data signal	
16	G3	GREEN data signal	
17	G4	GREEN data signal	
18	G5	GREEN data signal (MSB)	
19	GND		
20	B0	BLUE data signal (LSB)	
21	B1	BLUE data signal	
22	B2	BLUE data signal	
23	B3	BLUE data signal	
24	B4	BLUE data signal	
25	B5	BLUE data signal (MSB)	
26	GND		
27	ENAB	Data Enable signal	[Note 1]
		(Control signal for image location in horizontal direction)	
28	Vcc	Power supply in	
29		(Please supply the same voltage to Pin28 and Pin29.)	
30	R/L	Horizontal scanning direction control signal	[Note 2]
		(Rightwards/Leftwards)	
31	U/D	Vertical scanning direction control signal	[Note 2]
	. 1	(Upwards/downwards)	

The back shield case is internally grounded to GND of the module.

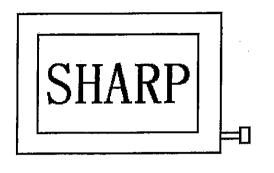
The front shield case is not certainly grounded to GND of the module.

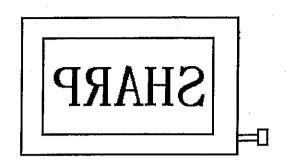
Please apply a same voltage to Pin28 and pin29.

[Note 1]

The horizontal display location is designated and controlled by rising timing of ENAB signal.

However if ENAB signal is fixed to "Low", display location is designated by the default setting in the module. (Don't use the module by fixing ENAB to "High")See: Chapter 7-2

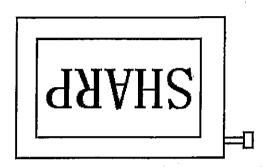




(R/L=High, U/D=High)

(R/L=Low, U/D=High)





(R/L=High, U/D=Low)

(R/L=Low, U/D=Low)

4.2. Backlight section

Employed connector: BHR-04VS-1 (JST)

CN₂

Adapted connector: SM04 (4.0) B-BHS-1 (JST)

Pin no.	Symbol	Function
1	V_{HIGH}	Power supply for lamp (High voltage side)
2	V_{HIGH}	Power supply for lamp (High voltage side)
3	NC	This is electrically opened.
. 4	V _{LOW}	Power supply for lamp (Low voltage side)

XThe input voltage wave forms to terminal \bigcirc and terminal \bigcirc should be in a same phase.

It has the possibility to discharge abnormally between the terminals in case of input in a reversed phase.

5 Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Input voltage	$v_{\rm I}$	Ta=25°C	-0.3 ~ +6	V	[Note 1]
Supply voltage	Vcc	Ta=25°C	0~+6	V	-
Storage temperature	Tstg		- 30 ~ + 80	°C	[Note 2]
Operating temperature (Panel surface)	Торр	-	-10 ~ +70	°C	

[Note 1] CK, R0 ~ R5, G0 ~ G5, B0 ~ B5, Hsync, Vsync, ENAB, R/L and U/D

[Note 2] Humidity: Less than 95%RH at Ta \leq 40°C and

Maximum wet-bulb temperature must not exceed 39°C at Ta>40°C,

with no condensation.

6 Electrical characteristics

6.1 TFT-LCD Panel driving section

Ta=25℃

Parameter	Symbol	Min	Тур	Max	Unit	Remarks
Supply voltage	Vcc	+3.0	+3.3	+3.6	V	[Note1]
	Vcc	+4.5	+5.0	+5.5	V	
,	Icc	-	290	580	mA	[Note2]
Current dissipation						Vcc=3.3V
	Icc	-	200	400	mA	[Note2]
				l		Vcc=5.0V
Allowed input ripple voltage	V _{RF}	-	-	100	mV p-p	Vcc=5V/3.3V
Input voltage ("Low" state)	V _{IL}	- 0.3	-	0.9	V	[Note3]
Input voltage ("High" state)	V _{IH}	2.3	-	5.5	V	
	I _{OL1}	-	-	1.0	μА	Vcc=3.3V V _J =0V
Input leakage current (low)	[Note4]	-	-	1.0	μА	Vcc=5.0V V _I =0V
	I _{OL2}		-	6.0	μΑ	Vcc=3.3V V _I =0V
	[Note5]	-	-	7.0	μА	Vcc=5.0V V ₁ =0V
	I _{OL3}	-		120	μА	Vcc=3.3V V ₁ =0V
•	[Note6]	*	-	180	μА	Vcc=5.0V V _I =0V
	I _{OL4}	-	-	6.0	μА	Vcc=3.3V V _I =0V
	[Note7]			6.0	μА	Vcc=5.0V V ₁ =0V
	I _{OH1}	-	-	1.0	μА	Vcc=3.3V V _I =3.3V
Input leakage current (High)	[Note4]			1.0	μA	Vcc=5.0V V _j =5.0V
	I _{OH2}	-	-	6.0	μΑ	Vcc=3.3V V _j =3.3V
•	[Note5]			7.0	μА	Vcc=5.0V V ₁ =5.0V
	$ I_{OH3} $			40	μА	Vcc=3.3V V ₁ =3.3V
	[Note6]			80	μА	Vcc=5.0V V ₁ =5.0V
	I _{OH3}			6.0	μА	Vcc=3.3V V ₁ =3.3V
	[Note7]			6.0	μА	Vcc=5.0V V ₁ =5.0V

[Note1] Vcc turn-on/off conditions

 $0 < t1 \le 15 ms$

 $0 < t2 \le 20 \text{ms}$

 $0 < t3 \le 1s$

1s < t4

Vcc-dip conditions

- 1) At $2.7V \le Vcc < 3.0V$ td $\le 10ms$
- 2) At Vcc < 2.7V

Vcc dip conditions should also follow the Vcc turn-on/off conditions

[Note2] Current dissipation (Typ.):

When Monochrome 16 level-gray-bar pattern is displayed (Figure below, Refer Page 10 for details of RGB)

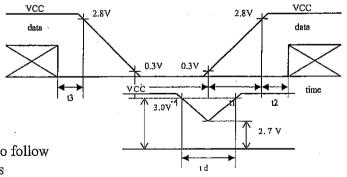
Current dissipation (Max.):

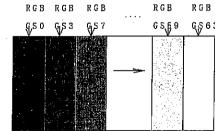
When GS3 and GS10 gray bar are displayed alternately.

[Note3] CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L and U/D

.[Note4] R0~R5, G0~G5, B0~B5, Hsync, and Vsync

[Note5] ENAB [Note6] R/L and U/D [Note7] CK





6.2 Backlight driving Section

Ta=25°C

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remarks
Lamp voltage	VL	330	370	410	V rms	IL=5×2mA rms
Lamp current	IL	9.0	10.0	11.0	mA rms	5mArms×2[Note6-1]
Lamp frequency	fL	35	-	80	KHz	Recommend
	Vs	_	-	660	V rms	Ta=-10°C [Note6-2]
Kick-off voltage		_	-	530		Ta=25°C [Note6-2]
[Note6-1]		-	-	960		Ta=-10°C [Note6-3]
		_	-	770		Ta=25°C [Note6-3]
Lamp life time	LL		50,000	_	Hour	[Note6-4]

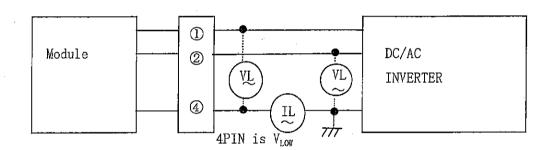
(Inverter: HARISON TOSHIBA LIGHTING CORPORATION Type HIU-359A-S2(18pF) is used at drive frequency f=50kHz)

Caution: Use Inverter providing symmetrical sine-wave in positive/negative polarity with no spike.

[Note6-1] Connect Module's metal shield case to GND of inverter circuit.

There are two lamps in back light. A piece of lamp current is 5mArms.

Lamp current is measured with current meter for high frequency as shown below.



[Note6-2] The voltage at the output of inverter circuitry.

The open output voltage of the inverter shall be maintained for more than 1sec; otherwise the lamp may not be turned on

The input voltage wave forms to terminal ① and terminal ② should be in a same phase. It has the possibility to discharge abnormally between the terminals in case of input of a reversed phase.

For the sake of the safety, please so design the inveter as to prevent abnormal discharge when one of the two lamps is broken or reaches the end of life.

[Note6-3] The voltage at the output of transformer built in inverter circuitry.

[Note6-4] The data for lamp is for your reference, because lamp is consumable component.

- a) Lamp life time is defined by either 1 or 2 below. (Continuous turning on at Ta=25°C, II=5×2mA rms)
 - ① When a brightness of lamp surface became 50% of the initial value under the standard condition.
 - ②When a kick-off voltage in Ta=-10°C exceeded maximum value (960 Vrms) at the output of inverter circuitry
 - b) In case of operating under lower temp, environment, the lamp exhaustion is accelerated and the brightness becomes lower.

(Continuous operating for around 1 month under lower temp. condition may reduce the brightness to half of the original brightness.)

In case of such usage under lower temp. environment, periodical lampexchange is recommended

7 Timing Characteristics of Input Signals Timing diagrams of input signal are shown in Fig.2.

7.1 Timing Characteristics

	Parameter	Symbol	Min.	Тур.	Max.	Unit
Clock	Clock Frequency		_	25.2	27.8	MHz
	"High" time	Tch	18	<u>-</u>	-	ns
	"Low" time	Tcl	18	-	-	ns
Data	Setup time	Tds	5	-	1	ns
	Hold time	Tdh	5	-	-	ns
Hsync	Period	TH	30.00	31.78		μs
		ļ	770	800	900	clock
	Pulse width	THp	2	96	200	clock
Vsync	Period	TV	515	525	560	line
	Pulse width	TVp	1	-	34	line
Horizont	al display period	THd	640	640	640	clock
Phase di	ference between	THc	(Tc/2)-10	_	(Tc/2)+10	ns
Hsync ar	id clock					
Phase dit	ference between	TVh	-2	- '	2	clock
Hsync an	id Vsync					
Vertical	display period	TVd	480	480	480	line

Note) In case of lower frequency, the deterioration of display quality, flicker etc., may occur.

7.2 Display Position in horizontal direction

Display position in horizontal direction is designated by rising timing of ENAB signal.

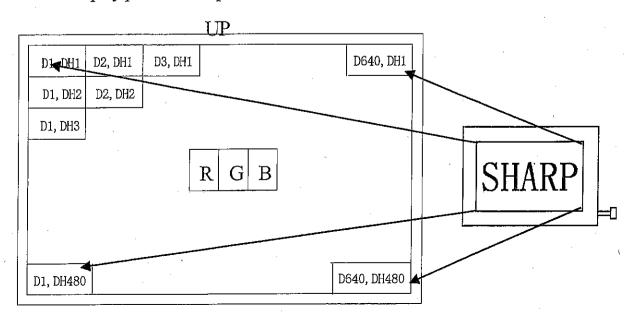
propie fortgere	IOII III IIOIIIIOIII	.,, 0001011 15	440.8	9			
Para	ameter	Symbol	MIN	TYP	MAX	Unit	Note
ENAB signal	Setup time	Tes	7	-	(Tc/2) -5	ns	
_	Pulse width	Tep	2	640	640	clock	
	between Hsync and B signal	THe	44	<u>-</u>	TH-664	clock	,

When ENAB is fixed to "Low", the horizontal display will starts from the clock C100 (clock) as shown in Fig.2. When the phase difference is not greater than 100 clock, hold ENAB terminal in "High" state for more than (100-THe), otherwise display will start from C100 (clock).

7.3 Display position in vertical direction Display start position in vertical direction is fixed to the 34th line. The ENAB signal does not relate to Vertical display position.

7.4 Input signal and display on the screen

Display position of input data



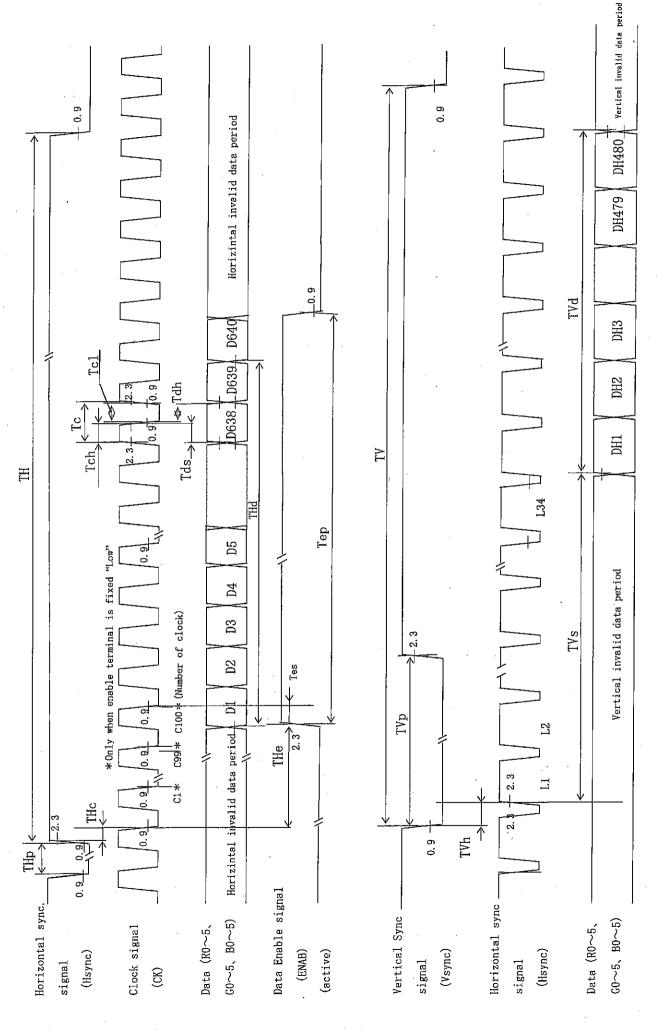


Fig 2. Input signal waveforms

8 Input Signals, Basic Display Colors and Gray Scale of Each Color

٠	Colors &	Data signal																		
		Gray	RO	R1	R2	R3	R4	R5	G0	GI	G2	G3.	G4	G5	В0	B1	В2	В3	B4	B5
	Gray scale	Scale	RU	K1	R2	ĸs	K4	KS	Gu	GI	G2	U3.	U4	GS	טמ	ы	DZ	Б3	D4	ъ
	Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	-	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Green	-	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
임	Cyan		0	,0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
C	Red		1	1.	1	1	1	1	0	0	0	0_	0	0	0	0	0	0	0	0
Basic Color	Magenta	-	1	1.	1	1	1	1	0	0	0	0	0	0	1.	1	1	1	1	1
^m	Yellow		1	1	1	1	1	1	1	1	1	1	1	1	Ô	0	0	0	0	0
	White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
l g	Û	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
fR	Darker	GS2	0	1	.0	0	0	0	0	0 -	0	0	0	0	0	0	0	0	0	0
Gray Scale of Red	Û	\downarrow	Ψ							1	/					\downarrow				
cal	Û	$\overline{}$					Ψ				Ψ									
15 S	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Ü	Û	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	.0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
u _e	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
j. Zig	Û	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
of (Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
ale	仓	₩			1	•		Ī			1						\downarrow			
Sc	Û	$\overline{}$			1	<u> </u>					1						<u> </u>			
Gray Scale of Green	Brighter	GS61	0	0	0	0	0	0	1	0	1	1 .	1	1	0	0	0	0	0	0
Ū	û	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0 -	0	0	0	0	0
ne	Black	GS0	. 0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
[B]	仓	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0_	0	0	0	0
e 0.	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
cal	Û	$\overline{\psi}$			1					•	1						\downarrow			
Gray Scale of Blue	Û	4			4									_			<u> </u>			_
Gra	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
	Ŷ	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

^{0:} Low level voltage, 1: High level voltage

Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

9 Optical Specification

Ta=25°C, Vcc=+3.3V

Parameter		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Viewing angle	Horizontal	θ 21, θ 22	CR≥5	60	70	-	° (Deg.)	[Note9-1,4]
Range	Vertical	θ 11		55	70	-,	° (Deg.)	
		θ 12		35	40	•	° (Deg.)	
Contrast	ratio	CR max	Best viewing	250	350	-		[Note9-2]
			angle					
Response time	Rise	Tr	θ = 0°	1	8	20	ms	[Note9-3]
	Fall	Td			21	40	ms	
Chromaticity	Chromaticity of white			0.263	0.313	0.363		[Note9-4]
		У	IL=5mArms×2	0.279	0.329	0.379	-	
Lumina	nce	Y		240	325	-	cd/m^2	

Lamp test in the module is made with the inverter type HIU-359A-S2(18pF) at 50kHz. The optical specifications are measured 30 minute after turning lamp on and in a dark room or equivalent condition, according to the method shown in Fig.9-1, 2 below.

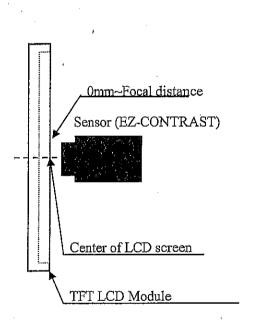


Fig.9-1 Measuring setup for

Viewing angle and Contrast

(BM-5 is used for contrast.)

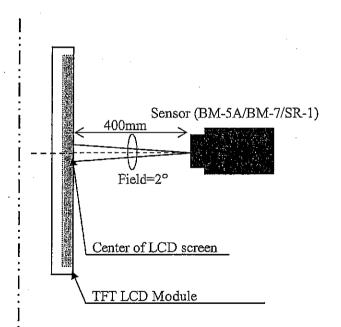
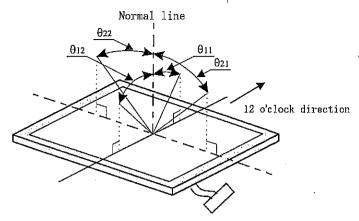


Fig.9-2 Measuring setup for
Luminance, Chromaticity
and Response time

(BM-7 is used for Luminance, SR-1 is for response)

[Note9-1] Definitions of viewing angle range:



[Note9-2] Definition of contrast ratio:

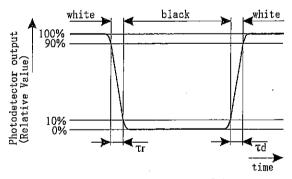
The contrast ratio is defined as the following.

The best viewing angle of this module is slightly leaned to 12 o'clock from normal line. In the field where θ_{11} exceeds this angle, gray-scale is reversed partially.

The gray-scale in the field of 6 o'clock direction is brighter than that of 12 o'clock direction and isn't reversed.

[Note9-3] Definition of response time:

The response time is defined as the following figure and shall be measured by switching the input signal between "black" and "white" alternatively.



[Note9-4] This parameter should be measured at the center of the screen and 30 minutes after turn-on.

Inverter frequency: 50 kHz

The characteristics are measured when the driver circuit is not powered.

10 Display Qualities

Please refer to the Outgoing Inspection Standard.

11 Handling Instruction

11.1 Assembling the module

- 1) In this module, the contact of the front shield case and the back is not always contact electrically each other.
 - The front shield case and the back touches at the point "A" hole perfectly, when this module is mounted to the unit of customer.
 - Therefore, please fix the mounting hole in the same plane in the case of the module installation, so that stress such as bow and crook may not be added to the module.
 - •To ensure the electric contact of the front shield case and the back, please fasten the mounting hole "A" of figure.1 surely, when you mount this module to the customer's set.
- 2) Connection/disconnection of the module's input connector shall be made while power is off.
- 3) Make sure to connect module's metal shield case to GND of inverter circuit.

 If its connection is not made perfectly, some of the following problems may occur.
 - a) Increase of noise caused by backlight
 - b) Unstable output from inverter circuit
 - c) Possibly over-heat in some section

11.2 Instruction for assembling

Since the front polarizer is easily damaged, pay attention to avoid rubbing with something hard or sharp. A protective film "Laminator" is attached on the module screen to avoid scratch and/or dust.

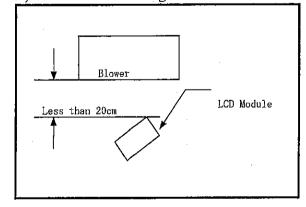
You are recommended remove the "Laminator" just before you embed the module into your products, paying attention to avoid electrostatic damage as followings.

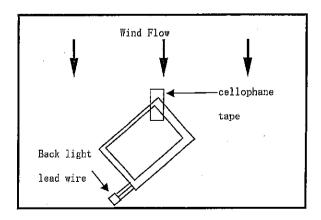
A) Work Environment

Since removing laminator may causes electrostatic charge that tends to attract dust, the following work environment would be desired.

- a) Floor: Conductive treatment having 1MΩ resistance onto floor's tile
- b) The room free from dust coming from outdoor environment, and put an adhesive mat at entrances
- c) Humidity from 50% to 70% and temperature from 15°C to 27°C are desirable.
- d) Worker should ware conductive shoes, conductive fatigue, conductive glove and earth wrist band.

B)Instruction for working





a) Wind direction of an electrostatic discharging blower should slightly downward to properly blow the module. The distance between the blower and the module should be less than 20cm. Also, pay attention to the direction of the module. [See the above]

- b) To prevent polarizer from scratching, adhesive tape (cellophane tape) should be stuck at the part of laminator sheet, which is closed to blower. [See the above]
- c) Pull slowly adhesive tape to peel the laminator off, with spending more than 5 second.
- d) The module without laminator should be moved to the next process to prevent dust.
- e) How to eliminate dust on polarizer.

Blow dust away by N₂ blower having measures of electrostatics

Since the front polarizer is easily damaged, wiping dust off is not adequate.

If the polarizer is soiled, it is suggested to peel dust off by using adhesive surface of adhesive tape.

When the metal section [shield case or shield back case] is soiled, wipe it off with dry and soft cloth. If not so easy, breathe upon it then wipe off. If the water droplet or fat is left for long term, it may cause stain or tarnish, it should be wipe off immediately.

Since TFT-LCD panel is made of glass substrate, dropping the panel or banging it against hard objects may cause cracking or fragmentation.

Since CMOS LSIs are incorporated in this module, please pay special care to electrostatic while handling it, and earth human body.

11.3 Caution in product design

The notes and cautions below should be followed when product is designed with this module.

The module should be protected with cover to prevent salt content and/or water droplet.

Take enough shielding countermeasure not to interfere to peripheral electronic device.

11.4 Others

The LCD has the nature that its performance is degradation by ultra-violet light. Don't leave the LCD module in direct sunlight or strong ultra violet ray.

If stored at the temperatures lower than the rated storage temperature, the LC may freeze and it may cause LCD panel damage. If storage temperature exceeds the specified rating, the molecular orientation of the LC may change to that of a liquid, and they may not revert to their original state. Store the module in normal room temperature.

The inductive loss caused by routing of lamp lead wire, which is closed to conductive section, may require the kick-off voltage greater than specified kick-off voltage.

The liquid crystal may leak out when the LCD is broken. If the liquid crystal drip into the eyes or mouth washes it out immediately.

The caution to other ordinal electronic component should be followed also.

12 Packing Form

- 12.1 Fig.3 shows packaging form.
- 12.2 Carton stock conditions
 - a) Maximum number of Carton being stuck: 8
 - b) Maximum number of product contained:

20 Unit

c) Carton size:

495mm (W) x 329mm (H) x 218mm (D)

d) Total mass (for 20Unit):

Approximately 7kg

e) Carton stock environment:

1) Temperature:

 $0 \sim 40$ °C

2) Humidity:

Up to 60%RH

3) Ambiance:

No gases bite into electronic components and wiring materials

4) Period:

Approximately 3month

5) Unpacking:

To prevent LCD module from damaging by ESD,

unpack the module with effective measure after controlling

humidity 50%RH or more.

13 Marking of product name

13.1 Serial No. indication

Serial No. is indicated by labeling. The location is given in Fig.1 Outline dimension.

Indicated contents:

LQ064V3DG02

XXXXXXXX

Model name

Serial No.

Serial No. contents 1st digit:

last digit of produced year (ex. 2005 \rightarrow "5")

2nd digit:

Produced month 1, 2, $3 \sim 9$, X, Y, Z

3rd ~ 8th digit:

Sequential number 000001 ~

out 1

Interoffice control code

9th digit:

14 Miscellaneous

- a) Variable resister has been set optimally before shipment, so do not change any adjusted value. If adjusted value is changed, the specification may be not satisfied.
- b) Don't disassemble this module, it may cause malfunction.
- c) Image retention may occur when the fixed image is display for long time.

15 Reliability Test Items

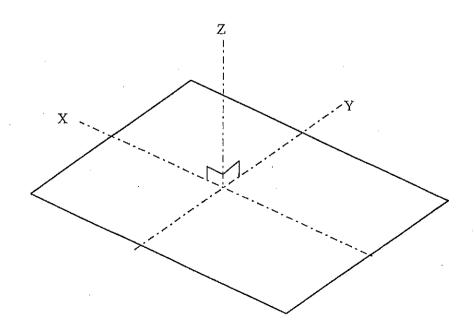
Note: The condition of temperature is the operation temperature given in Absolute maximum rating.

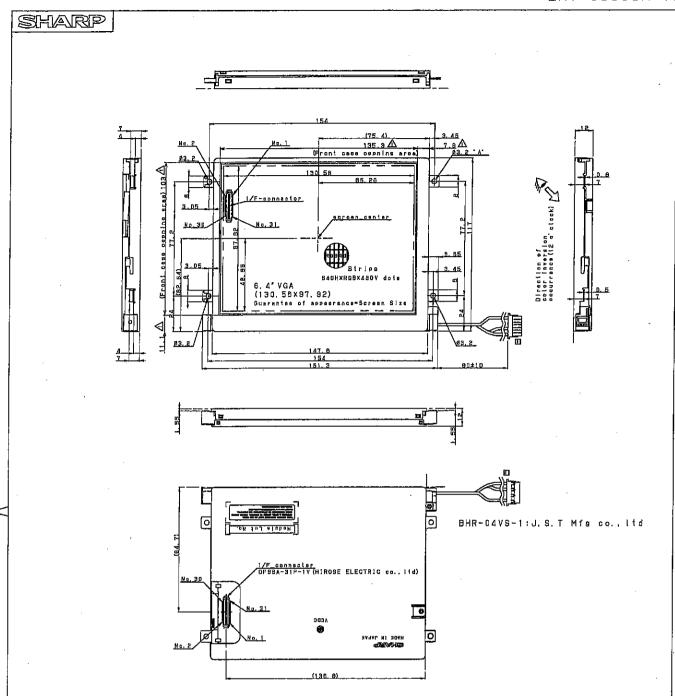
No.	Test parameter	Conditions		
1	High temperature storage test	Leaves the module at Ta=80°C for 240h		
2	Low temperature storage test	Leaves the module at Ta=-30°C for 240h		
3	High temperature	Operates the module at Ta=40°C; 90~95%RH for 240h		
	& high humidity operation test	(No condensation)		
4	High temperature operation test	Operates the module with +70°C at panel surface for 240h		
5	Low temperature operation test	Operates the module at Ta=-10°C for 240h		
6	Strength against ESD	±200V · 200pF [0 Ω] one time for each terminal		
7	Shock test	Max. acceleration: 490m/s ²		
	(non-operating)	Pulse width: 11ms, half sine wave		
		Direction: $\pm X, \pm Y, \pm Z$		
		once for each direction.		
8	Vibration test	Frequency: 10~57Hz/Vibration width (one side):0.075mm		
	(non-operating)	: 58~500Hz/Gravity:9.8m/s ²		
	(Sweep time: 11 minutes		
		Test period: 3 hours		
		(1 hour for each direction of X,Y,Z)		
9	Thermal shock test	-30°C ~ +80°C /50 cycle		
		[0.5h] [0.5h]		

[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

[Note] The directions of X, Y, Z are defined as below:





- ·General tolerance is ± 0.5 .
- ·Take care in set design to hidethe scratches and bubbles appeared on the polarizer or other frame area which is located outside of active area.
- •To ensure the electric contact of the front shield case and the back, please fasten the mounting hole A of figure.1 surely, when you mount this module to the customer's set.

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FIG 1 DIMENSIONS OF THE MODULE

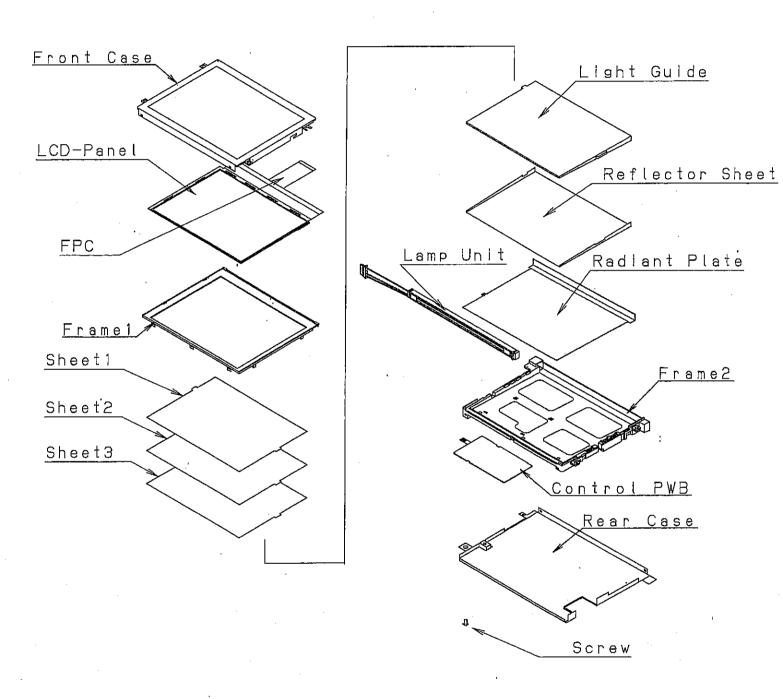


Fig 4 MODULE STRUCTURE

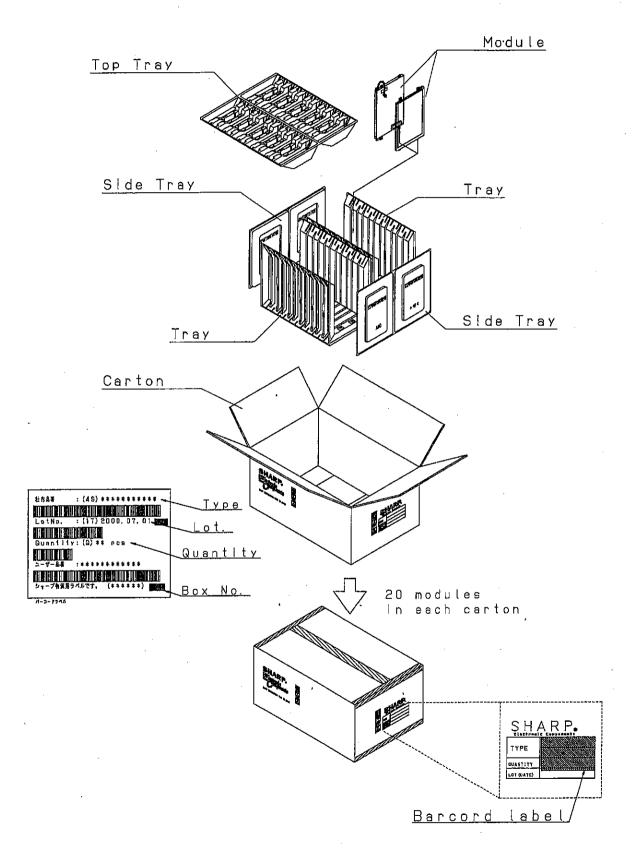


Fig 3 PACKING FORM