

SC88PIO 80188 CPU with I/O

The SC88PIO is an 80188 CPU, with up to 256Kbyte EPROM and up to 256Kbyte SRAM, with battery back-up and write protection on-board. The main applications are for fast low-cost target systems, or as the main CPU in large systems. The SC88PIO also integrates 28 buffered parallel I/O lines on the 50-way Arcom standard Signal Conditioning Bus connector.

The SC88PIO has two serial I/O channels, two counter-timers, DMA, and interrupt handling. Eight lines are output (24mA sink), eight lines are input and eight are bit-bidirectional (with 16mA sink capability), two are counter/timer inputs and two are counter/timer outputs. Therefore low-cost target systems can be built with just a SC88PIO and a Signal Conditioning Bus board.

Built as an enhancement of the low-cost SC88T, the SC88PIO is software-compatible with the SC88T. This allows use of exactly the same code and development tools as used on the SC88T. The interrupt, DMA and bus interface can be configured to be identical to the SC88T. The SC88PIO has extended temperature range and low-power CMOS options.



Extended Temperature Range Options

Interface: STEbus default master

Power consumption: 1.5A @ 5V, 30mA @ ±12V

Ordering Information:

SC88PIO	8MHz 80188 CPU, with 64Kb SRAM
SC88PIO-82	8MHz 80188 CPU, with 256Kb SRAM
SC88PIOCM	8MHz 80C188 low-power CPU, with 64Kb SRAM
SC88PIOCM-82	8MHz 80C188 low-power CPU, with 256Kb SRAM
SC88PIOXM	8MHz 80188 extended temperature range CPU, with 64Kb SRAM
SC88PIOXM-82	8MHz 80188 extended temperature range CPU, with 256Kb SRAM

Features:

- 8MHz 80188 processor
- 28 parallel I/O lines
- Up to 256Kb EPROM
- Up to 256Kb SRAM - battery-backed
- Two serial ports
- Software compatible with SC88T
- SourceVIEW development toolkit supported

