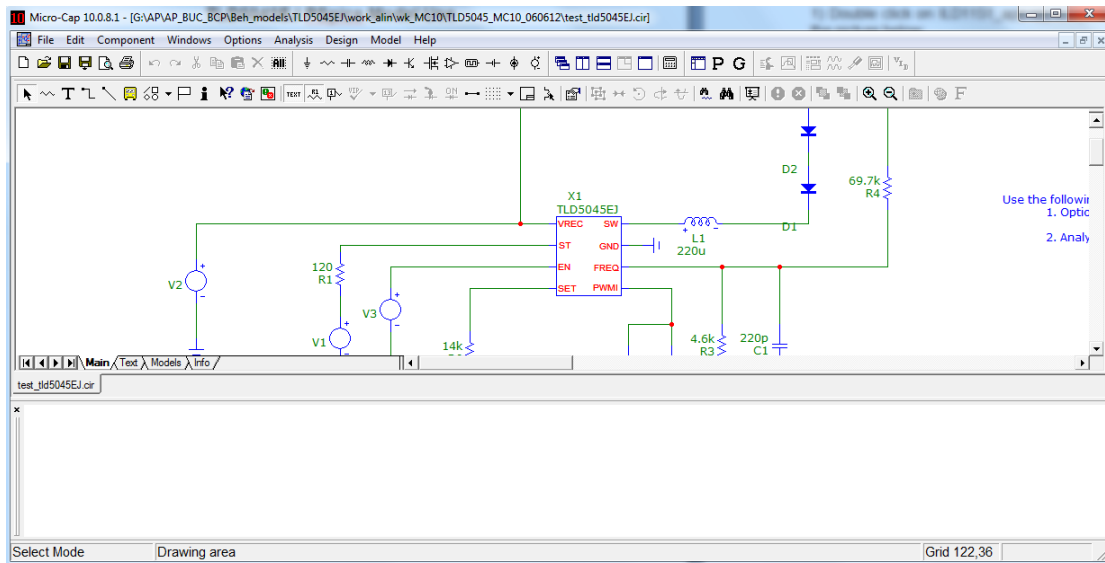


## TLD5045EJ PSpice Model Use

Files	Description
Tld5045EJ.lib	PSpice model
test_tld5045ej.cir	Application example

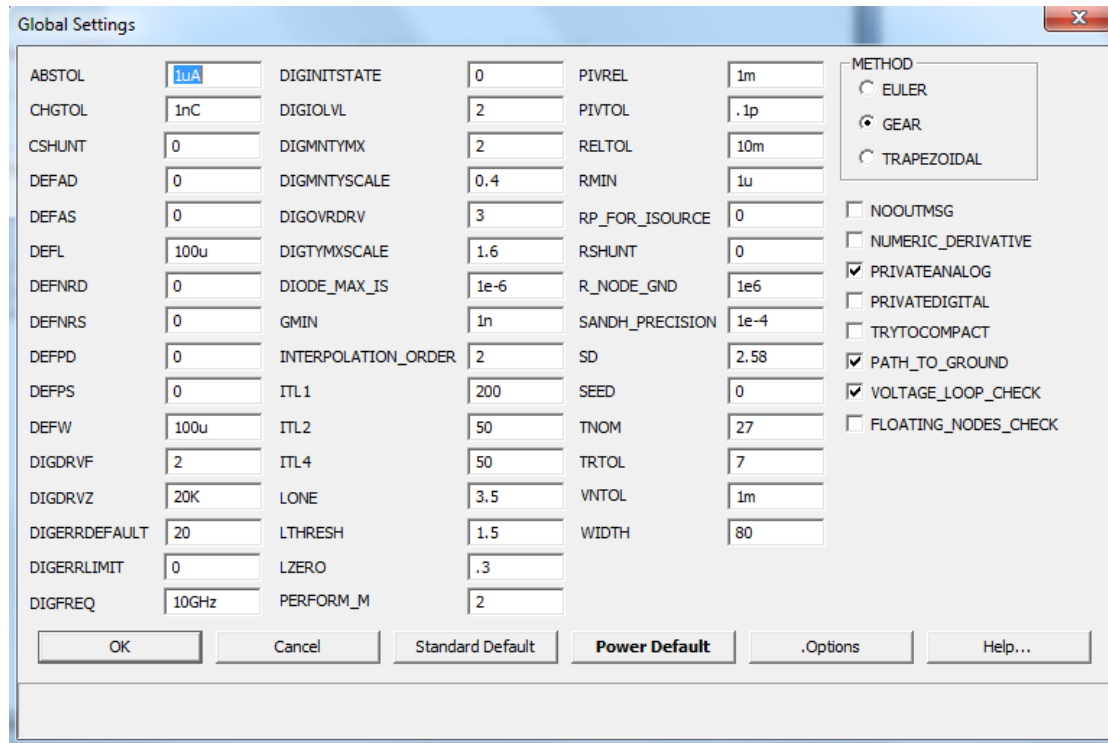
### Micro-CAP 10

Open test\_tld5045ej.cir with Micro-Cap10.



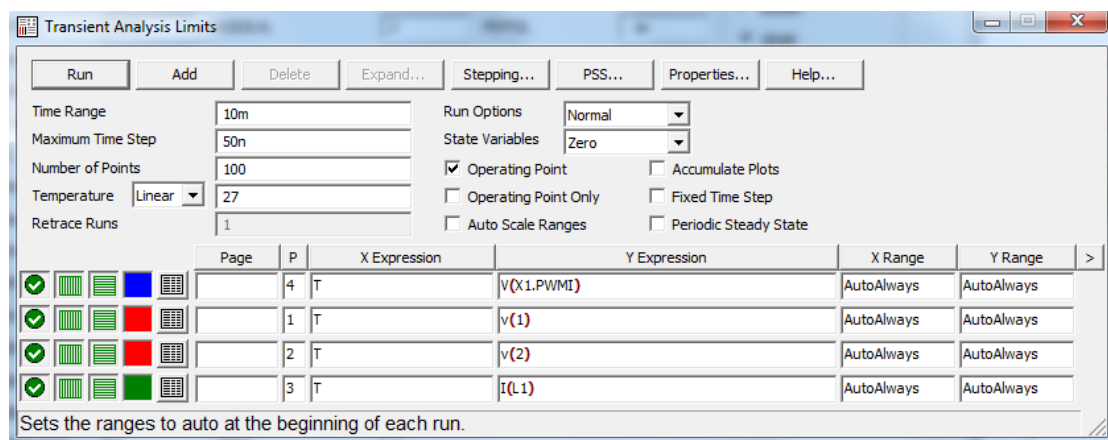
## Global Settings

Options => Global Settings...: Press Power Default button.

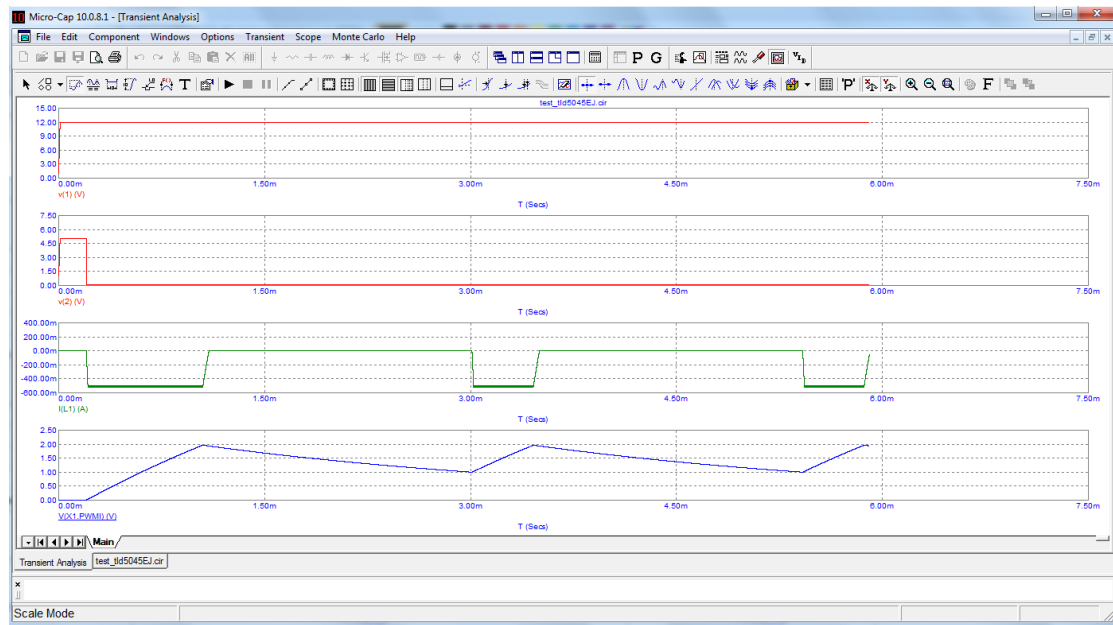


## Simulate the application example

Analysis => Transient...: Run



## Results:



## Disclaimer:

The Simulation Model is subject to change without notice. In addition, models can be a useful tool in evaluating device performance, they cannot reflect the accurate device performance under all conditions, nor are they intended to replace bread boarding for final verification. Infineon therefore does not assume any warranty or liability whatever arising from their use. Infineon does not assume any warranty or liability for the values and functions of the Simulation Model.

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