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Revisions

Rev	Description	Date	Approved
X1	Initial Draft	18/09/13	David Erazmus
X2	Memory section updated (A070 release)	24/09/13	David Erazmus
X3	1. J23 changed as 1X2 header to sel only 3.3V 2. Transistor&Diode, changed from SQ2301 & SS8P3L to PMV65XP & STPS8L30B in 1.25V onboard reg section	26/09/13	David Erazmus
X4	1. Reset Section buffer updated and circuit modified to reduce a jumper	07/10/13	David Erazmus
X5	1. extra TP on net 1.25V_SR removed	14/10/13	David Erazmus
A	1. Back Annatated 2. A085 release	16/10/13	David Erazmus

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ICAP Classification: FCP: FIUO: X PUBI:

Designer: John Paul Franklin	Drawing Title: **X-MPC5777C-516DS**	
Drawn by: John Paul Franklin	Page Title: **Table of Contents/Revisions**	
Approved: David Erazmus	Size C / Document Number SCH-28088 PDF: SPF-28088	Rev A
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1.Unless Otherwise Specified:
All resistors are in ohms, 5%, 1/8 Watt
All capacitors are in uF, 20%, 50V
All voltages are DC
All polarized capacitors are aluminumelectrolytic

2.Intrruted lines coded with the same letter or letter combinations are electrically connected.

3.Device type numberis for reference only. the number varies with te manufacturer.

4.Special signal usage:
_B Denotes - Active-Low Signal
<> or [] Denotes - Vectored SIgnals

5.Inteepret diagram in accordance with american National Standards Institute specifications, current revision, with the exception of logic block symbology

DEFAULT JUMPER SETTINGS

REFDES	Default settings	Description	Header	Jumper Type
J10	1 & 2	VDDA_EQ Selection	HDR 1X2 TH	100MIL
J24	OPEN	VDDFLA Selection	HDR 1X2 TH	100MIL
J7	1 & 2 (5.0V)	VDDA_MISC Select	HDR 1X2 TH	100MIL
J8	1 & 2	VDDA_SD Selection	HDR 1X2 TH	100MIL
J9	1 & 2	VRH_SD Selection	HDR 1X2 TH	100MIL
J11	1 & 2	VRH_EQ Selection	HDR 1X2 TH	100MIL
J3	1 & 2	VDDE8,9,10 Select	HDR 1X2 TH	100MIL
J29	2 & 3 (5.0V)	VDDEHX Selection	HDR 1X3	2MM
J27	1 & 2	Reset Enable	1X2	2MM
J1	2 & 3 (3.3V_MB)	3.3V SR Selection	HDR 1X3	2MM
J15	1 & 2 (3.3V)	VDDE2 Selection	HDR 1X3	2MM
J17	2 & 3 (5V)	VDDPWR Selection	HDR 1X3	2MM
J19	2 & 3 (5.0V)	VDDPMC Selection	HDR 1X3	2MM
J2	2 & 3 (5.0V_MB)	5V SR Selection	HDR 1X3	2MM
J25	2 & 3 (5.0V)	VDDEH3A Selection	HDR 1X3	2MM
J26	2 & 3	PM4 as PCSA2	HDR 1X3	2MM
J28	1 & 2 (3.3V)	VDDEA Selection	HDR 1X3	2MM
J4	2 & 3 (CS#1)	SRAM Chip Select	HDR 1X3	2MM
J32	2 & 3 (5.0V)	VDD_HV_IO_MAIN	HDR 1X3	2MM
J31	1 & 2 (3.3V)	JTAG VDD Selection	HDR 1X3	2MM
J18	2 & 3 (GND)	REGSEL to GND	HDR 1X3	2MM
J20	2 & 3	REGCTL-1.25V_OB1	HDR 1X3	2MM
J6	1 & 2 (5.0V_LR)	VDDA Selection	HDR 1X3	2MM
J22	4 & 3	1.25V_SR_MB	HDR 2X3	2MM
J16	3 & 5 (GND)	VSTBY Selection	HDR 2X3	2MM
J13	OPEN (ON)	3.3V Regulator OFF	1X2	2MM
J14	OPEN (ON)	5V Regulator OFF	1X2	2MM

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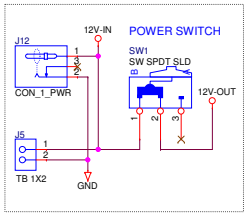
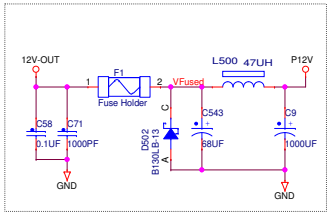
Power supply : Connectors and Regulators

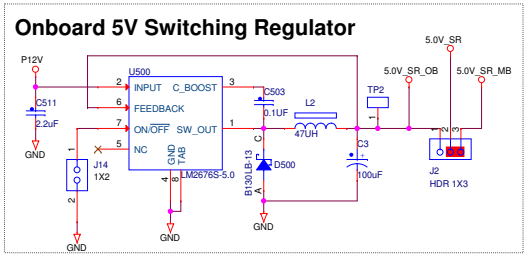
Input Power Conectors



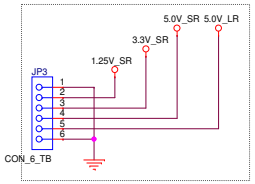
Power supply input and filter



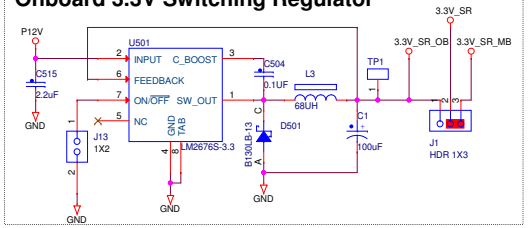
Onboard 5V Switching Regulator

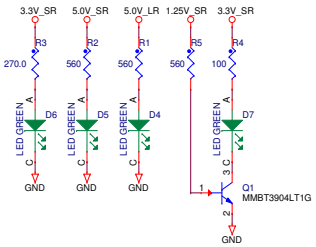


External Power feed

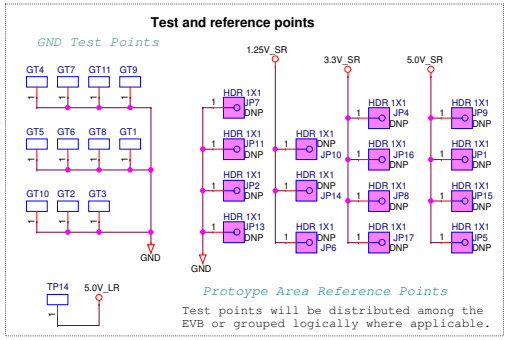


Onboard 3.3V Switching Regulator





Test and reference points



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ICAP Classification:	FCP: ___ FIUO: X PUBI: ___	
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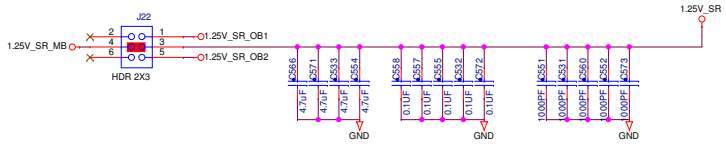
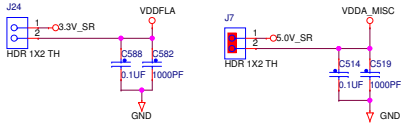
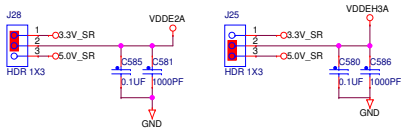
MCU Power and 1.2V Regulator

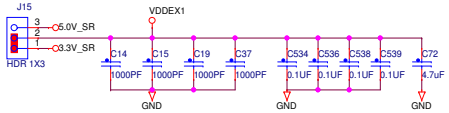
DEFAULT JUMPER SETTINGS

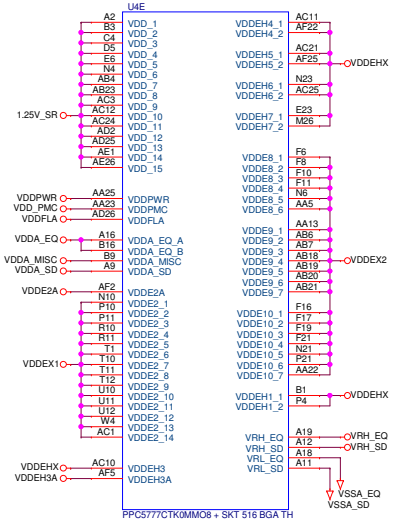
REFDES	Default settings	Description	Header	Jumper Type
J10	1 & 2	VDDA_EQ Selection	HDR 1X2 TH	100MIL
J24	OPEN	VDDFLA Selection	HDR 1X2 TH	100MIL
J7	1 & 2 (5.0V)	VDDA_MISC Select	HDR 1X2 TH	100MIL
J8	1 & 2	VDDA_SD Selection	HDR 1X2 TH	100MIL
J9	1 & 2	VRH_SD Selection	HDR 1X2 TH	100MIL
J11	1 & 2	VRH_EQ Selection	HDR 1X2 TH	100MIL
J3	1 & 2	VDDE8,9,10 Select	HDR 1X2 TH	100MIL
J29	2 & 3 (5.0V)	VDDEHX Selection	HDR 1X3	2MM
J27	1 & 2	Reset Enable	1X2	2MM
J1	2 & 3 (3.3V_MB)	3.3V SR Selection	HDR 1X3	2MM
J15	1 & 2 (3.3V)	VDDE2 Selection	HDR 1X3	2MM
J17	2 & 3 (5V)	VDDPWR Selection	HDR 1X3	2MM
J19	2 & 3 (5.0V)	VDDPMC Selection	HDR 1X3	2MM
J2	2 & 3 (5.0V_MB)	5V SR Selection	HDR 1X3	2MM
J25	2 & 3 (5.0V)	VDDEH3A Selection	HDR 1X3	2MM
J26	2 & 3	PM4 as PCSA2	HDR 1X3	2MM
J28	1 & 2 (3.3V)	VDDE2A Selection	HDR 1X3	2MM
J4	2 & 3 (CS#1)	SRAM Chip Select	HDR 1X3	2MM
J32	2 & 3 (5.0V)	VDD_HV_IO_MAIN	HDR 1X3	2MM
J31	1 & 2 (3.3V)	JTAG VDD Selection	HDR 1X3	2MM
J18	2 & 3 (GND)	REGSEL to GND	HDR 1X3	2MM
J20	2 & 3	REGCTL-1.25V_OB1	HDR 1X3	2MM
J6	1 & 2 (5.0V_LR)	VDDA Selection	HDR 1X3	2MM
J22	4 & 3	1.25V_SR_MB	HDR 2X3	2MM
J16	3 & 5 (GND)	VSTBY Selection	HDR 2X3	2MM
J13	OPEN (ON)	3.3V Regulator OFF	1X2	2MM
J14	OPEN (ON)	5V Regulator OFF	1X2	2MM

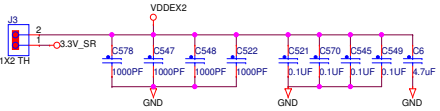


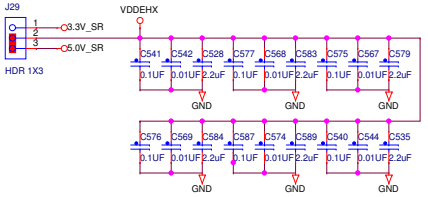


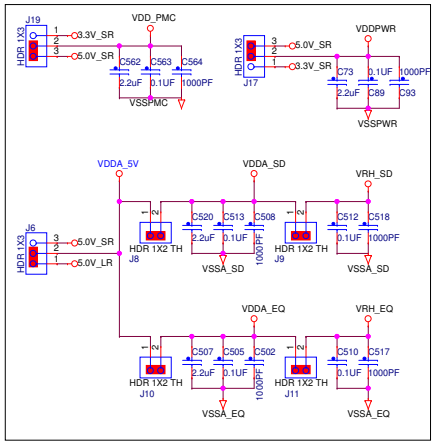


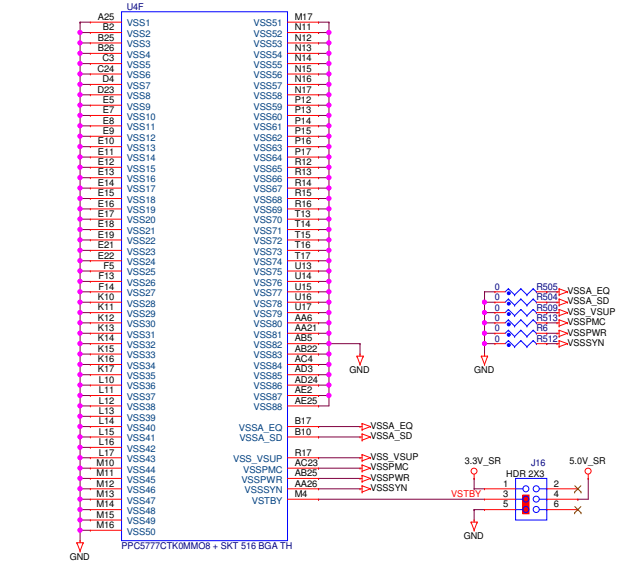




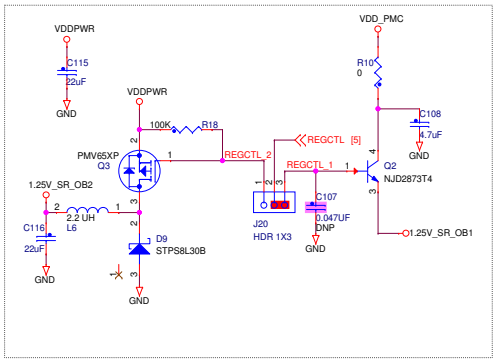








Onboard 1.25V Switching Regulator



ICAP Classification:	FCP:	FIUO: X	PUBI:	
Drawing Title:	X-MPC5777C-516DS			
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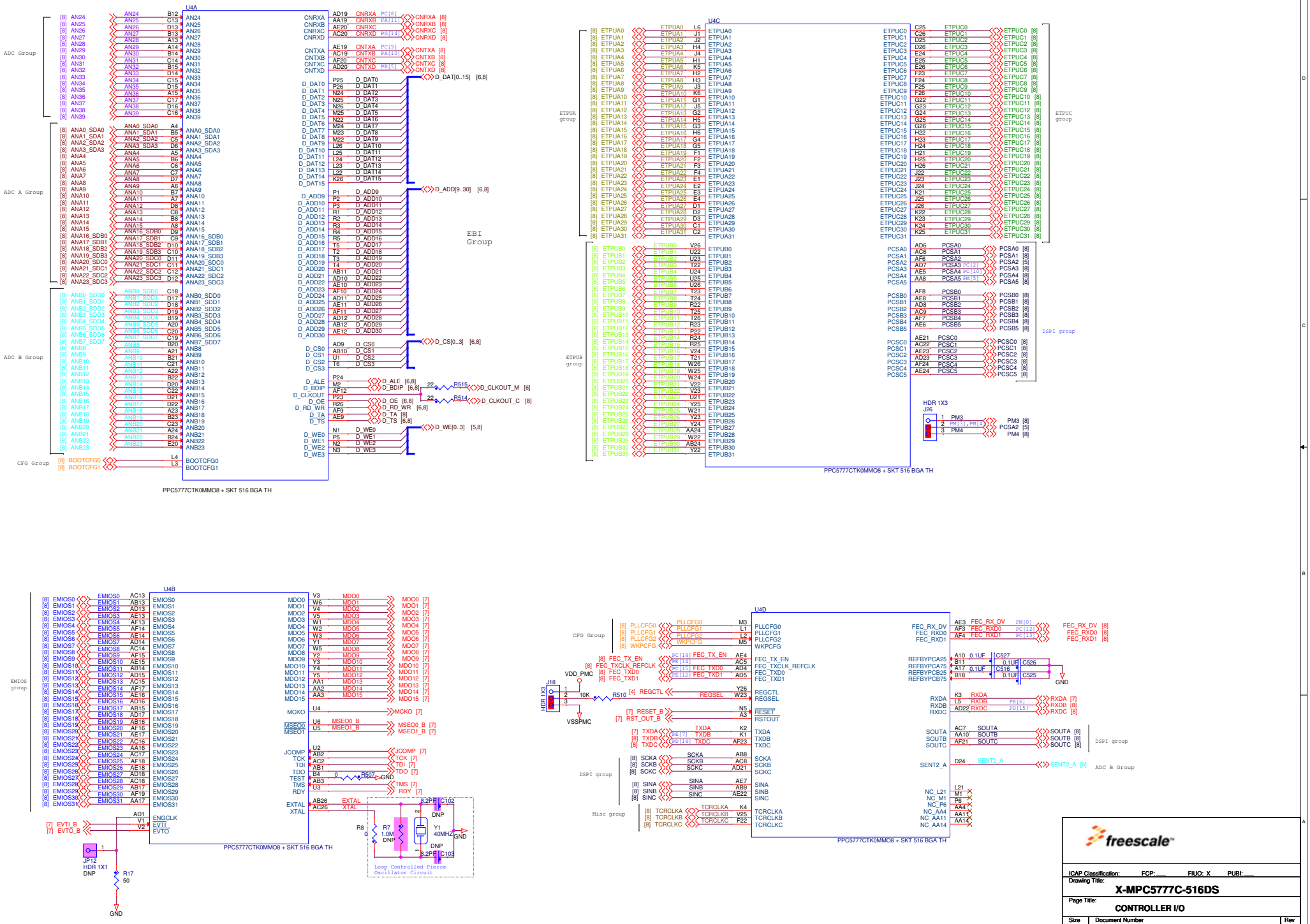
CONTROLLER I/O



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ICAP Classification:	FCP: ___	FIUO: X	PUBI: ___
Drawing Title:	X-MPC5777C-516DS		
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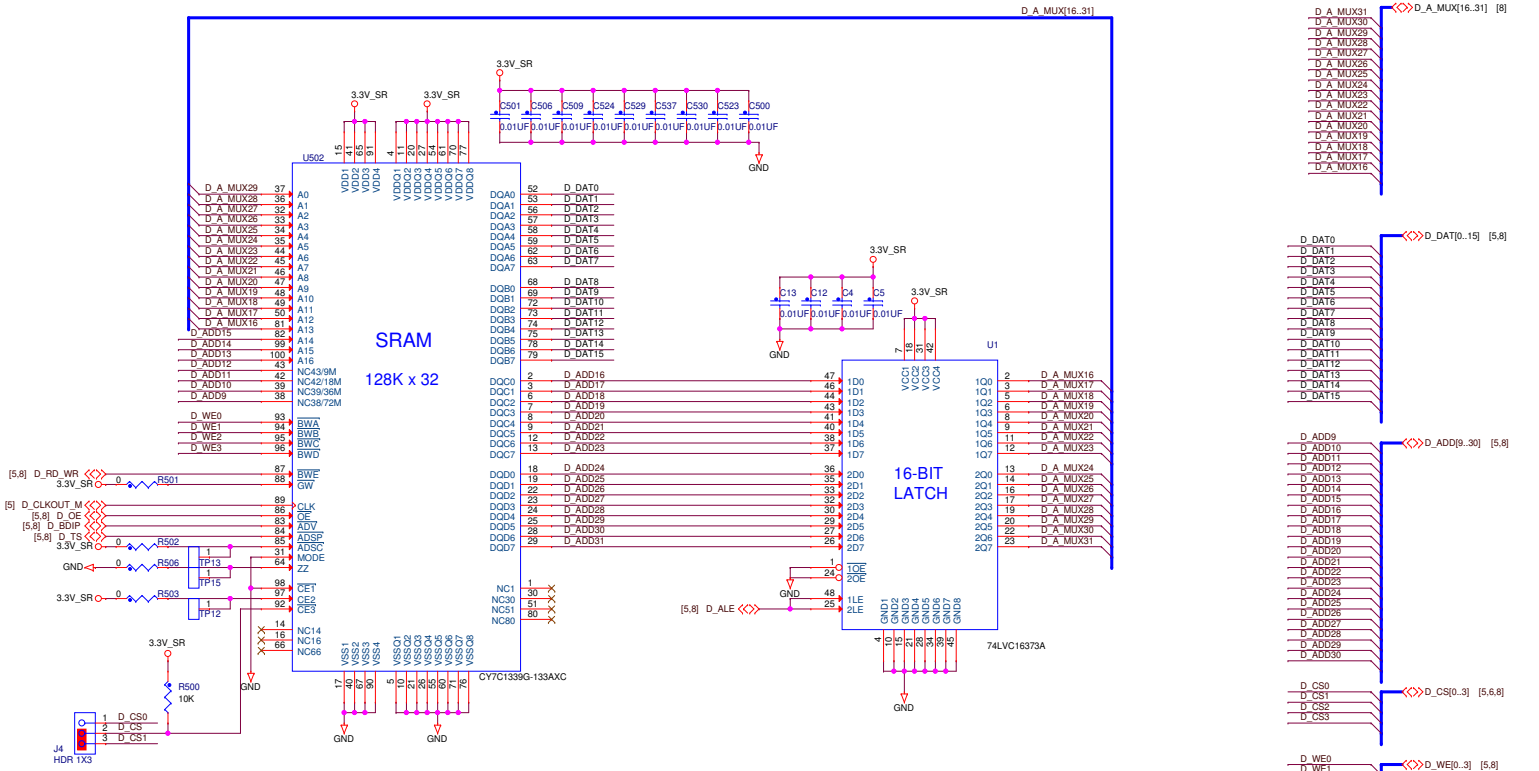
ON BOARD MEMORY



ICAP Classification:	FCP: ___	FIUO: X	PUBI: ___
Drawing Title:	X-MPC5777C-516DS		
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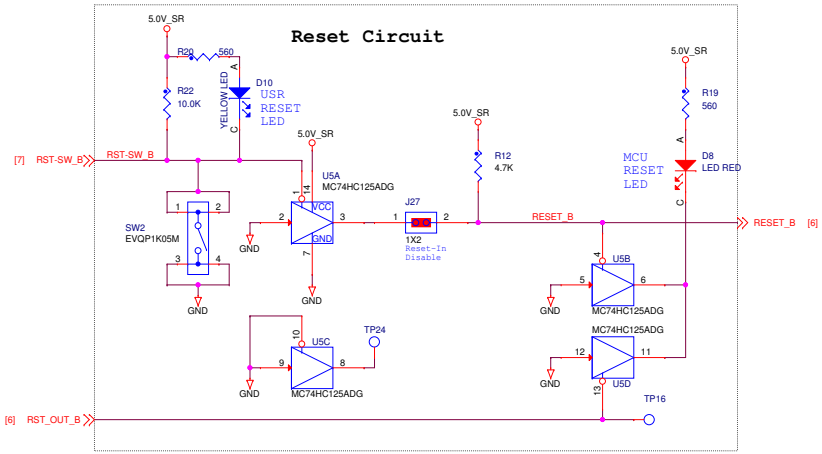
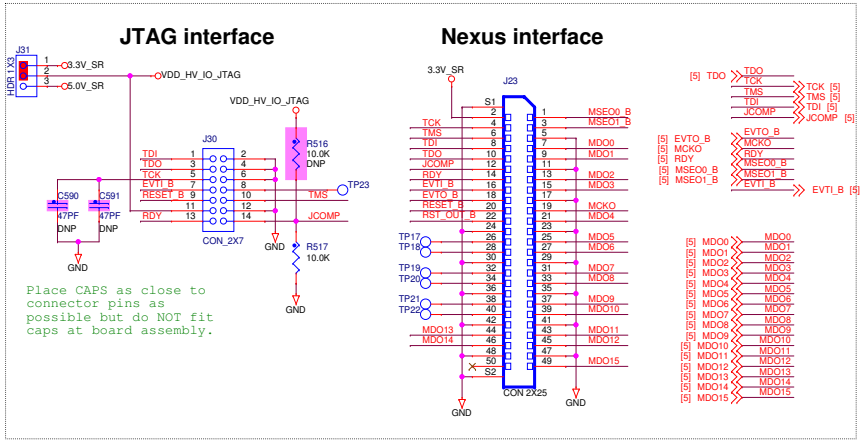
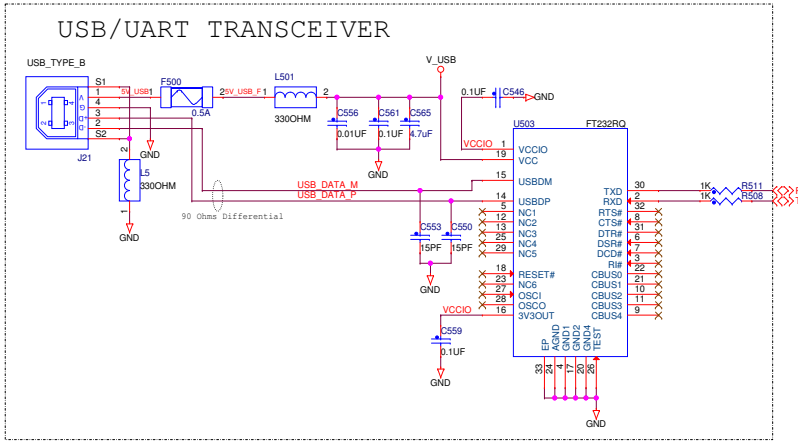
RESET, JTAG, NEXUS & UART







freescale

ICAP Classification:	FCP: ___ FIUO: X PUBI: ___	
Drawing Title:	X-MPC5777C-516DS	
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DAUGHTER CARD TO MOTHERBOARD CONNECTOR