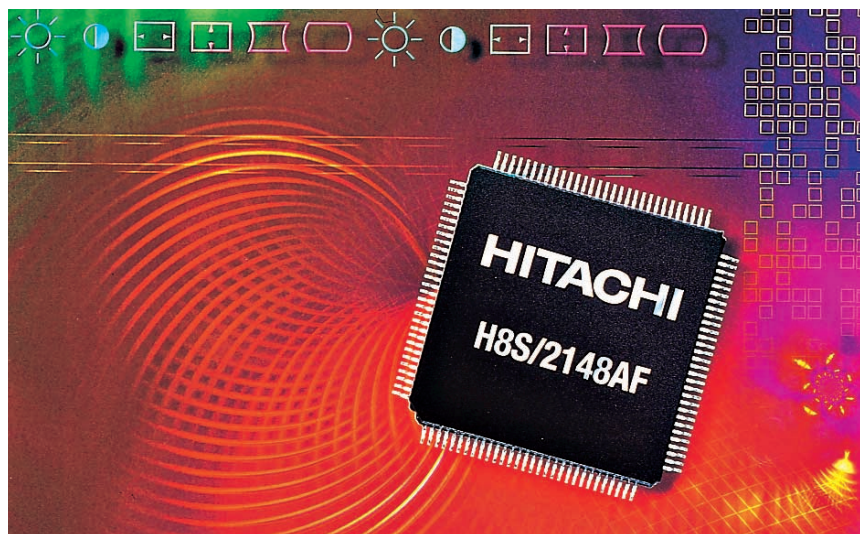


High Performance, Low Cost Flash Microcontrollers

Hitachi H8S/2144F Series

A complete family of code and function compatible Flash microcontrollers, providing low cost solutions for every application.



Key features include

- 20 MHz, 50 ns H8S CPU Core
- Pin compatible 64k, 128k and 128k + I²C

Flash Versions

- 1 x 16-bit timer and up to four 8-bit timers
- 2 watchdog timers
- Up to 3 Serial channels, optional I²C interface
- Up to 24 channels of A/D, 2 Channels of D/A
- Mask ROM versions also available

100-pin

H8S/2142F

64 k bytes Flash
2 kbytes SRAM

H8S/2144F

128 k bytes Flash
4 kbytes SRAM

H8S/2148F

128 k bytes Flash
4 kbytes SRAM

+I²C

80-pin

H8S/2132F

64 k bytes Flash
2 kbytes SRAM

H8S/2134F

128 k bytes Flash
4 kbytes SRAM

H8S/2138F

128 k bytes Flash
4 kbytes SRAM

+I²C

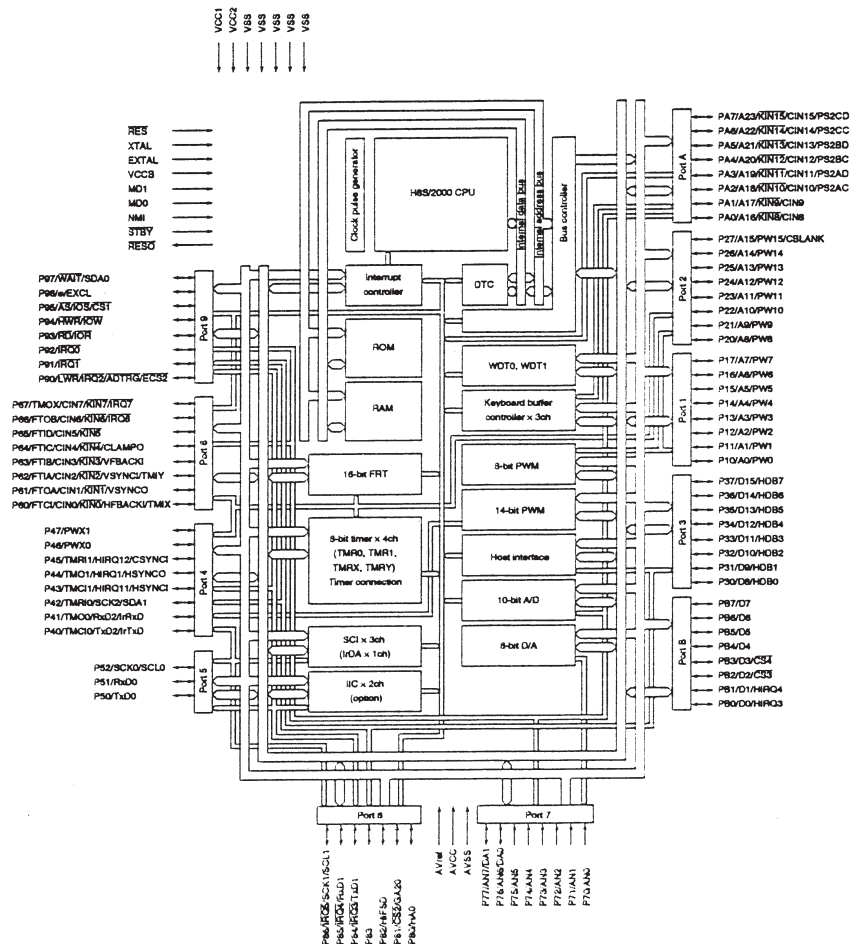
64-pin

H8S/2128F

128 k bytes Flash
4 kbytes SRAM

+I²C

H8S/2148F



Hitachi H8 Development Tools



- H8S/2148 E6000 In circuit Emulator
 - Full in-circuit emulation with HDI debugger
- H8S/2144 Evaluation Boards
 - Evaluation board with HDI debugger
- Hitachi Embedded Workshop
 - Complete "C" development environment including compiler
- Hitachi MakeApp
 - Applications builder for H8S/2144F
- Hitachi Flash Development tools
 - Programming tools for on-chip flash

- 20 MHz 16/32-bit H8S CPU Core
- 50 ns minimum instruction cycle time
- 128 kbytes single supply in circuit programmable Flash
- 4 kbytes SRAM
- 1 x 16-bit timer
- 4 x 8-bit timers
- 16 channel 8-bit PWM Timer
- 2 channel 14-bit PWM Timer
- 2 x Watchdog timers
- 3 channels SCI
- 2 channels I2C Interface
- 8 channels of 10-bit A/D
- 16 channels of 6-bit A/D
- 2 channels 8-bit D/A
- Data Transfer controller
- Keyboard controller / Host CPU Interface
- 82 I/O pins
- 100 pin QFP or TQFP package

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