

# Specification

**BTHQ 96040AV2-FERE-12-12C-COG**

**Doc. No.:COG-BT96040A-05**

**Version july 2008**

**DOCUMENT REVISION HISTORY**

DOCUMENT REVISION FROM TO	DATE	DESCRIPTION	CHANGED BY	CHECKED BY
A	2008.07.30	<p>First Release. Based on a.) Test Specification: VL-TS-COG-BT96040A-XX REV.C 2008.07.07 b.) VL-QUA-012B REV.X 2008.02.18</p> <p>According to VL-QUA-012B, for positive mode, LCD size is small because Unit Per Laminate=28 which is more than 6pcs/Laminate.</p>	LINDA ZHU	TAN XIAO DI

## CONTENTS

	<u>Page No.</u>
1. GENERAL DESCRIPTION	4
2. MECHANICAL SPECIFICATIONS	4
3. INTERFACE SIGNALS	6
4. ABSOLUTE MAXIMUM RATINGS	7
4.1 ELECTRICAL MAXIMUM RATINGS –FOR IC ONLY	7
4.2 ENVIRONMENTAL CONDITION	7
5. ELECTRICAL SPECIFICATIONS	8
5.1 TYPICAL ELECTRICAL CHARACTERISTICS	8
5.2 TIMING SPECIFICATIONS	9
5.3 COMMAND DESCRIPTION	11
6. LCD SPECIFICATIONS	12
7. LCD COSMETIC CONDITIONS	20
8. REMARK	20

**Specification  
of  
LCD Module Type  
Item No.: COG-BT96040A-05**

**1. General Description**

- 96 x 40 dots FSTN B & W Positive Reflective Dot Matrix LCD module.
- Viewing angle: 12 O'clock.
- Driving scheme: 1/68 duty, 1/9 bias.
- Driving IC: 'SITRONIX' ST7549Ti (COG) LCD controller/driver or equivalent.
- Logic voltage: 3V.
- "RoHS" compliance.

**2. Mechanical Specifications**

The mechanical detail is shown in Fig. 1 and summarized in Table 1 below.

Table 1

Parameter	Specifications	Unit
Outline dimensions	46.0(W) x 36.2(H) x 1.98(D) (Excluded pins and EPOXY)	mm
Viewing area	40.00(W) x 23.00(H)	mm
Active area	35.217(W) x 18.225(H)	mm
Display format	96 x 40	dots
Dot size	0.352(W) x 0.441(H)	mm
Dot spacing	0.015(W) x 0.015(H)	mm
Dot pitch	0.367 (W) x 0.456(H)	mm
Weight:	Approx: 6.5	gram

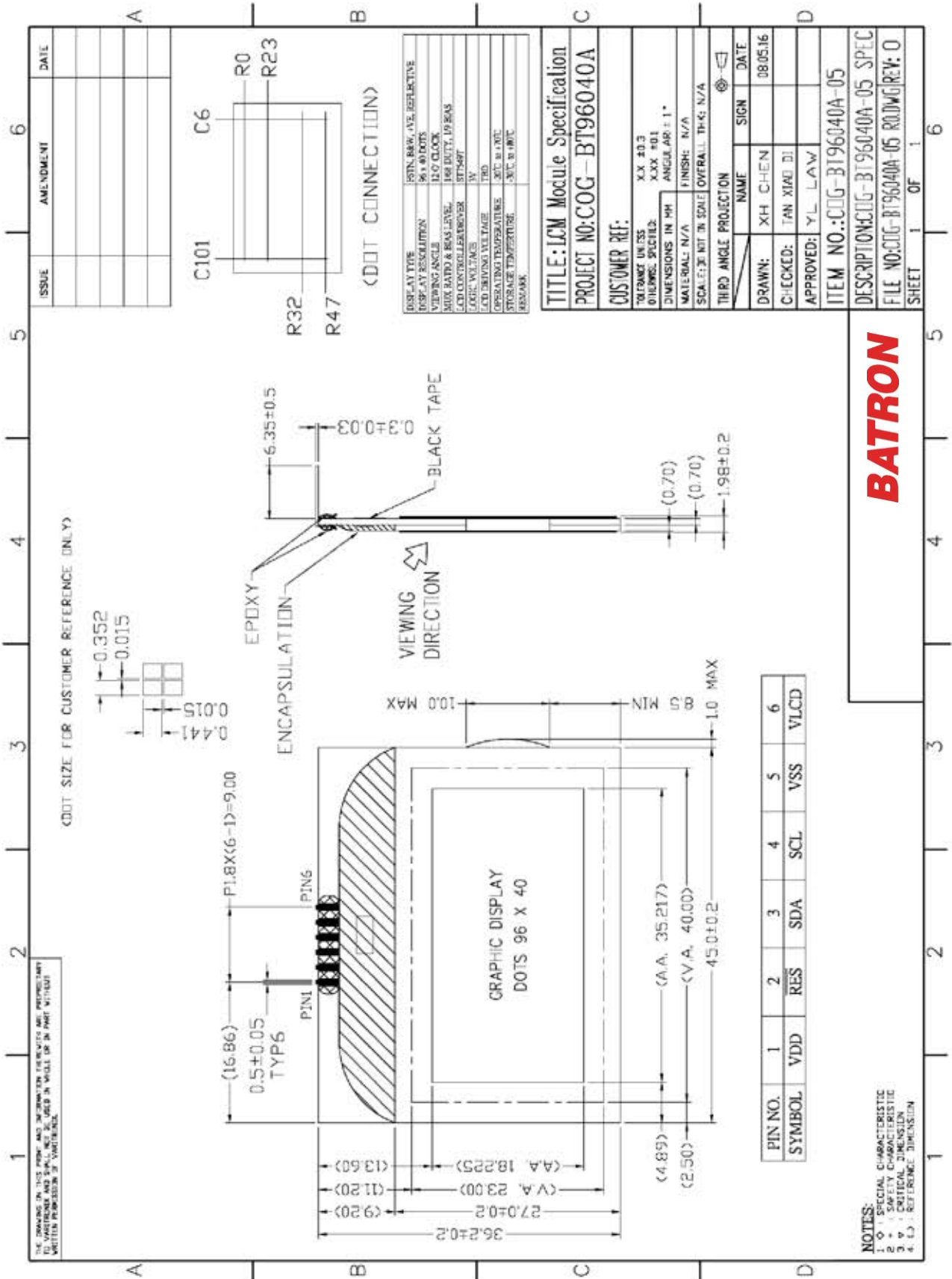


Figure 1: Module Specification

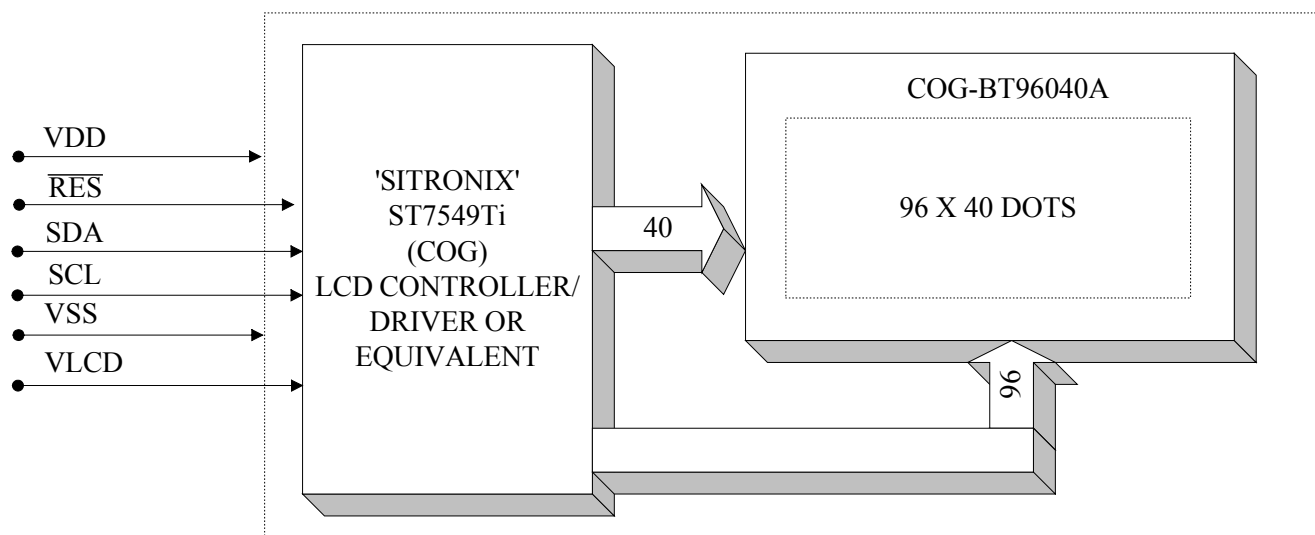


Figure 2: Block Diagram

### 3. Interface signals

Table 2

Pin No.	Symbol	Description
1	VDD	Power supply for logic (+3V).
2	$\overline{\text{RES}}$	Reset Input Pin. When $\overline{\text{RES}}$ is " L ", initialization is executed.
3	SDA	Serial input data.
4	SCL	Serial clock input.
5	VSS	Ground (0V).
6	VLCD	Power supply for LCD driver.

## 4. Absolute Maximum Ratings

### 4.1 Electrical Maximum Ratings-For IC Only

Table 3

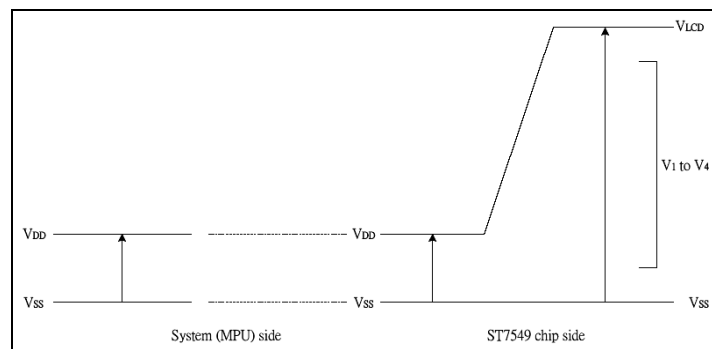
Parameter	Symbol	Min.	Max.	Unit
Power supply voltage(VDD1)	VDD1	-0.5	5	V
Power supply voltage(VDD2)	VDD2	-0.5	5	V
Power supply voltage(VLCDIN) (VDD standard)	VLCDIN	4.5	13	V
Power supply voltage(V1,V2,V3,V4) (VDD standard)	V1,V2,V3,V4	0.3	VLCDIN	V
Input voltage	CSB,RESB,A0, /WR,/RD,D7~D0	-0.5	5	V

Note: The modules may be destroyed if they are used beyond the absolute maximum ratings.

All voltages are with respect to VSS unless otherwise noted.

Insure that the voltage levels of V1, V2, V3, and V4 are always such that

$V_{LCDIN} \geq V_0 \geq V_1 \geq V_2 \geq V_3 \geq V_4 \geq V_{SS}$ .



### 4.2 Environmental Condition

Table 4

Item	Operating Temperature (Topr)		Storage Temperature (Tstg) (Note 1)		Remark
	Min.	Max.	Min.	Max.	
Ambient Temperature	-20°C	+70°C	-30°C	+80°C	Dry
Humidity (Note 1)	90% max. RH for $T_a \leq 40^\circ\text{C}$ <50%RH for $40^\circ\text{C} < T_a \leq$ Maximum operating temperature				No condensation
Vibration (IEC 68-2-6) cells must be mounted on a suitable connector	Frequency: 10 ~ 55 Hz Amplitude: 0.75 mm Duration: 20 cycles in each direction.				3 directions
Shock (IEC 68-2-27) Half-sine pulse shape	Pulse duration: 11 ms Peak acceleration: $981 \text{ m/s}^2 = 100g$ Number of shocks: 3 shocks in 3 mutually perpendicular axes.				3 directions

Note 1: Product cannot sustain at extreme storage conditions for long time.

## 5. Electrical Specifications

### 5.1 Typical Electrical Characteristics

At  $T_a = 25\text{ °C}$ ,  $V_{DD} = V_{DD1} = V_{DD2} = +3.0V \pm 5\%$ ,  $V_{SS} = 0V$ .

Table 5

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Supply voltage (Logic)	VDD-VSS		2.85	3.0	3.15	V
High-level Input Voltage	$V_{IHC}$		0.7 VDD	-	VDD	V
Low-level Input Voltage	$V_{ILC}$		VSS	-	0.3 VDD	V
Supply Current (Logic & LCD)	IDD	Character mode, VDD = 3V, Note 1	-	0.6	0.9	mA
		Checker board mode, VDD = 3V, Note 1	-	0.65	1.0	mA

Note 1: There is tolerance in optimum LCD driving voltage during production and it will be within the specified range.

Note 2: VLCD can not be tested.



## 5.2 Timing Specifications

### Reset Timing

Ta = -20 °C to +70 °C, VDD = 3.3V, VSS=0V;

Table 6 (a)

Item	Signal	Symbol	Condition	Rating			Units
				Min.	Typ.	Max.	
Reset time		tR		—	—	1	us
Reset "L" pulse width	RESB	tRW		1	—	—	us

Ta = -20 °C to +70 °C, VDD = 2.8V, VSS=0V;

Table 6 (b)

Item	Signal	Symbol	Condition	Rating			Units
				Min.	Typ.	Max.	
Reset time		tR		—	—	2.0	us
Reset "L" pulse width	RESB	tRW		2.0	—	—	us

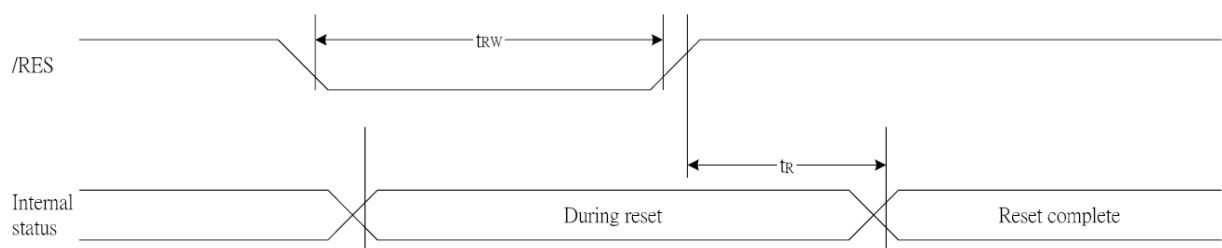


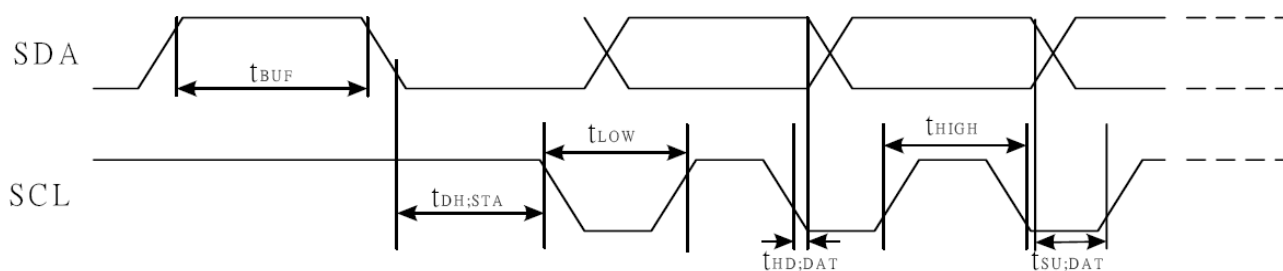
Figure 3: Reset Timing Diagram

## I<sup>2</sup>C BUS INTERFACE

Ta = -20 °C to + 70 °C, VDD = 3.3V, VSS=0V;

Table 7

Item	Signal	Symbol	Condition	Rating		Units
				Min.	Max.	
SCL clock frequency	SCL	FSCLK		-	400	kHZ
SCL clock low period	SCL	TLOW		1.3	-	us
SCL clock high period	SCL	THIGH		0.6	-	us
Data set-up time	SI	TSU;Data		100	-	ns
Data hold time	SI	THD;Data		0	0.9	us
SCL,SDA rise time	SCL	TR		20+0.1Cb	300	ns
SCL,SDA fall time	SCL	TF		20+0.1Cb	300	ns
Capacitive load represented by each bus line		Cb		-	400	pF
Setup time for a repeated START condition	SI	TSU;SUA		0.6	-	us
Start condition hold time	SI	THD;STA		0.6	-	us
Setup time for STOP condition		TSU;STO		0.6	-	us
Tolerable spike width on bus		TSW		-	50	ns
BUS free time between a STOP and START condition	SCL	TBUF		1.3		us



## 5.3 Command Description

Referential instruction setup flow: Initializing with the built-in power supply circuits.

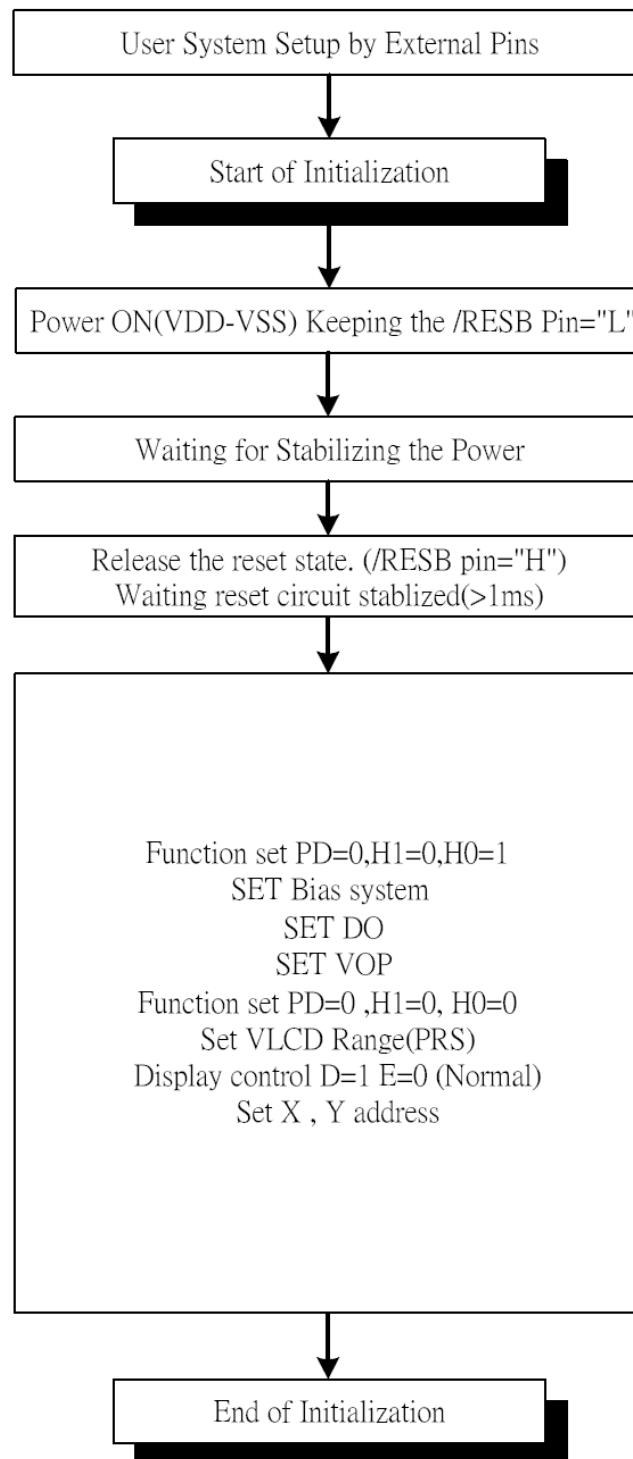


Figure 5: Initializing with the built-in Power Supply Circuits









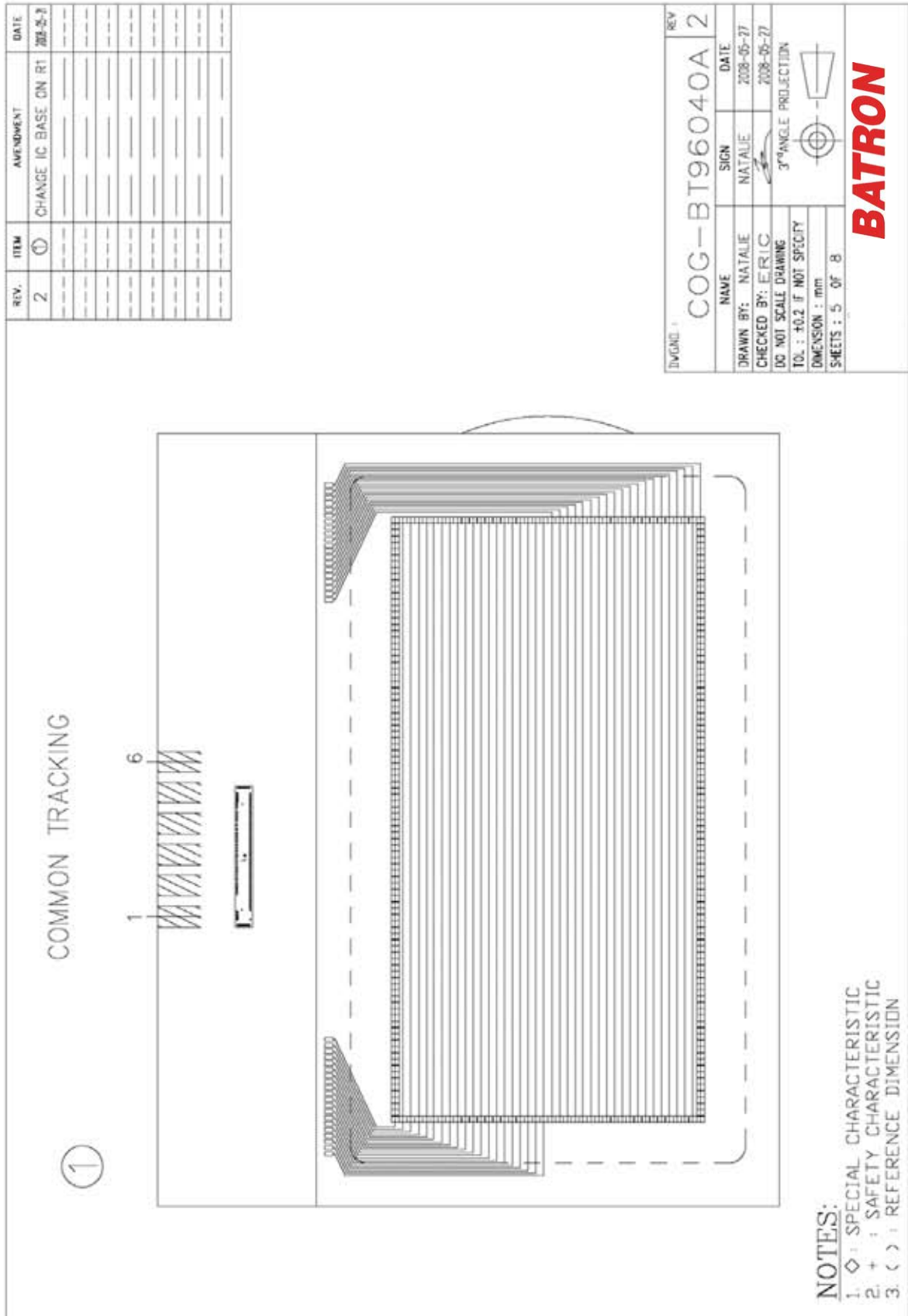


Figure 10: LCD drawing 5







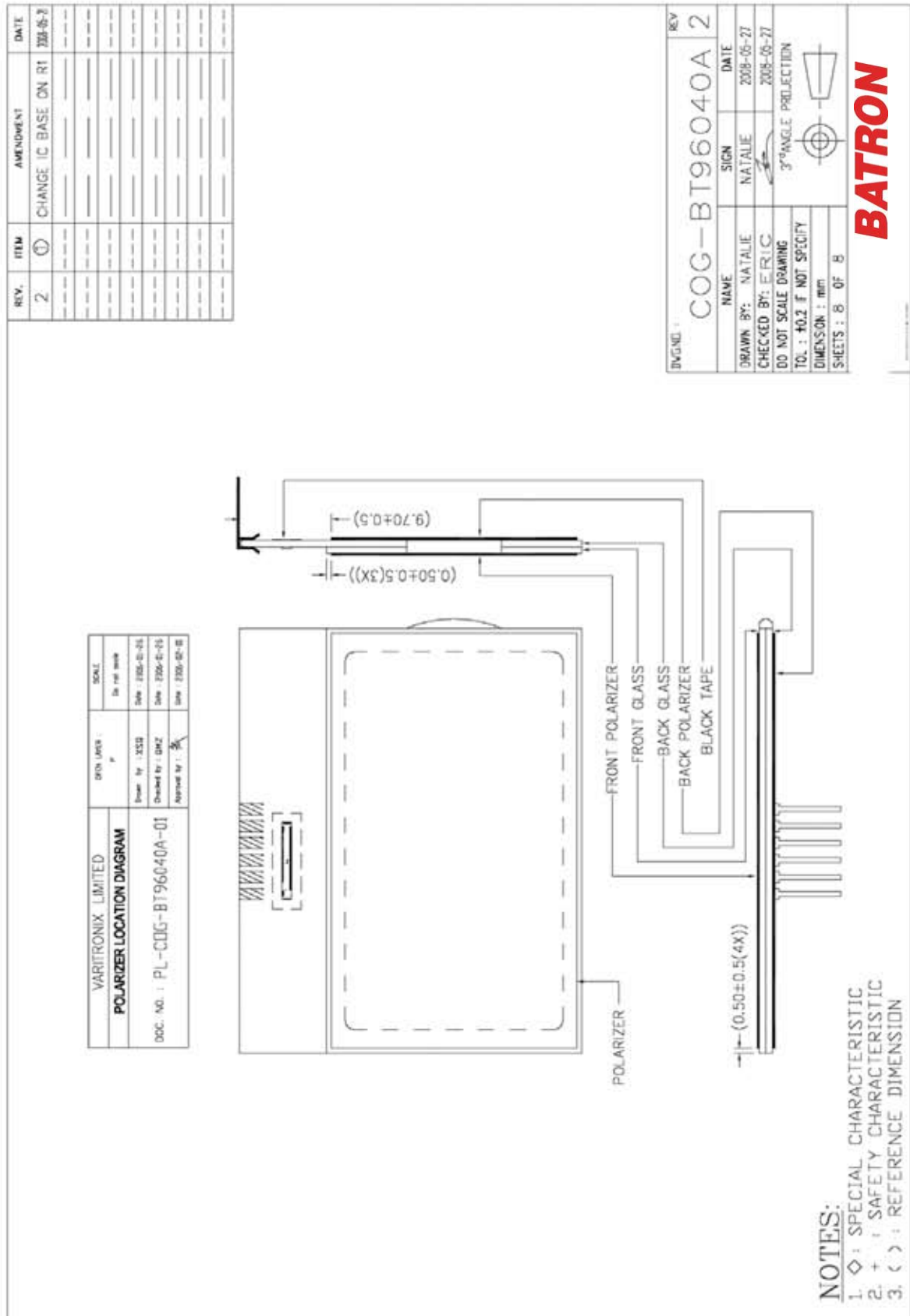


Figure 13: LCD drawing 8



Data Modul Headquarters Munich  
Landsberger-Str. 322  
D-80687 Munich - Germany  
Tel.: +49-89-56017-0



Sales Office Duesseldorf  
Fritz-Vomfelde-Str. 8  
D-40547 Duesseldorf - Germany  
Tel.: +49-211-52709-0



Sales Office Hamburg  
Borsteler Chaussee 51  
D-22453 Hamburg - Germany  
Tel.: +49-40-42947377 - 0



Sales Office Stuttgart  
Friedrich-List-Str. 42  
D-70771 Leinfelden-Echterdingen  
Germany  
Tel.: +49-711-782385-0



Data Modul France, S.A.R.L.  
Bat B - Hall 204  
1-3 Rue des Campanules  
77185 Lognes - France  
Tel.: +33-1-60378100



Data Modul Italia, S.r.l.  
Regus Center Senigallia  
Via Senigallia 18/2  
20161 Milano - Italy  
Tel.: +39-02-64672-509



Data Modul Iberia, S.L.  
c/ Adolfo Pérez Esquivel 3  
Edificio Las Americas III Oficiana 40  
28230 Parque Empresarial  
Madrid Las Rozas - Spain  
Tel.: +34-916 366 458

Data Modul Ltd. / UK  
Collons Building  
3 Vigo Place - Aldridge - Walsall - WS9 8UG  
United Kingdom  
Tel.: +44-121-698-8641

Data Modul Inc. / USA  
275 Marcus Blvd, Unit K  
Hauppauge, NY 11788  
USA  
Tel.: (631)-951-0800