Not for New Design - End of Life - Last Available Purchase Date is 31-August-2011

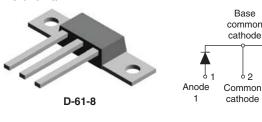


VS-81CNQ035A, VS-81CNQ040A, VS-81CNQ045A

Vishay Semiconductors

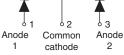
Schottky Rectifier New Generation 3 D-61 Package, 2 x 40 A

VS-81CNQ...A



VS-81CNQ...ASM





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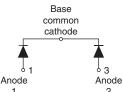
Anode

2

D-61-8-SM

VS-81CNQ...ASL





PRODUCT SUMMARY				
I _{F(AV)}	2 x 40 A			
V _R	35 V to 45 V			

FEATURES

- 175 °C T_J operation
- Center tap module
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- New fully transfer-mold low profile, small footprint, high current package
- Designed and qualified for industrial level

DESCRIPTION

The center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	80	A		
V _{RRM}		35 to 45	V		
I _{FSM}	t _p = 5 μs sine	4600	A		
V _F	40 A _{pk} , T _J = 125 °C (per leg)	0.54	V		
TJ	Range	- 55 to 175	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-81CNQ035A	VS-81CNQ040A	VS-81CNQ045A	UNITS
Maximum DC reverse voltage	V _R	35	40	45	V
Maximum working peak reverse voltage	V _{RWM}		40	40	v

VS-81CNQ035A, VS-81CNQ040A, VS-81CNQ045A

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS VALUES UN		UNITS	
Maximum average forward current See fig. 5	I _{F(AV)}	$T_{\rm AV}$ 50 % duty cycle at T _C = 141 °C, rectangular waveform 80			
Maximum peak one cycle non-repetitive surge current per leg		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	4600	А
See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	rated V_{RRM} applied	790	
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 8 A, L = 1.7 mH 54 m.		mJ	
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s8Frequency limited by T _J maximum V _A = 1.5 x V _R typical8		А	

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	_ TEST CONDITIONS VALUES U		UNITS	
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	40 A	- T _J = 25 °C	0.60	V
		80 A		0.74	
		40 A	- T _J = 125 °C	0.54	
		80 A		0.66	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C		5	mA
See fig. 2		T _J = 125 °C	V _R = Rated V _R	45	
Maximum junction capacitance per leg	CT	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 °C		2600	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		5.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs	

Note

⁽¹⁾ Pulse width < 300 μ s, duty cycle < 2 %

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THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C	
Maximum thermal resistation to case per leg	ance,	P	DC operation See fig. 4	0.85		
Maximum thermal resist		R _{thJC}	DC operation	0.42	°C/W	
Typical thermal resistant case to heatsink (D-61-8		R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30		
Approvimente weight				7.8	g	
Approximate weight				0.28	oz.	
Mounting torque	minimum			40 (35)	kgf · cm	
(D-61-8 only)	maximum			58 (50)	(lbf · in)	
				81CN0	Q035A	
Marking device			Case style D-61-8	81CNQ040A		
				81CN0	81CNQ045A	
				81CNQ035ASM		
			Case style D-61-8-SM	81CNQ0	81CNQ040ASM	
				81CNQ	81CNQ045ASM	
				81CNQ035ASL		
			Case style D-61-8-SL	81CNQ040ASL		
				81CNQ045ASL		

VS-81CNQ035A, VS-81CNQ040A, VS-81CNQ045A

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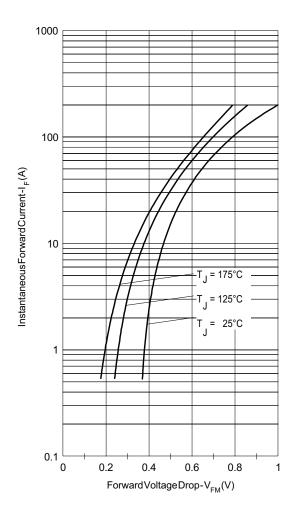


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

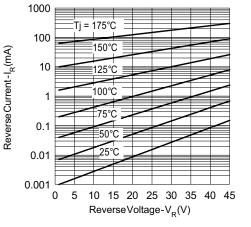


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

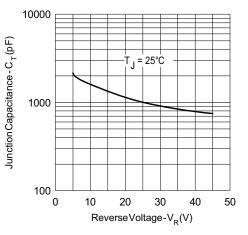
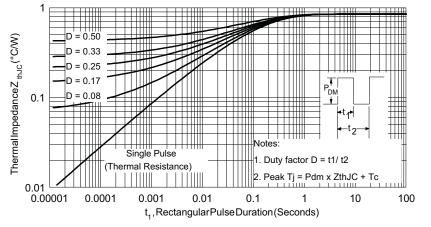


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





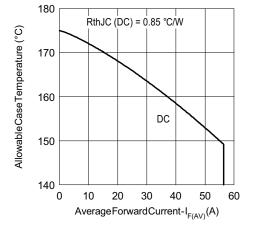
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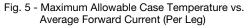
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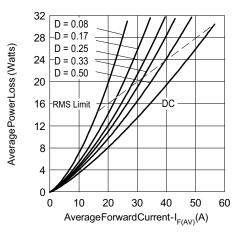
VS-81CNQ035A, VS-81CNQ040A, VS-81CNQ045A

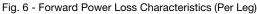
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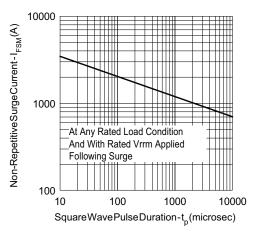


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

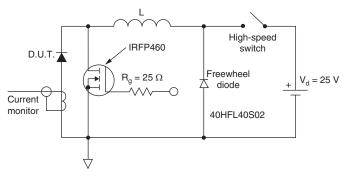


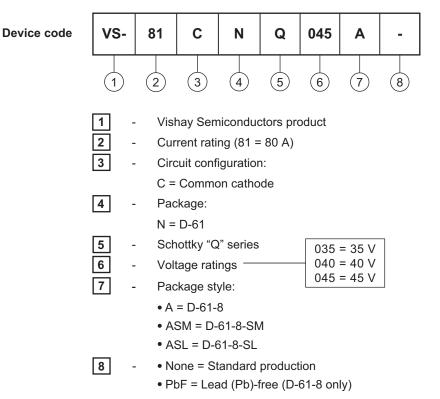
Fig. 8 - Unclamped Inductive Test Circuit

VS-81CNQ035A, VS-81CNQ040A, VS-81CNQ045A

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Vishay Semiconductors Schottky Rectifier New Generation 3 D-61 Package, 2 x 40 A

ORDERING INFORMATION TABLE



Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95354</u>				
Part marking information	www.vishay.com/doc?95356			



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