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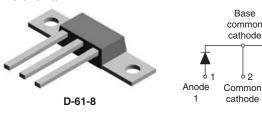


#### VS-81CNQ035A, VS-81CNQ040A, VS-81CNQ045A

Vishay Semiconductors

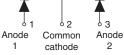
#### Schottky Rectifier New Generation 3 D-61 Package, 2 x 40 A

VS-81CNQ...A



VS-81CNQ...ASM





3

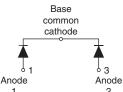
Anode

2

D-61-8-SM

VS-81CNQ...ASL





PRODUCT SUMMARY				
I <sub>F(AV)</sub>	2 x 40 A			
V <sub>R</sub>	35 V to 45 V			

#### FEATURES

- 175 °C T<sub>J</sub> operation
- Center tap module
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- New fully transfer-mold low profile, small footprint, high current package
- Designed and qualified for industrial level

#### DESCRIPTION

The center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	80	A		
V <sub>RRM</sub>		35 to 45	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	4600	A		
V <sub>F</sub>	40 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.54	V		
TJ	Range	- 55 to 175	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-81CNQ035A	VS-81CNQ040A	VS-81CNQ045A	UNITS
Maximum DC reverse voltage	V <sub>R</sub>	35	40	45	V
Maximum working peak reverse voltage	V <sub>RWM</sub>		40	40	v

# **VS-81CNQ035A, VS-81CNQ040A, VS-81CNQ045A**

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS VALUES UN		UNITS	
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	$T_{\rm AV}$ 50 % duty cycle at T <sub>C</sub> = 141 °C, rectangular waveform 80			
Maximum peak one cycle non-repetitive surge current per leg		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	4600	А
See fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	rated $V_{RRM}$ applied	790	
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 8 A, L = 1.7 mH 54 m.		mJ	
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s8Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical8		А	

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	_ TEST CONDITIONS VALUES U		UNITS	
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	40 A	- T <sub>J</sub> = 25 °C	0.60	V
		80 A		0.74	
		40 A	- T <sub>J</sub> = 125 °C	0.54	
		80 A		0.66	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C		5	mA
See fig. 2		T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	45	
Maximum junction capacitance per leg	CT	$V_R$ = 5 $V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		2600	pF
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		5.5	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000		V/µs	

#### Note

<sup>(1)</sup> Pulse width < 300  $\mu$ s, duty cycle < 2 %

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THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C	
Maximum thermal resistation to case per leg	ance,	P	DC operation See fig. 4	0.85		
Maximum thermal resist		R <sub>thJC</sub>	DC operation	0.42	°C/W	
Typical thermal resistant case to heatsink (D-61-8		R <sub>thCS</sub>	Mounting surface, smooth and greased Device flatness < 5 mils	0.30		
Approvimente weight				7.8	g	
Approximate weight				0.28	oz.	
Mounting torque	minimum			40 (35)	kgf · cm	
(D-61-8 only)	maximum			58 (50)	(lbf · in)	
				81CN0	Q035A	
Marking device			Case style D-61-8	81CNQ040A		
				81CN0	81CNQ045A	
				81CNQ035ASM		
			Case style D-61-8-SM	81CNQ0	81CNQ040ASM	
				81CNQ	81CNQ045ASM	
				81CNQ035ASL		
			Case style D-61-8-SL	81CNQ040ASL		
				81CNQ045ASL		

# **VS-81CNQ035A, VS-81CNQ040A, VS-81CNQ045A**

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Schottky Rectifier New Generation 3 D-61 Package, 2 x 40 A



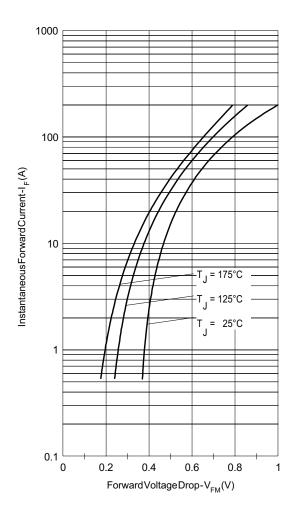


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

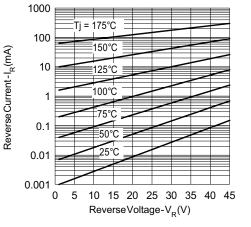


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

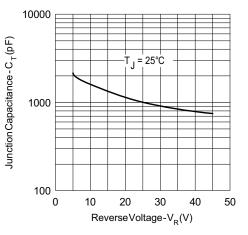
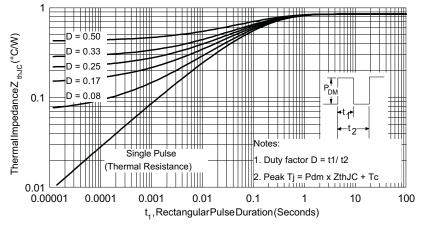


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





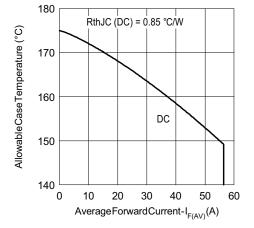
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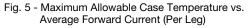
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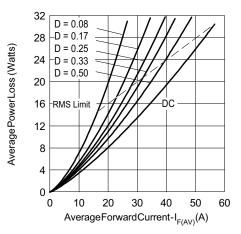
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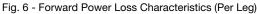
Schottky Rectifier New Generation 3 D-61 Package, 2 x 40 A

**Vishay Semiconductors** 









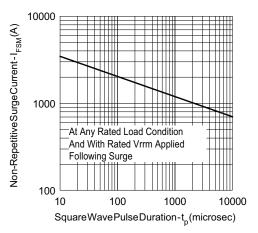


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

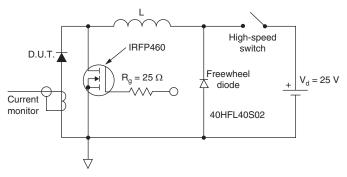


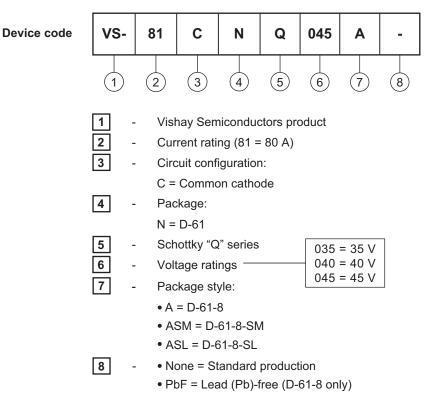
Fig. 8 - Unclamped Inductive Test Circuit

# **VS-81CNQ035A, VS-81CNQ040A, VS-81CNQ045A**

VISHAY.

Vishay Semiconductors Schottky Rectifier New Generation 3 D-61 Package, 2 x 40 A

#### ORDERING INFORMATION TABLE



Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95354</u>				
Part marking information	www.vishay.com/doc?95356			



Vishay

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