Atmel AVR32930: UC3-A3 Xplained Getting Started Guide

Features

- Easy to reprogram with just a USB cable and a preprogrammed boot loader
- Easy to debug code with JTAG-based debuggers and emulators

1 Introduction

The Atmel[®] UC3-A3 Xplained evaluation kit is a hardware platform for evaluating the Atmel AT32UC3A3256 microcontroller.

The kit offers a range of features that enable Atmel AVR[®] UC3 users to get started using UC3 peripherals right away and understand how to integrate the UC3 device in their own design. This getting started guide will take you through all the basic steps needed to start using the UC3-A3 Xplained evaluation kit.

Figure 1-1. UC3-A3 Xplained evaluation kit.





32-bit Atmel Microcontrollers

Application Note

Rev. 32163A-AVR-03/11





2 What is needed to get started?

To be able to run the preprogrammed code, you need the Atmel UC3-A3 Xplained evaluation kit and a USB cable (Standard-A to Mini-B or Mini-AB) to connect the board to a PC.

When connecting the UC3-A3 Xplained board to a PC, the operating system will request a driver file for installing the serial communication driver. This driver file is available in the zip file associated with this document. The driver file supports both the 32- and the 64-bit versions of Windows[®] XP and Windows 7. Driver installs are not necessary on Linux[®] operating systems.

To modify the example code or to write new code and compile it, you need a toolchain for the Atmel AVR microcontrollers and an integrated development environment (IDE) to edit and debug the code. Atmel provides both Atmel AVR Studio[®] 4 and Atmel AVR32 GNU Toolchain. Other IDEs and toolchains are also available, such as IAR Embedded Workbench[®].

If you also want to debug the code at runtime, debuggers like the Atmel AVR JTAGICE mkll or the Atmel AVR ONE! are needed. More information about these tools is available from the Atmel website.

2.1 Documentation for the UC3-A3 Xplained

The "AVR32918: UC3-A3 Xplained Hardware User's Guide" package includes all indepth information about the kit, such as schematics, BOM, assembly drawings, 3D plots, and more. The package is available from the Atmel website.

www.atmel.com/xplained

2.2 Atmel AVR Software Framework (ASF)

The Atmel AVR Software Framework provides software drivers and libraries to build applications for Atmel AVR devices. It has been designed to help develop and merge the different components of a software design. It can easily integrate into an operating system (OS) or run as a standalone product.

The ASF (version 1.7.0) that integrates with Atmel AVR32 Studio can be found here:

http://atmel.com/dyn/products/tools card.asp?tool id=4192

However, the above framework does not include the latest examples for this kit. The new AVR Software Framework (version 2+) should be used to get the latest software updates, and it can be found here:

http://asf.atmel.no/readme.html

2.3 Integrated development environment for Atmel AVR 32-bit microcontrollers

2.3.1 AVR32 Studio

The AVR32 Studio is a free integrated development environment (IDE) for developing applications for the Atmel AVR 32-bit microcontrollers. It offers an advanced editor as well as programming and debugging support.

Atmel AVR32 Studio

http://www.atmel.com/dyn/products/tools_card.asp?tool_id=4116



To learn how to use this tool, please read the document:

AVR32015: AVR32 Studio getting started

http://atmel.com/dyn/resources/prod_documents/doc32086.pdf

2.3.2 IAR Embedded Workbench for Atmel AVR32

IAR Embedded Workbench is an integrated development environment with project management tools and editor.

IAR Embedded Workbench for Atmel AVR32

http://www.iar.com/website1/1.0.1.0/124/1/

2.4 FLIP (BatchISP)

BatchISP is a command line tool for programming the flash memory and EEPROM of the board controller, and is part of the FLIP installation. It can be used to communicate with the preprogrammed boot loader on the board controller.

FLIP (FLexible In-system Programmer) for programming: <u>http://atmel.com/dyn/products/tools_card.asp?tool_id=3886</u>

2.5 Toolchain

In order to build an application from the source code, a toolchain, which includes the assembler, compiler, linker, and other required tools, is needed. The code for this kit is compatible with the AVR32 GNU Toolchain and the IAR[™] AVR Toolchain.

2.5.1 Atmel AVR32 GNU Toolchain

The AVR32 GNU Toolchain is a free toolchain that can be downloaded from the Atmel website. It includes all the necessary tools, such as C/C++ compiler, assembler and linker, for AVR application development. The toolchain is included in the Atmel AVR32 Studio installation, but it is also available as a separate installation. The toolchain is also available for Linux. For more information about the supported distributions, please see the release notes on the Atmel website.

AVR32 GNU toolchain (free C/C++ compiler for AVR 8- and 32-bit MCUs) http://www.atmel.com/dyn/products/tools_card.asp?tool_id=4118

2.5.2 IAR AVR32 Toolchain

The IAR AVR32 toolchain is a commercial toolchain from IAR that includes all the necessary tools, such as C/C++ compiler, assembler, and linker, for AVR application development. A 30-day evaluation version as well as a 32K (code size limited) kick-start version are available from the IAR website.

IAR Embedded Workbench for Atmel AVR32

http://www.iar.com/website1/1.0.1.0/124/1/

2.6 Programmers and debuggers

Programmers and debuggers are needed when either the application is programmed without using the boot loader, or when debugging support is needed. Several different tools are available that can be used directly with the Xplained kit. The most common tools are described in the following sections.





2.6.1 Atmel AVR Dragon

AVR Dragon[™] is a low-cost debug and programming tool that can be used for code development with the UC3-A3 Xplained kit.

Atmel AVR Dragon (on-chip programming and debugging tool) http://atmel.com/dyn/products/tools_card.asp?tool_id=3891

2.6.2 Atmel AVR JTAGICE mkll

AVR JTAGICE mkll is a mid-range debug and programming tool that can be used for code development with the UC3-A3 Xplained kit.

Atmel AVR JTAGICE mkll (on-chip programming and debugging tool) http://www.atmel.com/dyn/products/tools_card.asp?tool_id=3353

2.6.3 Atmel AVR ONE!

AVR ONE! is a high-end debug and programming tool that can be used for code development with the UC3-A3 Xplained kit.

Atmel AVR ONE! (on-chip programming and debugging tool) http://www.atmel.com/dyn/products/tools_card.asp?tool_id=4279

3 Board overview

Figure 3-1 shows the features on the UC3- A3 Xplained board. For more information on the Atmel UC3-A3 Xplained board, please see the Atmel application note, AVR32918: UC3-A3 Xplained Hardware User's Guide.



Figure 3-1. Overview of the UC3-A3 Xplained board.





4 Connecting the board

Connect a USB cable (Standard-A to Mini-B or Mini-AB) between the board and a PC or a USB power supply to power it up. That is all that is needed. When power is applied, the Power/Status LED will light up green.

WARNING Do not power the board without having the jumper or an ammeter mounted on the power measurement header. Otherwise, the device may be damaged. The power measurement header is located next to the USB connector, as shown in Figure 3-1.

4.1 Connecting the Atmel AVR Dragon

A 10-pin header cable is needed to connect the AVR Dragon tool to the Atmel UC3-A3 Xplained board. Connect the cable between the JTAG connector on the AVR Dragon kit and the UC3-A3 Xplained JTAG connector. Take a look at Figure 4-1 to see how the connection should be made.



Figure 4-1. Connecting the AVR Dragon tool to the UC3-A3 Xplained board.

4.2 Connecting the Atmel AVR JTAGICE mkll

The grey connector on the AVR JTAGICE mkll probe has to be used when connecting to the UC3-A3 Xplained board. See Figure 4-2 for reference on how to make the connection. The notch of the grey connector must be placed into the cut-out of the board.

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Figure 4-2. Connecting the Atmel AVR JTAGICE mkII to the UC3-A3 Xplained board.

4.3 Connecting the Atmel AVR ONE!

The AVR ONE! JTAG connector can be connected to the UC3-A3 Xplained kit with a standoff adapter, as shown in Figure 4-3.









5 Programming the kit

5.1 Programming via the boot loader

The boot loader is evoked by pushing the physical push button (SW0) during powerup; that is, push the button, and then connect a USB cable to the kit. Programming can be performed by using the BatchISP command line tool, which is part of the FLIP installation. BatchISP can also run under Atmel AVR32 Studio, which has a GUI interface.

Once the boot loader is active, it will appear as a USB DFU class device, and Windows will ask for a driver. The driver can be found under the FLIP installation directory.

🥔 A	VR Targets 🗙	_	🥔 🗆 🗖
	Name	Adapter	Board
72	AVR Dragon	AVR Dragon	Unspecif
.	AVR ONE!	AVR ONE!	Unspecif
~	AVR32 Simulator	AVR32 Simulator	AVR32 S
	JTAGICE mkII	JTAGICE mkII	Unspecif
示	UC3A3 DFU	USB DFU	Unspecif
<			>

Figure 5-1. The AVR32 Studio AVR Targets menu.

To program a new firmware, right-click on the UC3A3 DFU target in the AVR Targets section of AVR32 Studio (see Figure 5-1), and then select "Program..." from the menu (see Figure 5-2).

Figure 5-2. The DFU boot loader menu.

<	VR Targe	 Program Read Verify Erase Read fuses Program fuses 		> *
	Name AVR Dra AVR ON AVR32 :	Set as Default Remove Properties	Delete Enter	Board Unspecified Unspecified AVR32 Simulator
UC3	JTAGIC A3 DFU	Scan Targets DFU USB DFI	U	

The following window will appear on the screen:

Figure 5-3. The DFU boot loader file select and programming options.

😇 Progr	am target	×
UC3A3 [Program	DFU target	AVR32
File path:	D:\Temp\uc3-a3_xplained.he	x Browse
Offset:	0x0	
Length:		🗹 Entire file
Options Verify Unloc Start	r memory after programming k flash before erasing executing after programming	Erase flash before programming Reset MCU after programming
Ø		OK Cancel

Browse and select the .hex file, set up the options as shown here, and click OK. A small progress bar will appear in the lower right corner to indicate that programming is ongoing. When the progress bar stops and disappears, the programming is done, and the application will start automatically.

5.2 Restoring the boot loader

If the boot loader has been overwritten or erased, for example by performing a chip erase command, the boot loader can be reprogrammed by using one of the programmer tools described in Section 2.6, Programmers and debuggers.

5.2.1 Reprogramming the boot loader via Atmel AVR32 Studio

To reprogram the boot loader, right-click on the target to use in the AVR32 Studio AVR Targets window, as shown in Figure 5-4, and select "Program Bootloader" from the context menu.





Figure 5-4. The AVR32 Studio AVR Targets menu.

		Program B	ootloader			
		놀 Program				
	<	💰 Read				>
	Register	📥 Verify				Ť
	riogistor	🗳 Erase				
		nter and Chip Erase				
		Read Gene	ral Register:	s		
		Secure Fla	sh			
		Halt				
		Reset	Reset			
		Resume				
		Resume Fr	om		•	
		Read Fuse	s			
		Program Fi	Program Fuses		_	
Read MCU Info			_			
		Upgrade Firmware				
	<	Debug			•	>
	📣 AVR T	Run			•	
	Na	Set as Def	ault		pard	
	AVI	Remove		Delete	specified	
	avi 🥌	Properties		Enter	ispecified	
	🧢 AVI	Scan Targe	ets		'R32 Simulati	or
	ATC 🛄	GICE IIKII	JIAGICE II	1841	unspecified	
	T UC	3A3 DFU	USB DFU		Unspecified	
	1					1
						1

Select the boot loader version to program, as shown in Figure 5-5. Please see the document, AVR32784: AVR32 UC3 USB DFU Bootloader, for more information about the available versions and possible settings for the boot loader.

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Figure 5-5. The DFU boot loader version selection menu.

AT32	2UC3 Bootloader wizard	
AT32UC Please s	C3 Bootloader selection page. select the bootloader.	
Part:	UC3A3256	
Type:	USB DFU	~
Version:	1.0.3	*
?	< <u>B</u> ack <u>N</u> ext > Einish	Cancel

Set up all the specifications for the boot loader, as shown in Figure 5-6, and select finish to complete the boot loader programming.

Figure 5-6. The DFU boot loader options setting menu selections.

AT32UC3 Bootloade	r wizard		- 🗆 🛛
User Page and Fuse Co Please setup your bootload	onfigurations er configuration		
User Page Configuration	33 🗢 🗌 ord 0x929E21A	From GPIO0 to GPIO10 GPIO Low Level F	9
- Fuse Bit Configuration ISP_BOD_EN ISP_IO_COND_EN ISP_FORCE FuseBitConfiguration Value	✓ ✓ ✓ OxFFF7FFFF		
Use this link to get : <u>AVR32 US</u>	38 DFU bootloader -	documentation	
? < <u>s</u>	ack Next >	Einish	Cancel





6 Example applications

Several examples are available in ASF that can be downloaded from the Atmel website ASF section: http://asf.atmel.no. Each example has the full source code with documentation, as well as ready-made binaries for getting started quickly. The examples can be compiled using either the GCC or IAR compilers.

6.1 Preprogrammed firmware

The Atmel AT32UC3A3256 that is mounted on the Atmel UC3-A3 Xplained board is preprogrammed with a USB boot loader and default demonstration firmware.

The preprogrammed firmware in the AT32UC3A3256 is a demonstration of the Atmel QTouch[®] button and the Atmel QTouch slider. Touching the SW1 QTouch button will cause LED1 to light up. Moving a finger along the QTouch slider will cause either LED2 or LED3 to light up, depending on the position of the finger.

The delay at the start of the application is due to an SDRAM check, which is performed each time the kit is powered up. This has been removed in the updated firmware, described in Section 6.2.

6.2 Updated default firmware

This application note includes a software package that contains the source code of a demo application. The demo application demonstrates the QTouch functionality of the UC3-A3 Xplained board similar to the pre-programmed default firmware but with two differences:

- 1) When powered up, the board will not perform the SDRAM check any more since this has been removed from the application.
- 2) If the slider is touched, the intensity of LED3 will be adjusted according to the touch position on the slider, and LED2 will light up when the slider is touched.

7 EVALUATION BOARD/KIT IMPORTANT NOTICE

This evaluation board/kit is intended for use for **FURTHER ENGINEERING**, **DEVELOPMENT**, **DEMONSTRATION**, **OR EVALUATION PURPOSES ONLY**. It is not a finished product, and may not (yet) comply with some or any technical or legal requirements that are applicable to finished products, including, without limitation, directives regarding electromagnetic compatibility, recycling (WEEE), FCC, CE, or UL (except as may be otherwise noted on the board/kit). Atmel supplied this board/kit "AS IS," without any warranties, with all faults, at the buyer's and further users' sole risk. The user assumes all responsibility and liability for proper and safe handling of the goods. Further, the user indemnifies Atmel from all claims arising from the handling or use of the goods. Due to the open construction of the product, it is the user's responsibility to take any and all appropriate precautions with regard to electrostatic discharge and any other technical or legal concerns.

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