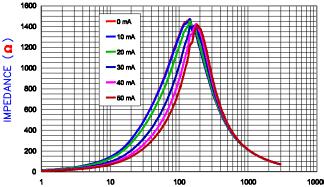
# HZ0402A152R-10

## UNCONTROLLED DOCUMENT

### PHYSICAL DIMENSIONS:

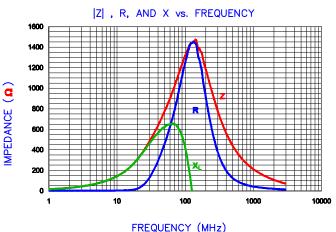
A 1.00 [.040] ± 0.15 [.006] B 0.50 [.020] ± 0.15 [.006] C 0.50 [.020] ± 0.15 [.006] D 0.30 [.012] MAX.

> Z vs FREQUENCY IMPEDANCE UNDER DC BIAS





FREQUENCY (MHz)

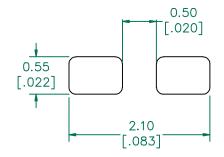


RO 1/5 2002/95/EC

Nominal
Minimum
Maximum

NOTES: UN
1. TAPED A
SPECIFIC

#### LAND PATTERNS FOR REFLOW SOLDERING



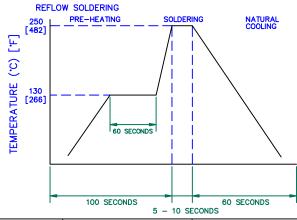
(For wave soldering, add 0.763 (.030 to this dimension.)

ELECTRICAL CHARACTERISTICS:									
Z @ 100MHz ( <b>Ω</b> )		DCR ( \( \Omega\)	Rated Current						
Nominal	1500								
Minimum	1125								
Maximum	1875	2.00	50 mA						

#### NOTES: UNLESS OTHERWISE SPECIFIED

- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 10,000 PCS/REEL, PAPER TAPE.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.

#### RECOMMENDED SOLDERING CONDITIONS



	5 - 10 SECONDS										
DIMENSIONS ARE IN mm [INCHES].			This print is the property of Lairo								
				Tech, and is loaned in confidence subject to return upon request a	.d =	■ ■ <b>.</b> .					
				with the understanding that no							
				copies shall be made without the written consent of Laird Tech. All	=	TECHNOLOGIES'					
				rights to design or invention are							
				reserved.							
				PROJECT/PART NUMBER:	REV	PART TO	PE:	DRAWN BY:			
				HZ0402A152R-10	l B	co-	-FIRE JRK				
В	UPDATE COMPANY LOGO	02/15/08	JRK	DATE: 07/24/07 S	ALE: N	E: NTS SHEET:					
Α	ORIGINAL DRAFT	07/24/07	JRK	GAD #	IOL #		2	of 2			
REV	DESCRIPTION	DATE	INT	CAD # HZ0402A152R-10-B	•	-	-				

AGILENT E4991A RF Impedance/Material Analyzer
HP 16194A Test Fixture. TEST REF. 6231