

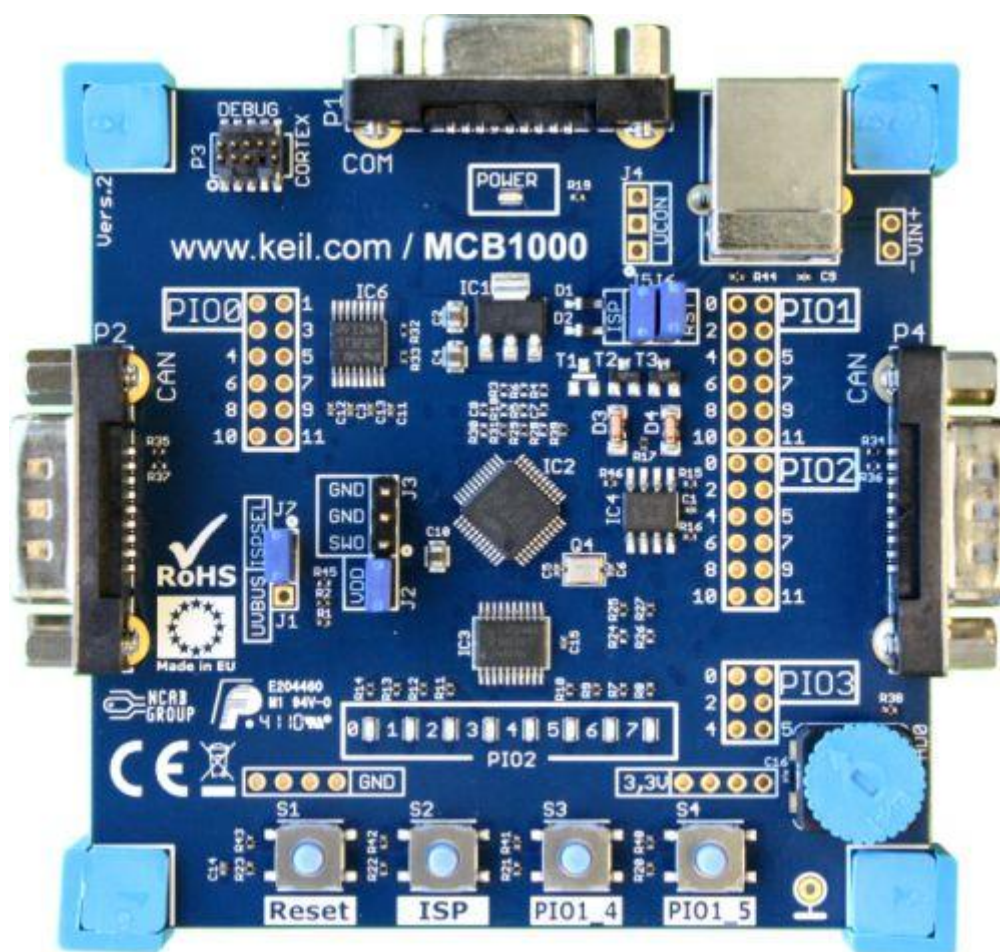
Introduction

[Home](#) » Introduction

The Keil **MCB1000 Evaluation Board** allows you to generate and test application programs for the NXP LPC11xx and NXP LPC13xx device families. With this hands-on process, you can determine the hardware and software requirements for current and future product development.

This board is available in five configurations: the **MCB11U10**, the **MCB1114**, the **MCB11C14**, the **MCB1313** and the **MCB1343**.

- The **MCB11U10** is populated with the [NXP LPC11U14/201](#) Cortex-M0 device (See 2nd note below).
- The **MCB1114** is populated with the [NXP LPC1114FBD48/302](#) Cortex-M0 device.
- The **MCB11C14** is populated with the [NXP LPC11C14FB48/301](#) Cortex-M0 device with CAN support.
- The **MCB1313** is populated with the [NXP LPC1313FBD48](#) Cortex-M3 device.
- The **MCB1343** is populated with the [NXP LPC1343FBD48](#) Cortex-M3 device with USB support.



Note

- Except as noted, this manual uses **MCB1000** as a generic term to refer to the **MCB11U10**, the **MCB1114**, the **MCB11C14**, the **MCB1313** or the **MCB1343** board.
- The MCB11U10 PCB is physically identical to the MCB1114 PCB but the silkscreen information is different, because the port names and numbers used on the device are located on different pins for this device. The port/pin differences are noted on those pages that reference pinouts and are given in detail in the schematic.