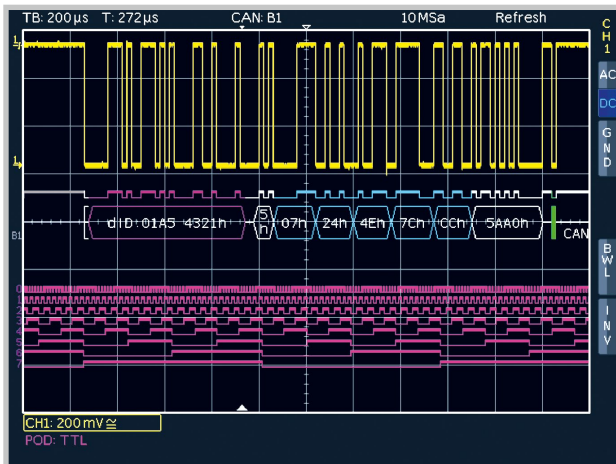
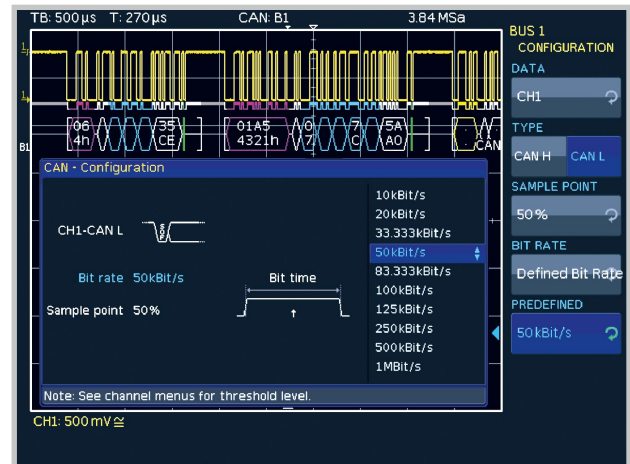


## H0012 CAN/LIN Bus Analysis

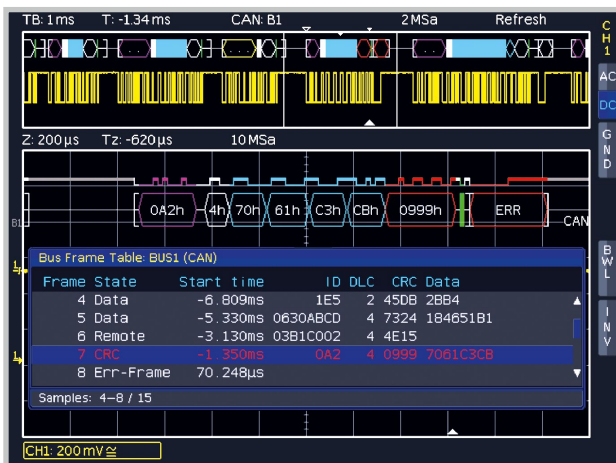
for all Oscilloscopes of the HMO Series



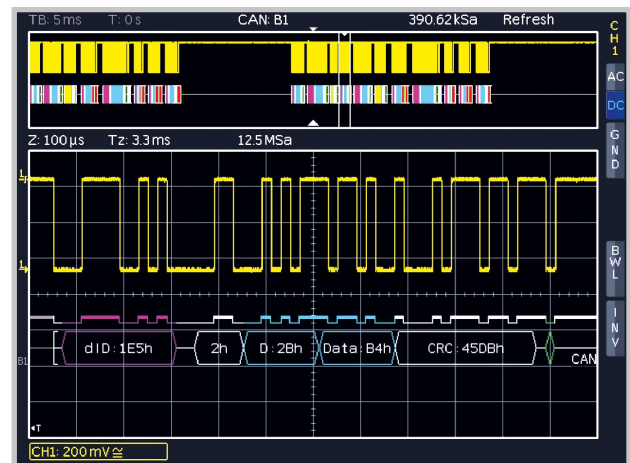
Mixed Signal and Bus Display



CAN Bus Configuration



CAN Bus list display



CAN Bus HEX

- ✓ CAN, LIN Bus Trigger and Decode
- ✓ Hardware accelerated Decode in Real Time
- ✓ Color Coded Display of the Content for intuitive Analysis and easy Overview
- ✓ More Details of the decoded Values come visible with increasing Zoom Factor
- ✓ Bus and List Display with synchronous Display of the Data
- ✓ Decode into ASCII, Binary, Hexadecimal or Decimal Format
- ✓ Up to four Lines to show the decoded Values
- ✓ Powerful Trigger to isolate specific Messages
- ✓ Option for all Oscilloscopes of the HMO Series, retrofittable

H0012

# H0012 CAN/LIN Bus Analysis

CAN Bus		LIN Bus
<b>Bus Configuration</b>		
<b>Bit rates</b>	Pre-Defined or User-Select, 100 Bit/s...4 Mb/s (HM0352x/2524), 100 Bit/s...2 Mb/s (HM072x...202x)	Pre-Defined or User-Select, 100 Bit/s...4 Mb/s (HMO352x/2524), 100 Bit/s...2 Mb/s (HMO72x...202x)
<b>Signal Type</b>	CAN-L or CAN-H, Single Ended or Differential Probe (Analog Channels only)	n/a
<b>Sample Point Range</b>	25...90%	n/a
<b>Threshold</b>	Pre-Defined or User-Select	Pre-Defined or User-Select
<b>Polarity</b>	n/a	High or Low Active
<b>Protocol Version</b>	n/a	1.x, 2.x, J2602, 1.x or 2.x
<b>Trigger</b>		
<b>Source</b>	digital Channel LCH 0...15 [Opt. H03508], analog Channel CH 1...2 [CH 1...4]	digital Channel LCH 0...15 [Opt. H03508], analog Channel CH 1...2 [CH 1...4]
<b>Event</b>	Start of Frame (SOF), End of Frame (EOF) Error Frame Error condition: Stuff Bit Error, CRC Error, Not Acknowledge, Form Error Overload Frame Data Frame (11 or 29 Bit ID) Remote Frame (11 or 29 Bit ID) Identifier: 0, 1, X (Don't Care) Pattern, Trigger when =, ≠, <, > Identifier and Data: ID and 64 Bit data pattern (0, 1, X), trigger when =, ≠, <, >	Start of Frame (SOF), Wake Up Frame Error Frame Error condition: Checksum Error, Parity Error Synchronisation Error Identifier: 0, 1, X (Don't Care) Pattern, Trigger when =, ≠, <, > Identifier and Data: ID and 64 Bit data pattern (0, 1, X), trigger when =, ≠, <, >
<b>Input format</b>	Hexadecimal or Binary	Hexadecimal or Binary
<b>Hardware accelerated Decode</b>		
<b>Source</b>	digital Channel LCH 0...15 [Opt. H03508], analog Channel CH 1...2 [CH 1...4]	digital Channel LCH 0...15 [Opt. H03508], analog Channel CH 1...2 [CH 1...4]
<b>Display Bus</b>	<b>color coded for</b>  Start and End of Frame: White brackets Data ID: Magenta, Remote ID: Yellow DLC: White, Data: Cyan, CRC: White ACK: Green, Overload: White, Error: Red  <b>up to four lines for decoded values, synchronous display of the Bit lines</b>	<b>color coded for</b>  Start and End of Frame: White brackets Break: Magenta, Synchronisation: White Identifier: Yellow, Parity: Green, Data: Cyan Checksum: White, Error: Red, Wake Up: Magenta  <b>up to four lines for decoded values, synchronous display of the Bit lines</b>
<b>Table</b>	<b>Display of Bus 0 or 1</b>  Frame Number State (Frame Type or Error Description) Start Time, Identifier, DLC, CRC, Data	<b>Display of Bus 0 or 1</b>  Frame Number State (Frame Type or Error Description) Start Time, Identifier, Length, Checksum, Data
<b>Format</b>	Identifier & other: hexadecimal Data: ASCII, binary, decimal, hexadecimal	Identifier & other: hexadecimal Data & Checksum: ASCII, binary, decimal, hexadecimal