

**Product data sheet** 

### 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a leadless medium power DFN2020MD-6 (SOT1220) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

### 2. Features and benefits

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- Trench MOSFET technology
- Very fast switching
- Small and leadless ultra thin SMD plastic package: 2 x 2 x 0.65 mm
- Exposed drain pad for excellent thermal conduction
- Tin-plated 100 % solderable side pads for optical solder inspection

### 3. Applications

- Charging switch for portable devices
- DC-to-DC converters
- Power management in battery-driven portables
- Hard disk and computing power management

## 4. Quick reference data

Table 1. Quid	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	30	V
V <sub>GS</sub>	gate-source voltage			-20	-	20	V
I <sub>D</sub>	drain current	$V_{GS}$ = 10 V; $T_{amb}$ = 25 °C; t ≤ 5 s	[1]	-	-	10.4	А
Static characteristics							
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 7 A; T <sub>j</sub> = 25 °C		-	16.5	19.5	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.





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# 5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	D	drain		D
2	D	drain		
3	G	gate		G
4	S	source		\$ 017aaa253
5	D	drain	Transparent top view	077442200
6	D	drain	DFN2020MD-6 (SOT1220)	
7	D	drain		
8	S	source		

# 6. Ordering information

Table 3. Ordering information							
Type number	Package	age					
	Name	Description	Version				
PMPB20EN	DFN2020MD-6	DFN2020MD-6: plastic thermal enhanced ultra thin small outline package; no leads; 6 terminals	SOT1220				

# 7. Marking

Table 4.   Marking codes	
Type number	Marking code
PMPB20EN	1B

**30 V N-channel Trench MOSFET** 

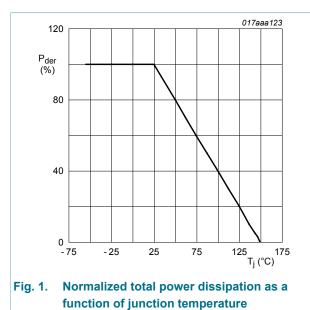
### 8. Limiting values

#### Table 5.Limiting values

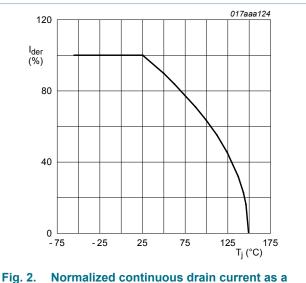
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	30	V
V <sub>GS</sub>	gate-source voltage			-20	20	V
I <sub>D</sub>	drain current	$V_{GS}$ = 10 V; $T_{amb}$ = 25 °C; t ≤ 5 s	[1]	-	10.4	Α
		V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 25 °C	[1]	-	7.2	А
		V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 100 °C	[1]	-	4.6	А
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	30	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[1]	-	1.7	W
		T <sub>amb</sub> = 25 °C; t ≤ 5 s	[1]	-	3.5	W
		T <sub>sp</sub> = 25 °C		-	12.5	W
Tj	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-dra	in diode		1	1		
l <sub>S</sub>	source current	T <sub>amb</sub> = 25 °C	[1]	-	2.2	А

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.



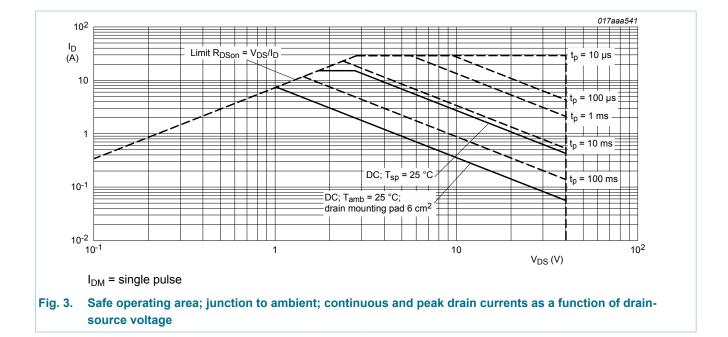
$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$



function of junction temperature

$$I_{der} = \frac{I_D}{I_{D(25^\circ C)}} \times 100 \%$$

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### 9. Thermal characteristics

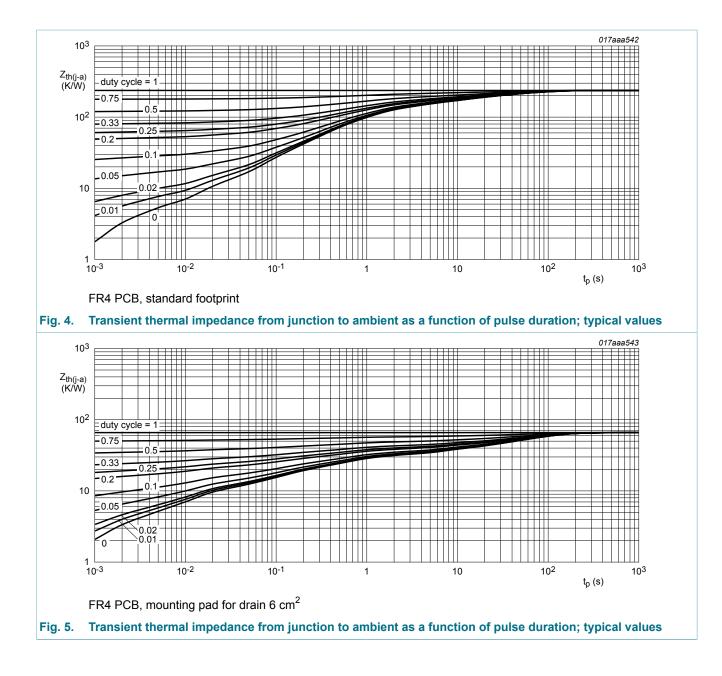
Table 6.         Thermal characteristics								
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit	
from	thermal resistance	in free air	[1]	-	235	270	K/W	
	from junction to ambient		[2]	-	67	74	K/W	
	ampient		[3]	-	33	36	K/W	
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	5	10	K/W	

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>, t  $\leq$  5 s

### 30 V N-channel Trench MOSFET



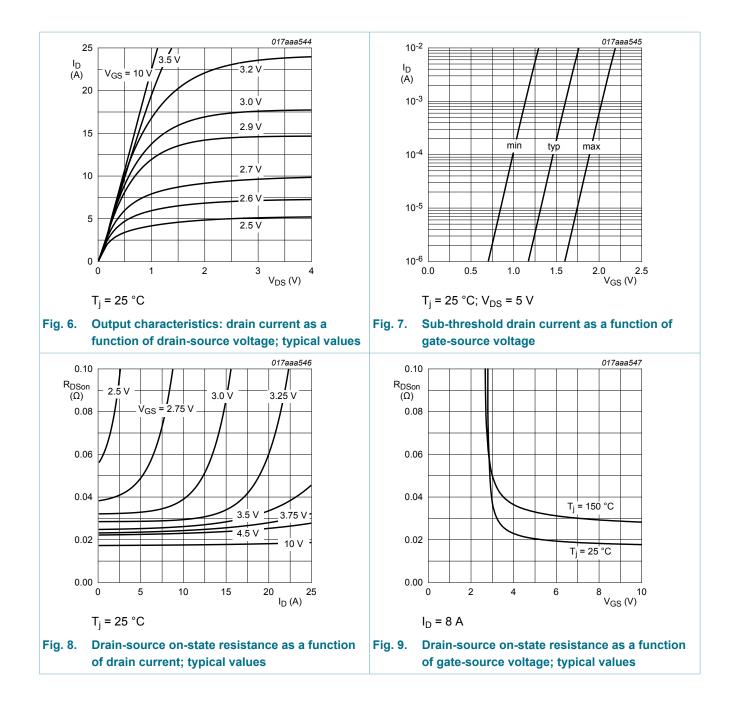
**30 V N-channel Trench MOSFET** 

## **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	octeristics	1				
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	30	-	-	V
V <sub>GSth</sub>	gate-source threshold voltage	$I_{D}$ = 250 µA; $V_{DS}$ = $V_{GS}$ ; $T_{j}$ = 25 °C	1	1.5	2	V
I <sub>DSS</sub>	drain leakage current	$V_{DS}$ = 30 V; $V_{GS}$ = 0 V; $T_j$ = 25 °C	-	-	1	μA
		$V_{DS}$ = 30 V; $V_{GS}$ = 0 V; $T_j$ = 150 °C	-	-	20	μA
I <sub>GSS</sub>	gate leakage current	$V_{GS}$ = 20 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	100	nA
		$V_{GS}$ = -20 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	100	nA
R <sub>DSon</sub> drain-source on-st resistance	drain-source on-state	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 7 A; T <sub>j</sub> = 25 °C	-	16.5	19.5	mΩ
	resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 7 A; T <sub>j</sub> = 150 °C	-	27	32	mΩ
		V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 7 A; T <sub>j</sub> = 25 °C	-	20.5	24.5	mΩ
9 <sub>fs</sub>	forward transconductance	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 7 A; T <sub>j</sub> = 25 °C	-	8	-	S
R <sub>G</sub>	gate resistance	f = 1 MHz	-	1.7	-	Ω
Dynamic ch	aracteristics	· · · ·				
Q <sub>G(tot)</sub>	total gate charge	$V_{DS}$ = 15 V; I <sub>D</sub> = 5 A; $V_{GS}$ = 10 V;	-	7.2	10.8	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C	-	1	-	nC
Q <sub>GD</sub>	gate-drain charge		-	0.67	-	nC
C <sub>iss</sub>	input capacitance	$V_{DS}$ = 15 V; f = 1 MHz; $V_{GS}$ = 0 V;	-	435	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	90	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	35	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 15 V; I <sub>D</sub> = 5 A; $V_{GS}$ = 4.5 V;	-	9	-	ns
t <sub>r</sub>	rise time	R <sub>G(ext)</sub> = 1.7 Ω; T <sub>j</sub> = 25 °C	-	17	-	ns
t <sub>d(off)</sub>	turn-off delay time		-	9	-	ns
t <sub>f</sub>	fall time		-	8	-	ns
Source-drai	n diode	· · · · · · · · · · · · · · · · · · ·	1			
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = 2.2 A; V <sub>GS</sub> = 0 V; T <sub>i</sub> = 25 °C	-	0.8	1.2	V

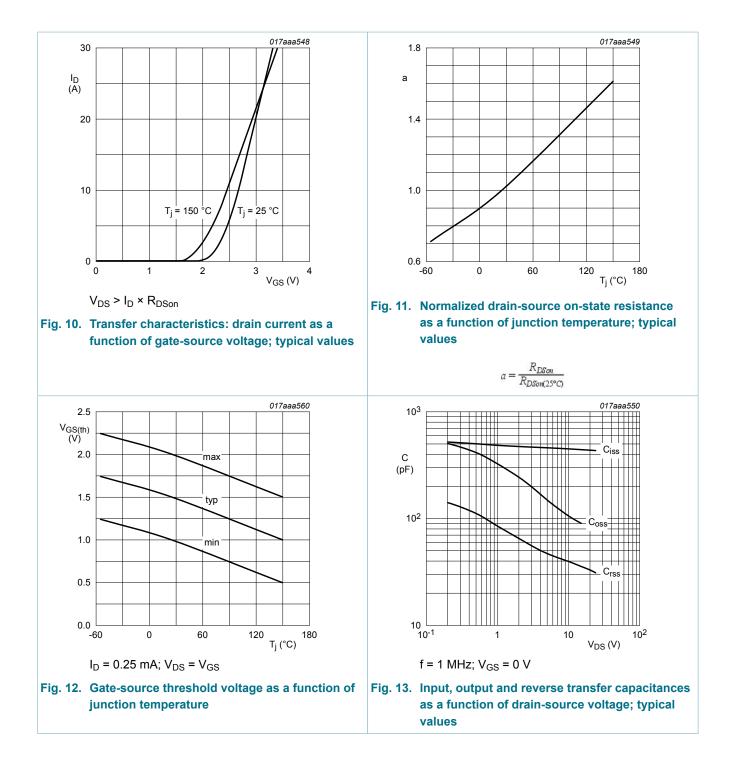
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#### 30 V N-channel Trench MOSFET



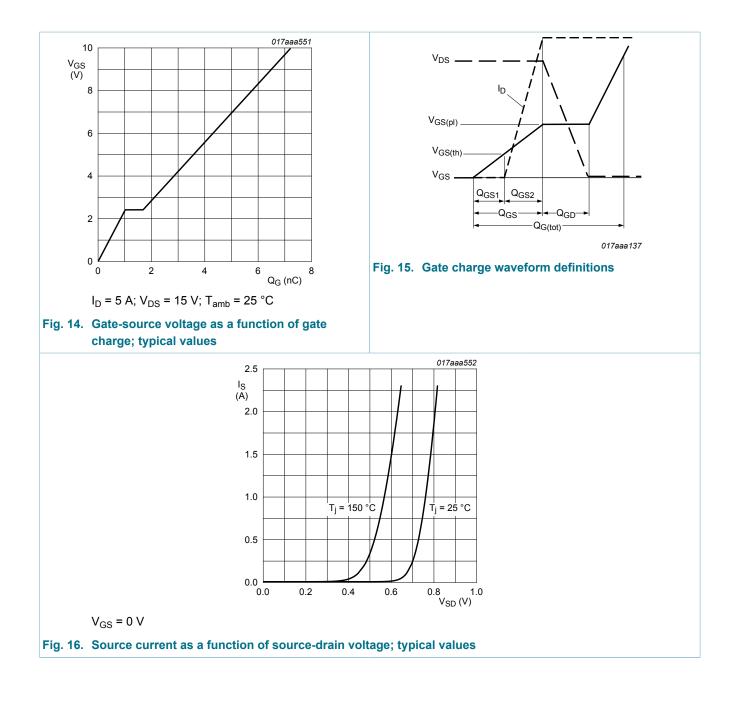
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#### 30 V N-channel Trench MOSFET



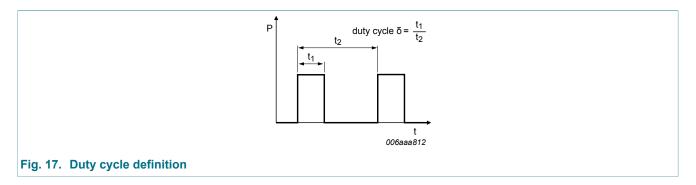
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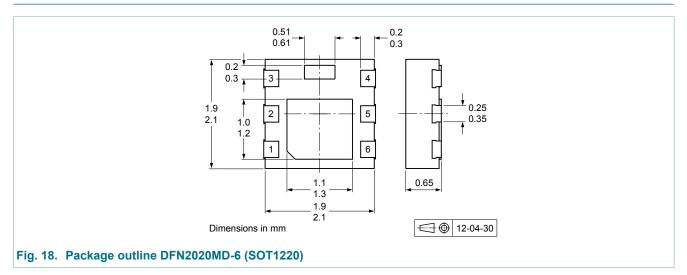


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## **11. Test information**

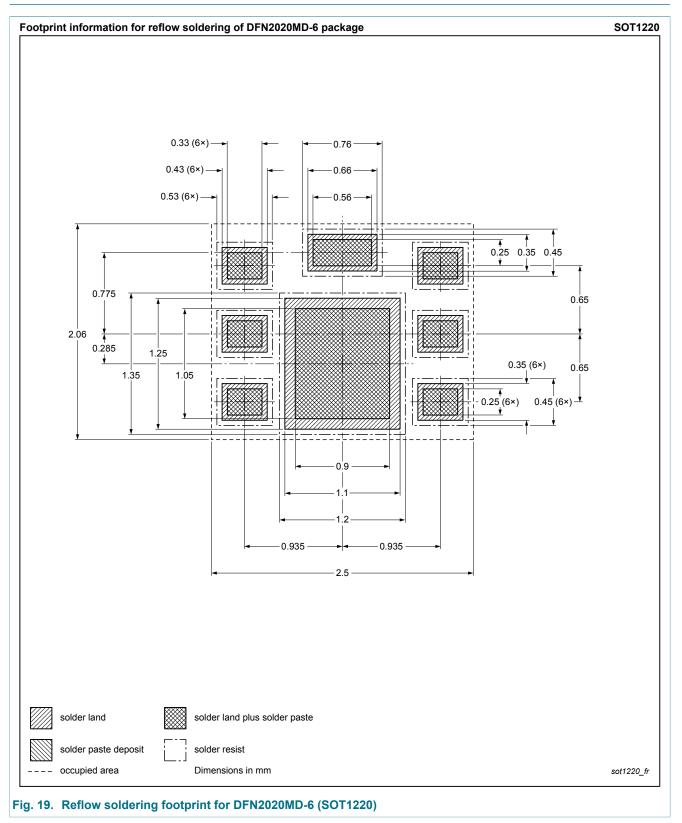


## 12. Package outline



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### 13. Soldering



# 14. Revision history

Table 8. Revision his	story						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMPB20EN v.2	20140114	Product data sheet	-	PMPB20EN v.1			
Modifications:	<ul> <li>Table 7: parameter value of V<sub>DS</sub> corrected from 10 V to 15 V in capacitance condition.</li> <li>Figure 16: V<sub>SD</sub> parameter corrected.</li> </ul>						
PMPB20EN v.1	20120516	Product data sheet	-	-			

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#### 15.1 Data sheet status

Document status [1][2]	Product status [ <u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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