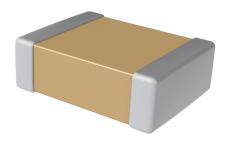


#### **Overview**

KEMET's Flexible Termination (FT-CAP) Multilayer Ceramic Capacitor in C0G dielectric incorporates a unique, flexible termination system that is integrated with KEMET's standard termination materials. A conductive silver epoxy is utilized between the base metal and nickel barrier layers of KEMET's standard termination system in order to establish pliability while maintaining terminal strength, solderability and electrical performance. This technology was developed in order to address the primary failure mode of MLCCs– flex cracks, which are typically the result of excessive tensile and shear stresses produced during board flexure and thermal cycling. Flexible termination technology inhibits the transfer of board stress to the rigid ceramic body, therefore mitigating flex cracks which can result in low IR or short circuit failures.

Although this technology does not eliminate the potential for mechanical damage that may propagate during extreme environmental and handling conditions, it does provide superior flex performance over standard termination systems. FT-CAP complements KEMET's Open Mode, Floating Electrode (FE-CAP), Floating Electrode with Flexible Termination (FF-CAP), and KEMET Power Solutions (KPS) product lines by providing a complete portfolio of flex mitigation solutions. Combined with the stability of C0G dielectric and designed to accommodate all capacitance requirements, these flex-robust devices are RoHS Compliant, offer up to 5 mm of flex-bend capability and exhibit no change in capacitance with respect to time and voltage. Capacitance change with reference to ambient temperature is limited to ±30 ppm/°C from -55°C to +125°C.

In addition to Commercial Grade, Automotive Grade devices are available which meet the demanding Automotive Electronics Council's AEC–Q200 qualification requirements.



#### **Ordering Information**

С	1206	X	563	J	3	G	Α	С	TU
Cerami	Case Size (L" x W")	Specification/ Series	Capacitance Code (pF)	Capacitance Tolerance <sup>1</sup>	Rated Voltage (VDC)	Dielectric	Failure Rate/ Design	Termination Finish <sup>2</sup>	Packaging/Grade (C-Spec) <sup>3</sup>
	0603 0805 1206 1210 1812 1825 2220 2225	X = Flexible Termination	2 significant digits + number of zeros. Use 9 for 1.0 – 9.9 pF Use 8 for 0.5 – .99 pF e.g., 2.2 pF = 229 e.g., 0.5 pF = 508	$B = \pm 0.10 \text{ pF}$ $C = \pm 0.25 \text{ pF}$ $D = \pm 0.5 \text{ pF}$ $F = \pm 1\%$ $G = \pm 2\%$ $J = \pm 5\%$ $K = \pm 10\%$ $M = \pm 20\%$	8 = 10 V 4 = 16 V 3 = 25 V 5 = 50 V 1 = 100 V 2 = 200 V A = 250 V	G = COG	A = N/A	C = 100% Matte Sn L = SnPb (5% minimum)	Blank = Bulk TU = 7" Reel Unmarked AUTO = Automotive Grade 7" Reel Unmarked

<sup>1</sup> Additional capacitance tolerance offerings may be available. Contact KEMET for details.

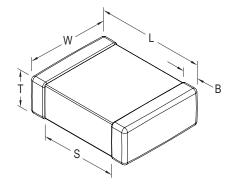
<sup>2</sup> Additional termination finish options may be available. Contact KEMET for details.

<sup>2,3</sup> SnPb termination finish option is not available on Automotive Grade product.

<sup>3</sup>Additional reeling or packaging options may be available. Contact KEMET for details.



### **Dimensions – Millimeters (Inches)**



EIA Size Code	Metric Size Code	L Length	W Width	T Thickness	B Bandwidth	S Separation Minimum	Mounting Technique
0603	1608	1.60 (.064) ±0.17 (.007)	0.80 (.032) ±0.15 (.006)		0.45 (.018) ±0.15 (.006)	0.58 (.023)	Solder Wave
0805	2012	2.00 (.079) ±0.20 (.008)	1.25 (.049) ±0.20 (.008)		0.50 (0.02) ±0.25 (.010)	0.75 (.030)	or
1206	3216	3.30 (.130) ±0.40 (.016)	1.60 (.063) ±0.20 (.008)		0.60 (.024) ±0.25 (.010)		Solder Reflow
1210	3225	3.30 (.130) ±0.40 (.016)	2.50 (.098) ±0.20 (.008)	See Table 2 for	0.60 (.024) ±0.25 (.010)		
1812	4532	4.50 (.178) ±0.40 (.016)	3.20 (.126) ±0.30 (.012)	Thickness	0.70 (.028) ±0.35 (.014)	N1/A	
1825	4564	4.60 (.181) ± 0.40 (.016)	6.40 (.252) ± 0.40 (.016)		0.70 (.028) ± 0.35 (.014)	N/A	Solder Reflow Only
2220	5650	5.90 (.232) ± 0.75 (.030)	5.00 (.197) ± 0.40 (.016)		0.70 (.028) ± 0.35 (.014)		
2225	5664	5.90 (.232) ± 0.75 (.030)	6.40 (.248) ± 0.40 (.016)		0.70 (.028) ± 0.35 (.014)		

#### **Benefits**

- -55°C to +125°C operating temperature range
- Superior flex performance (up to 5 mm)
- Lead (Pb)-Free, RoHS and REACH compliant
- EIA 0603, 0805, 1206, 1210, 1812, 1825, 2220, and 2225 case sizes
- DC voltage ratings of 10 V, 16 V, 25 V, 50 V, 100 V, 200 V and 250 V
- Capacitance offerings ranging from 0.5 pF up to 0.47  $\mu\text{F}$
- Available capacitance tolerances of  $\pm 0.10 pF, \pm 0.25 pF, \pm 0.5 pF, \pm 1\%, \pm 2\%, \pm 5\%, \pm 10\%, and \pm 20\%$
- No piezoelectric noise
- Extremely low ESR and ESL
- High thermal stability

- High ripple current capability
- Preferred capacitance solution at line frequencies and into the MHz range
- · No capacitance change with respect to applied rated DC voltage
- Negligible capacitance change with respect to temperature from -55°C to +125°C
- · Non-polar device, minimizing installation concerns
- 100% pure matte tin-plated termination finish allowing for excellent solderability
- Commercial & Automotive (AEC–Q200) Grades available
- SnPb termination finish option available upon request (5% minimum)



### **Applications**

Typical applications include critical timing, tuning, circuits requiring low loss, circuits with pulse, high current, decoupling, bypass, filtering, transient voltage suppression and blocking, as well as energy storage in critical and safety relevant circuits without (integrated) current limitation, including those subject to high levels of board flexure or temperature cycling.

#### **Qualification/Certification**

Commercial Grade products are subject to internal qualification. Details regarding test methods and conditions are referenced in Table 4, Performance & Reliability.

Automotive Grade products meet or exceed the requirements outlined by the Automotive Electronics Council. Details regarding test methods and conditions are referenced in document AEC–Q200, Stress Test Qualification for Passive Components. For additional information regarding the Automotive Electronics Council and AEC–Q200, please visit their website at www.aecouncil.com.

#### **Environmental Compliance**

Lead (Pb)-Free, RoHS, and REACH compliant without exemptions (excluding SnPb termination finish option).



## **Electrical Parameters/Characteristics**

Item	Parameters/Characteristics
Operating Temperature Range	-55°C to +125°C
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	±30 ppm/⁰C
Aging Rate (Maximum % Capacitance Loss/Decade Hour)	0%
Dielectric Withstanding Voltage (DWV)	250% of rated voltage (5 ±1 seconds and charge/discharge not exceeding 50 mA)
Dissipation Factor (DF) Maximum Limit @ 25°C	0.1%
Insulation Resistance (IR) Limit @ 25°C	1,000 megohm microfarads or 100 G $\Omega$ (Rated voltage applied for 120 ±5 seconds @ 25°C)

To obtain IR limit, divide  $M\Omega$ - $\mu$ F value by the capacitance and compare to  $G\Omega$  limit. Select the lower of the two limits.

Capacitance and dissipation factor (DF) measured under the following conditions:

1 MHz ±100 kHz and 1.0 ±0.2 Vrms if capacitance  $\leq$  1,000 pF

1 kHz  $\pm$ 50 Hz and 1.0  $\pm$ 0.2 Vrms if capacitance > 1,000 pF

Note: When measuring capacitance it is important to ensure the set voltage level is held constant. The HP4284 and Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON."



### **Post Environmental Limits**

	High Temperatu	ıre Life, Biased	Humidity, Mois	ture Resistance	)										
Dielectric	Dielectric         Rated DC         Capacitance         Dissipation Factor         Capacitance         Insulation           Voltage         Value         (Maximum %)         Shift         Resistance														
C0G	All	All	0.5	0.3% or ±0.25 pF	10% of Initial Limit										



### Table 1A – Capacitance Range/Selection Waterfall (0603 – 1206 Case Sizes)

			Ca	ase	Siz	e/S	Seri	es				С	060:	3X					C	)80	5X					С	120	6X		
	Сар			Vo	ltag	e Co	de			8	4	3	5	1	2	Α	8	4	3	5	1	2	Α	8	4	3	5	1	2	Α
Сар	Code		F	Rated	Volt	tage	(VDC	;)		10	16	25	50	100	200	250	10	16	25	50	100	200	250	\$	16	25	50	100	200	250
			Ca	apaci	tanc	e Tol	eran	се						P					ity a							s				
0.50 & 0.75 pF	508 & 758	В	С	D						СВ	CB	CB	CB	CB	CB		DC	DC	Chip DC	DC	DC	DC			ons					
1.0 - 9.1 pF*	109 - 919*	В	С	D						СВ	СВ	CB	CB	CB	CB		DC	DC	DC	DC	DC	DC		EB	EB	EB	EB	EB	EB	
10 - 91 pF* 100 pF	100 - 910* 101				F	G G	J J	K K	M	CB CB	CB CB	CB CB	CB CF	CB CB	CB CB		DC DC	DC DC	DC DC	DC DC	DC DC	DC DC		EB EB	EB EB	EB EB	EB EB	EB EB	EB EB	
110 - 180 pF*	101 111 - 181*				F	G	J	ĸ	M	СВ	CB	CB	CF	CB	CB		DC	DC	DC	DC	DC	DC		EB	EB	EB	EB	EB	EB	
200 - 300 pF*	201 - 301*				F	G	J	K	M	CB	CB	CB	CB	CB	CB	СВ	DC	DC	DC	DC	DC	DC		EB	EB	EB	EB	EB	EB	
330 pF	331				F	G	J	K	M	CB	CB	CB	CF	CB	CB	CB	DC	DC	DC	DC	DC	DC		EB	EB	EB	EB	EB	EB	
360 pF 390 pF	361 391				F F	G G	J	K K	M	CB CB	DC DC	DC DC	DC DC	DC DC	DC DC	DC DC		EB EB	EB EB	EB EB	EB EB	EB EB	EB EB							
430 pF	431				F	G	J	K	M	CB	DC	DC	DC	DC	DC	DC		EB	EB	EB	EB	EB	EB							
470 pF	471				F	G	J	К	М	СВ	CB	CB	CB	CB	CB	СВ	DC	DC	DC	DC	DC	DD		EB	EB	EB	EB	EB	EB	
510 pF	511				F	G	J	K	M	CB	DC	DC	DC	DC	DC	DC		EB	EB	EB	EB	EB	EB							
560 pF 620 pF	561 621				F	G G	J	K K	M	CB CB	DC DC	DC DC	DC DC	DC DC	DC DC	DC DC		EB EB	EB EB	EB EB	EB EB	EB EB	EB EB							
680 pF	681				F	G	J	K	M	CB	DC	DC	DC	DC	DC	DC		EB	EB	EB	EB	EB	EB							
750 pF	751				F	G	J	K	M	CB	DC	DC	DC	DC	DC	DC		EB	EB	EB	EB	EB	EB							
820 pF 910 pF	821 911				F F	G G	J J	K K	M	CB CB	DC DC	DC DC	DC DC	DC DC	DC DD	DC DD		EB EB	EB EB	EB EB	EB EB	EB EB	EB EB							
1,000 pF	102				F	G	J	K	M	CB	DC	DC	DC	DC	DD	DD		EB	EB	EB	EB	EB	EB							
1,100 pF	112				F	G	J	к	М	СВ	СВ	CB	CB	CB	CH	СН	DC	DC	DC	DC	DC	DC	DC	EB	EB	EB	EB	EB	EB	
1,200 pF	122				F	G	J	K	M	CB	CB	CB	CB	CB	CH	CH	DC	DC	DC	DC	DC	DC	DC	EB	EB	EB	EB	EB	EB	
1,300 pF 1,500 pF	132 152				F F	G G	J	K K	M	CB CB	CB CB	CB CB	CB CB	CB CB	CH CH	CH CH	DD DD	DD DD	DD DD	DD DD	DD DD	DC DC	DC DC	EB EB	EB EB	EB EB	EB EB	EC ED	EC EC	
1,600 pF	162				F	G	J	K	M	CB	CB	CB	CB	CB	CH	CH	DD	DD	DD	DD	DD	DC	DC	EB	EB	EB	EB	ED	ED	
1,800 pF	182				F	G	J	К	М	СВ	CB	CB	CB	CB	CH	СН	DD	DD	DD	DD	DD	DC	DC	EB	EB	EB	EB	ED	ED	
2,000 pF	202				F	G	J	K	M	CB	CB	CB	CB	CB	CH	CH	DC	DC	DC	DC	DC	DC	DC	EB	EB	EB	EB	ED	ED	
2,200 pF 2,400 pF	222 242				F F	G G	J	K	M	CB CB	CB CB	CB CB	CB CB	CB CB	CH	СН	DC DC	DC DC	DC DC	DC DC	DC DC	DC DC	DC DC	EB EB	EB EB	EB EB	EB EB	EE EC	EE EC	
2,700 pF	272				F	G	J	K	M	CB	CB	CB	CB	CB			DC	DC	DC	DC	DC	DC	DC	EB	EB	EB	EB	EC	EC	
3,000 pF	302				F	G	J	К	М	CB	CB	CB	CB	CB			DD	DD	DD	DD	DC	DC	DC	EC	EC	EC	EC	EC	EB	EB
3,300 pF 3,600 pF	332 362				F	G G	J J	K K	M	CB CB	CB CB	CB CB	CB CB	CB CB			DD DD	DD DD	DD DD	DD DD	DC DC	DC DD	DC DD	EC EC	EC EC	EC EC	EC EC	EE	EB EB	EB EB
3,900 pF	392				F	G	J	K	M	CB	CB	CB	CB	CB			DE	DE	DE	DE	DC	DD	DD	EC	EC	EC	EC	EF	EB	EB
4,300 pF	432				F	G	J	K	Μ	СВ	СВ	CB	CB	CB			DE	DE	DE	DE	DC	DD	DD	EC	EC	EC	EC	EC	EB	EB
4,700 pF	472				F	G	J	K	M	CB	CB	CB	CB	CB			DE	DE	DE	DE	DC	DD	DD	EC	EC	EC	EC	EC	EB	EB
5,100 pF 5,600 pF	512 562				F F	G G	J	K K	M	CB CB	CB CB	CB CB	CB CB				DE DC	DE DC	DE DC	DE DC	DC DC	DD DD	DD DD	ED ED	ED ED	ED ED	ED ED	ED ED	EB EB	EB EB
6,200 pF	622				F	G	J	ĸ	M	CB	CB	CB	CB				DC	DC	DC	DC	DC	DG	DG	EB						
6,800 pF	682				F	G	J	К	М	СВ	CB	CB	CB				DC	DC	DC	DC	DC	DG	DG	EB						
7,500 pF 8,200 pF	752 822				F	G G	J	K	M	CB CB	CB CB	CB CB					DC DC	DC DC	DC DC	DC DC	DC DC	DG DG	DG DG	EB EC	EB EC	EB EC	EB EC	EB EB	EB EC	EB EC
9,100 pF	912				F	G	J	K	M	CB	CB	CB					DC	DC	DC	DC	DC	00	00	EC	EC	EC	EC	EB	EC	EC
10,000 pF	103				F	G	J	К	М	СВ	СВ	CB					DC	DC	DC	DC	DD			ED	ED	ED	ED	EB	EC	EC
12,000 pF	123				F	G	J	K	M	CB	CB	CB					DC	DC	DC	DC	DE			EB	EB	EB	EB	EB	ED	ED
15,000 pF 18,000 pF	153 183				F	G G	J	K	M	СВ	СВ	CB					DC DC	DC DC	DC DC	DD DD	DG			EB EB	EB EB	EB EB	EB EB	EB EB	EF EH	EF EH
22,000 pF	223				F	G	J	K	M								DD	DD	DD	DF				EB	EB	EB	EB	EC	EH	
27,000 pF	273				F	G	J	K	M								DF	DF	DF					EB	EB	EB	EB	EE		
33,000 pF 39,000 pF	333 393				F	G G	J	K	M								DG DG	DG DG	DG DG					EB EC	EB EC	EB EC	EB EE	EE EH		
47,000 pF	473				F	G	J	ĸ	M								DG	DG	DG					EC	EC	EC	EE	EH		
56,000 pF	563				F	G	J	К	М															ED	ED	ED	EF			
68,000 pF	683				F	G	J	K	M															EF	EF	EF	EH			
82,000 pF 0.10 μF	823 104				F	G G	J	K K	M															EH	EH EH	EH EH	EH			
			F	Rated	Volt	_	(VDC	_		10	16	25	50	9	200	250	ę	9	25	50	100	200	250	ę	16	25	50	10	200	250
Сар	Cap Code			Vo	ltag	e Co						2	A																	
	0000		С	ase	Siz	e/S	eries C0603X C0805X C1206X																							

\*Capacitance range Includes E24 decade values only. (i.e., 10, 11, 12, 13, 15, 16, 18, 20, 22, 24, 27, 30, 33, 36, 39, 43, 47, 51, 56, 62, 68, 75, 82 and 91) These products are protected under US Patents 7,172,985 and 7,670,981, other patents pending, and any foreign counterparts.



### Table 1B – Capacitance Range/Selection Waterfall (1210 – 2225 Case Sizes)

			Ca	as	e S	Siz	e/	Se	rie	s			C	:12	210	X				C1	808	X		C1	<b>312</b>	X		C18	252	X		C22	220	X		C22	25	X
	Сар	Г			Volt	tage	e Co	ode			8	4	3		5	1	2	Α	5	1	2	4	5 ۱	1	2	Α	5	1	2	Α	5	1	2	A	5	1	2	Α
Сар	Code	F	F			-	age				9	4	_	_	20	10	200		50	Ę		_		_	_		50	10	200	250	20		200	_			200	250
	Coue	F					-	· ·			·					-	2				t Av												2	2	1 ~	-	2	7
			Ca	ара	cita	ance	e To	lera	ance												able																	
1.0 - 9.1 pF*	109 - 919*	В	0	2	D	_					FB	FE			FB	FB	FB																					
10 - 91 pF*	100 - 910*					F	G	J	K	M	FB	FE			FB	FB	FB																					
100 - 430 pF* 470 - 910 pF*	101 - 431*					F F	G G	J J	K K	M	FB FB	FE				FB FB	FB FB						GE	B GE	GB													
1,000 pF	471 - 911* 102					F	G	J	K	M	FB	FE				FB	FB						GE		GB													
1,100 pF	112			T		F	G	J	K	M	FB	FE	_	_	FB	FB	FB					E	GE	_														
1,200 pF	122					F	G	J	ĸ	M	FB	FE				FB	FB						GE															
1,300 pF	132					F	G	Ĵ	K	M	FB	FE				FB	FC						G															
1,500 pF	152					F	G	J	K	M	FB	FE				FB	FE						G															
1,600 pF	162					F	G	J	К	М	FB	FE		3   1	FB	FB	FE		1				G															
1,800 pF	182					F	G	J	K	М	FB	FE	B FE	3   1	FB	FB	FE						G	_	_													
2,000 pF	202					F	G	J	K	M	FB	FE				FC	FE						GI															
2,200 pF	222					F	G	J	K	M	FB	FE			FB	FC	FG						G	3 GE	GB													
2,400 pF	242					F	G	J	K	M	FB	FE			FB	FC	FC																					
2,700 pF	272					F	G	J	K	M	FB	FE	_			FC	FC						G	3 GE	GB													
3,000 pF	302					F	G	J	K	M	FB	FE			FB	FC	FF																					
3,300 pF	332					F	G	J	K	M	FB	FE			FB	FF	FF						G	3   GE	GB													
3,600 pF	362					F	G	J	K	M	FB	FE			FB	FF	FF																					
3,900 pF	392 432					F F	G G	J	K	M	FB FB	FE			FB FB	FF FF	FF FF						G	3   GE	GB		НВ	HB	НВ									
4,300 pF 4,700 pF	432			÷		F	G	J	K	M	FF	FF	_	_	FF	FG	FG						G	3 GE			цр	HB	ЦВ						KE	KE	KE	
5,100 pF	512					F	G	J	K	M	FB	FE				FG	FG																		KE	KE	KE	
5,600 pF	562					F	G	J	ĸ	M	FB	FE				FG	FG						G	3 GE	GH		HR	HB	HB						KE	KE		
6,200 pF	622					F	G	J	ĸ	M	FB	FE				FG	FB	FB					Ĭ												KE	KE	KE	
6,800 pF	682					F	G	J	K	M	FB	FE				FG	FB	FB					G	3 GE	GJ		НВ	HB	НВ		JE	JE	JB	JB		KE	KE	
7,500 pF	752	Ш		Т		F	G	J	K	М	FC	FC	_	_	_	FC	FB	FB				Г													KE	KE	KE	
8,200 pF	822					F	G	J	К	М	FC	FC				FC	FB	FB	1				GE	3 GF	I GB	GB	ΗВ	HB	HB		JE	JE	JB	JB			KE	
9,100 pF	912					F	G	J	K	M	FE	FE	E   FE	E   I		FE	FB	FB	1									1			1				KE	KE	KE	
10,000 pF	103					F	G	J	K	M	FF	FF	:   FF	=   I		FF	FB	FB					G	3 GF	I GB	GB	ΗВ	HB	HE		JE		JB	JB	KE	KE	KE	
12,000 pF	123					F	G	J	Κ	М	FG	FC	_	_		FB	FB	FB					G	_		_	HB	HB	HE		JE	JE	JB	JB	-	_	KE	
15,000 pF	153					F	G	J	K	M	FG	FG			FG	FB	FC						G			GB		HB			JE		JB	JB			KE	
18,000 pF	183					F	G	J	K	M	FB	FE			FB	FB	FC	FC					G				HB	HE			JE	JE	JB	JB		KE		
22,000 pF	223					F	G	J	K	M	FB	FE				FB	FF	FF					G				HB	HE			JE	JB	JB	JB				
27,000 pF	273					F	G	J	K	M	FB	FE			FB	FB	FG						G				НВ	HG			JE	JB	JB	JB		KE		
33,000 pF	333					F F	G G	J	K	M	FB	FE	_	_	_	FB FE	FH FH	FH FH				F	GE	_	-	GB GB					JB JB	JB	JB JB	JB	KE			
39,000 pF 47,000 pF	393 473					F	G	J	K	M	FB FB	FE				FE	FH						G			GD					JB	JB JB	JB	JB				
56,000 pF	563					F	G	J	K	M	FB	FE			FB	FF	' '	10					G			GD					JB	JB	JB	JB				
68,000 pF	683					F	G	J	ĸ	M	FB	FE				FG							GI			GK					JB	JB	JB	JB				
82,000 pF	823					F	G	J	ĸ	M	FC	FC				FH			1				GI		-	GM					JB	JB	JB	JB				
0.10 µF	104					F	G	J	K	M	FE	FE	_	_		FM							GI	_	_	GM					JB	JB	JD	_				
0.12 µF	124					F	G	J	К		FG	FG			FH									3 GH							JB	JB	JD	JD				
0.15 µF	154					F	G	J	K	М			I FI	H F	-M									) GN							JB	JB		JG				
0.18 µF	184					F	G	J	K	M													Gł								JB	JD		JG				
0.22 µF	224					F	G	J			FK	Fk	( Fł	<									Gł	<							JB	JD						
0.27 µF	274					F	G	J	K	M																					JB							
0.33 µF	334					F	G	J	K	M																					JD				1			
0.39 µF	394					F	G	J	K	M																					JG							
0.47 µF	474	┢	l				G	J		М				+	_	0	0	-	-	-		-			-	-		-	0	0	JG		-	-	-	-	-	_
	Сар	┝	F				age	· ·	DC)		9 9	-				100		250						-			50			- 250		100				<u> </u>	<u> </u>	- 250
Сар	Code	┝	_			-	e Co			-	8	4			5	1	2	A	5	1		4	A 5		2		5	1	2		5		2		1-	1	2	A
		Case Size/Series					-12	210	X				C1	808	X		C1	812	κ		C18	25)	۲. 		C22	2202	X		C22	25>	ί.							

\*Capacitance range Includes E24 decade values only. (i.e., 10, 11, 12, 13, 15, 16, 18, 20, 22, 24, 27, 30, 33, 36, 39, 43, 47, 51, 56, 62, 68, 75, 82 and 91) These products are protected under US Patents 7,172,985 and 7,670,981, other patents pending, and any foreign counterparts.



# Table 2 – Chip Thickness/Packaging Quantities

Thickness	Case	Thickness ±	Paper C	Quantity	Plastic (	Quantity
Code	Size	Range (mm)	7" Reel	13" Reel	7" Reel	13" Reel
СВ	0603	0.80 ± 0.07	4,000	10,000	0	0
CF	0603	0.80 ± 0.07*	4,000	15,000	0	0
CH	0603	0.85 ± 0.07	4,000	10,000	0	0
DC	0805	0.78 ± 0.10	0	0	4,000	10,000
DD	0805	0.90 ± 0.10	0	0	4,000	10,000
DE	0805	1.00 ± 0.10	0	0	2,500	10,000
DF	0805	1.10 ± 0.10	0	0	2,500	10,000
DG	0805	1.25 ± 0.15	0	0	2,500	10,000
EB	1206	0.78 ± 0.10	4,000	10,000	4,000	10,000
EC	1206	0.90 ± 0.10	0	0	4,000	10,000
ED	1206	1.00 ± 0.10	0	0	2,500	10,000
EE	1206	1.10 ± 0.10	0	0	2,500	10,000
EF	1206	1.20 ± 0.15	0	0	2,500	10,000
EH	1206	1.60 ± 0.20	0	0	2,000	8,000
FB	1210	0.78 ± 0.10	0	0	4,000	10,000
FC	1210	0.90 ± 0.10	0	0	4,000	10,000
FE	1210	1.00 ± 0.10	0	0	2,500	10,000
FF	1210	$1.10 \pm 0.10$	0	0	2,500	10,000
FG	1210	1.25 ± 0.15	0	0	2,500	10,000
FH	1210	1.55 ± 0.15	0	0	2,000	8,000
FM	1210	$1.70 \pm 0.20$	0	0	2,000	8,000
FJ	1210	1.85 ± 0.20	0	0	2,000	8,000
FK	1210	$2.10 \pm 0.20$	0	0	2,000	8,000
GB	1812	$1.00 \pm 0.10$	0	0	1,000	4,000
GD	1812	$1.25 \pm 0.15$	0	0	1,000	4,000
GH	1812	1.40 ± 0.15	0	0	1,000	4,000
GG	1812	1.55 ± 0.10	0	0	1,000	4,000
GK	1812	$1.60 \pm 0.20$	0	0	1,000	4,000
GJ	1812	$1.70 \pm 0.15$	0	0	1,000	4,000
GN	1812	$1.70 \pm 0.20$	Ő	0	1,000	4,000
GM	1812	$2.00 \pm 0.20$	0	0	500	2,000
HB	1825	$1.10 \pm 0.15$	Ő	Ő	1,000	4,000
HE	1825	$1.40 \pm 0.15$	Ő	Ő	1,000	4,000
HG	1825	$1.60 \pm 0.20$	Ő	0	1,000	4,000
JB	2220	$1.00 \pm 0.15$	0	Ő	1,000	4,000
JD	2220	$1.30 \pm 0.15$	0	0	1,000	4,000
JE	2220	$1.40 \pm 0.15$	Ő	Ő	1,000	4,000
JF	2220	$1.50 \pm 0.15$	Ő	Ő	1,000	4,000
JG	2220	$1.70 \pm 0.15$	0	0	1,000	4,000
KE	2225	1.40 ± 0.15	Ő	Ő	1,000	4,000
Thickness	Case	Thickness ±	7" Reel	13" Reel	7" Reel	13" Reel
Code	Size	Range (mm)	Paper C	Quantity	Plastic Quantity	

Package quantity based on finished chip thickness specifications.



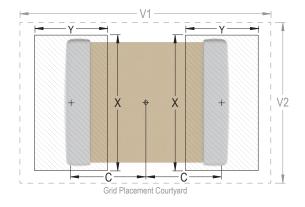
### Table 3 – Chip Capacitor Land Pattern Design Recommendations per IPC-7351 (mm)

EIA Size Code	Metric Size Code		Maxi	sity Lev mum (I rotrusio	Nost)	)		Media	sity Lev an (Nor rotrusio		)	Density Level C: Minimum (Least) Land Protrusion (mm)							
ooue	oode	С	Y	X	V1	V2	С	Y	X	V1	V2	С	Y	X	V1	V2			
0603	1608	0.85	1.25	1.10	4.00	2.10	0.75	1.05	1.00	3.10	1.50	0.65	0.85	0.90	2.40	1.20			
0805	2012	1.00	1.35	1.55	4.40	2.60	0.90	1.15	1.45	3.50	2.00	0.75	0.95	1.35	2.80	1.70			
1206	3216	1.60	1.65	1.90	5.90	2.90	1.50	1.45	1.80	5.00	2.30	1.40	1.25	1.70	4.30	2.00			
1210	3225	1.60	1.65	2.80	5.90	3.80	1.50	1.45	2.70	5.00	3.20	1.40	1.25	2.60	4.30	2.90			
1812	4532	2.30	1.75	2.30	7.40	3.30	2.20	1.55	2.20	6.50	2.70	2.10	1.35	2.10	5.80	2.40			
1825	4564	2.15	1.80	6.90	7.10	7.90	2.05	1.60	6.80	6.20	7.30	1.95	1.40	6.70	5.50	7.00			
2220	5650	2.85	2.10	5.50	8.80	6.50	2.75	1.90	5.40	7.90	5.90	2.65	1.70	5.30	7.20	5.60			
2225	5664	2.85	2.10	6.90	8.80	7.90	2.75	1.90	6.80	7.90	7.30	2.65	1.70	6.70	7.20	7.00			

**Density Level A:** For low-density product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes. KEMET only recommends wave soldering of EIA 0603, 0805, and 1206 case sizes.

**Density Level B:** For products with a moderate level of component density. Provides a robust solder attachment condition for reflow solder processes. **Density Level C:** For high component density product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC Standard 7351 (IPC–7351).

Image below based on Density Level B for an EIA 1210 case size.





### **Soldering Process**

#### **Recommended Soldering Technique:**

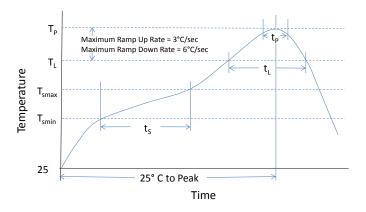
- Solder wave or solder reflow for EIA case sizes 0603, 0805 and 1206
- · All other EIA case sizes are limited to solder reflow only

#### **Recommended Reflow Soldering Profile:**

KEMET's families of surface mount multilayer ceramic capacitors (SMD MLCCs) are compatible with wave (single or dual), convection, IR or vapor phase reflow techniques. Preheating of these components is recommended to avoid extreme thermal stress. KEMET's recommended profile conditions for convection and IR reflow reflect the profile conditions of the IPC/J-STD-020 standard for moisture sensitivity testing. These devices can safely withstand a maximum of three reflow passes at these conditions.

Profile Feature	Terminati	on Finish
Prome reature	SnPb	100% Matte Sn
Preheat/Soak		
Temperature Minimum (T <sub>Smin</sub> )	100°C	150°C
Temperature Maximum (T <sub>Smax</sub> )	150°C	200°C
Time (t <sub>s</sub> ) from $T_{Smin}$ to $T_{Smax}$	60 – 120 seconds	60 – 120 seconds
Ramp-Up Rate $(T_L \text{ to } T_P)$	3°C/second maximum	3°C/second maximum
Liquidous Temperature $(T_L)$	183°C	217°C
Time Above Liquidous ( $t_L$ )	60 – 150 seconds	60 – 150 seconds
Peak Temperature (T <sub>P</sub> )	235°C	260°C
Time Within 5°C of Maximum Peak Temperature (t <sub>P</sub> )	20 seconds maximum	30 seconds maximum
Ramp-Down Rate $(T_P \text{ to } T_L)$	6°C/second maximum	6°C/second maximum
Time 25°C to Peak Temperature	6 minutes maximum	8 minutes maximum

Note 1: All temperatures refer to the center of the package, measured on the capacitor body surface that is facing up during assembly reflow.





### Table 4 – Performance & Reliability: Test Methods and Conditions

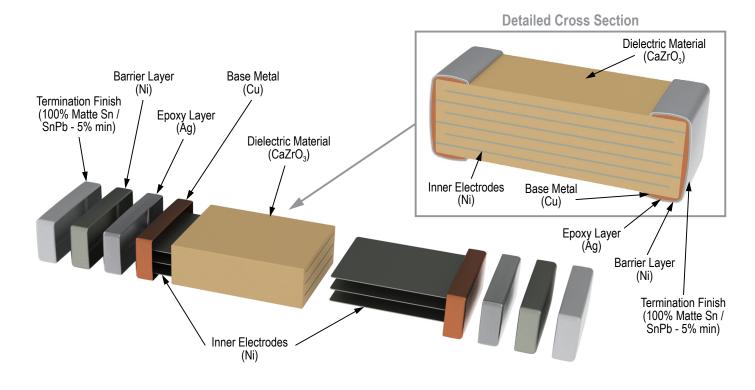
Stress	Reference	Test or Inspection Method
Terminal Strength	JIS-C-6429	Appendix 1, Note: Force of 1.8 kg for 60 seconds.
Board Flex	JIS-C-6429	Appendix 2, Note: Standard termination system – 2.0 mm (minimum) for all except 3 mm for C0G. Flexible termination system – 3.0 mm (minimum).
		Magnification 50 X. Conditions:
Soldorability	J-STD-002	a) Method B, 4 hours @ 155°C, dry heat @ 235°C
Solderability	J-31D-002	b) Method B @ 215°C category 3
		c) Method D, category 3 @ 260°C
Temperature Cycling	JESD22 Method JA-104	1,000 Cycles (-55°C to +125°C). Measurement at 24 hours +/- 2 hours after test conclusion.
Discod Uturriditu	MII – STD–202 Method 103	Load Humidity: 1,000 hours 85°C/85% RH and rated voltage. Add 100 K ohm resistor. Measurement at 24 hours +/- 2 hours after test conclusion.
Biased Humidity	MIL-STD-202 Method 103	Low Volt Humidity: 1,000 hours 85°C/85% RH and 1.5 V. Add 100 K ohm resistor. Measurement at 24 hours +/- 2 hours after test conclusion.
Moisture Resistance	MIL-STD-202 Method 106	t = 24 hours/cycle. Steps 7a and 7b not required. Unpowered. Measurement at 24 hours +/- 2 hours after test conclusion.
Thermal Shock	MIL-STD-202 Method 107	-55°C/+125°C. Note: Number of cycles required – 300, maximum transfer time – 20 seconds, dwell time – 15 minutes. Air – Air.
High Temperature Life	MIL–STD–202 Method 108 /EIA–198	1,000 hours at 125°C (85°C for X5R, Z5U and Y5V) with 2 X rated voltage applied.
Storage Life	MIL-STD-202 Method 108	150°C, 0 VDC for 1,000 hours.
Vibration	MIL-STD-202 Method 204	5 g's for 20 min., 12 cycles each of 3 orientations. Note: Use 8" X 5" PCB 0.031" thick 7 secure points on one long side and 2 secure points at corners of opposite sides. Parts mounted within 2" from any secure point. Test from 10 – 2,000 Hz
Mechanical Shock	MIL-STD-202 Method 213	Figure 1 of Method 213, Condition F.
Resistance to Solvents	MIL-STD-202 Method 215	Add aqueous wash chemical, OKEM Clean or equivalent.

#### **Storage and Handling**

Ceramic chip capacitors should be stored in normal working environments. While the chips themselves are quite robust in other environments, solderability will be degraded by exposure to high temperatures, high humidity, corrosive atmospheres, and long term storage. In addition, packaging materials will be degraded by high temperature– reels may soften or warp and tape peel force may increase. KEMET recommends that maximum storage temperature not exceed 40°C and maximum storage humidity not exceed 70% relative humidity. Temperature fluctuations should be minimized to avoid condensation on the parts and atmospheres should be free of chlorine and sulfur bearing compounds. For optimized solderability chip stock should be used promptly, preferably within 1.5 years of receipt.



### Construction



## **Capacitor Marking (Optional):**

Laser marking option is not available on:

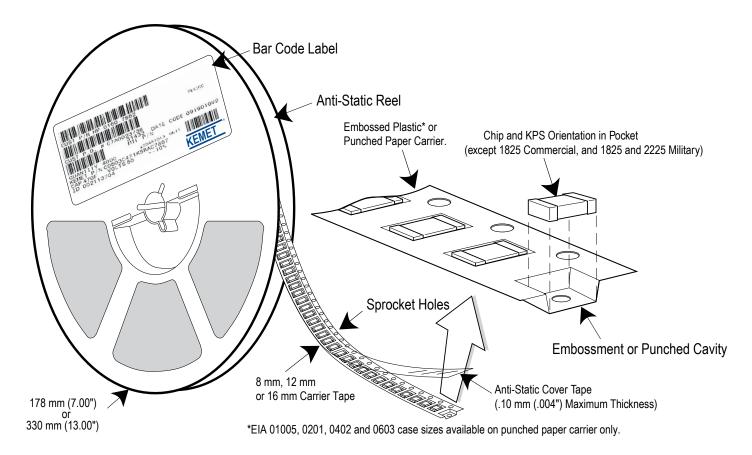
- C0G, Ultra Stable X8R and Y5V dielectric devices
- · EIA 0402 case size devices
- EIA 0603 case size devices with Flexible Termination option.
- · KPS Commercial and Automotive grade stacked devices.

These capacitors are supplied unmarked only.



### **Tape & Reel Packaging Information**

KEMET offers multilayer ceramic chip capacitors packaged in 8, 12 and 16 mm tape on 7" and 13" reels in accordance with EIA Standard 481. This packaging system is compatible with all tape-fed automatic pick and place systems. See Table 2 for details on reeling quantities for commercial chips.



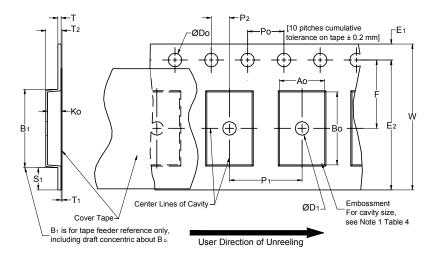
#### Table 5 – Carrier Tape Configuration – Embossed Plastic & Punched Paper (mm)

EIA Case Size	Tape Size (W)*	Pitch (P <sub>1</sub> )*
01005 – 0402	8	2
0603 – 1210	8	4
1805 – 1808	12	4
≥ 1812	12	8
KPS 1210	12	8
KPS 1812 & 2220	16	12
Array 0508 & 0612	8	4

\*Refer to Figures 1 & 2 for W and P<sub>1</sub> carrier tape reference locations. \*Refer to Tables 6 & 7 for tolerance specifications.



## Figure 1 – Embossed (Plastic) Carrier Tape Dimensions



### Table 6 – Embossed (Plastic) Carrier Tape Dimensions

Metric will govern

			Constant Dim	ensions — Mi	llimeters (Incl	nes)			
Tape Size	D <sub>0</sub>	D <sub>1</sub> Minimum Note 1	E <sub>1</sub>	P <sub>0</sub>	P <sub>2</sub>	R Reference Note 2	S <sub>1</sub> Minimum Note 3	T Maximum	T <sub>1</sub> Maximum
8 mm		1.0 (0.039)				25.0 (0.984)			
12 mm	1.5 +0.10/-0.0 (0.059 +0.004/-0.0)	1.5	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	30	0.600 (0.024)	0.600 (0.024)	0.100 (0.004)
16 mm		(0.059)				(1.181)			
			Variable Dime	ensions — Mil	limeters (Inch	ies)			
Tape Size	Pitch	B <sub>1</sub> Maximum Note 4	E <sub>2</sub> Minimum	F	P <sub>1</sub>	T <sub>2</sub> Maximum	W Maximum	A <sub>0</sub> ,B <sub>0</sub>	& K <sub>0</sub>
8 mm	Single (4 mm)	4.35 (0.171)	6.25 (0.246)	3.5 ±0.05 (0.138 ±0.002)	4.0 ±0.10 (0.157 ±0.004)	2.5 (0.098)	8.3 (0.327)		
12 mm	Single (4 mm) & Double (8 mm)	8.2 (0.323)	10.25 (0.404)	5.5 ±0.05 (0.217 ±0.002)	8.0 ±0.10 (0.315 ±0.004)	4.6 (0.181)	12.3 (0.484)	Not	e 5
16 mm	Triple (12 mm)	12.1 (0.476)	14.25 (0.561)	7.5 ±0.05 (0.138 ±0.002)	12.0 ±0.10 (0.157 ±0.004)	4.6 (0.181)	16.3 (0.642)		

1. The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location and hole location shall be applied independent of each other.

2. The tape with or without components shall pass around R without damage (see Figure 6).

3. If S<sub>1</sub> < 1.0 mm, there may not be enough area for cover tape to be properly applied (see EIA Standard 481 paragraph 4.3 section b).

4. B, dimension is a reference dimension for tape feeder clearance only.

5. The cavity defined by  $A_{\alpha}$ ,  $B_{\alpha}$  and  $K_{\alpha}$  shall surround the component with sufficient clearance that:

(a) the component does not protrude above the top surface of the carrier tape.

(b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.

(c) rotation of the component is limited to 20° maximum for 8 and 12 mm tapes and 10° maximum for 16 mm tapes (see Figure 3).

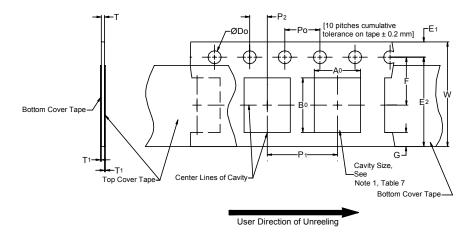
(d) lateral movement of the component is restricted to 0.5 mm maximum for 8 and 12 mm wide tape and to 1.0 mm maximum for 16 mm tape (see Figure 4).

(e) for KPS Series product,  $A_0$  and  $B_0$  are measured on a plane 0.3 mm above the bottom of the pocket.

(f) see Addendum in EIA Standard 481 for standards relating to more precise taping requirements.



### Figure 2 – Punched (Paper) Carrier Tape Dimensions



# Table 7 – Punched (Paper) Carrier Tape Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)									
Tape Size	D <sub>0</sub>	E <sub>1</sub>	P <sub>0</sub>	P <sub>2</sub>	T <sub>1</sub> Maximum	G Minimum	R Reference Note 2		
8 mm	1.5 +0.10 -0.0 (0.059 +0.004 -0.0)	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	0.10 (0.004) Maximum	0.75 (0.030)	25 (0.984)		
Variable Dimensions — Millimeters (Inches)									
Tape Size	Pitch	E2 Minimum	F	P <sub>1</sub>	T Maximum	W Maximum	A <sub>0</sub> B <sub>0</sub>		
8 mm	Half (2 mm)	6.25	3.5 ±0.05 (0.138 ±0.002)	2.0 ±0.05 (0.079 ±0.002)	1.1 (0.098)	8.3 (0.327)	Note 1		
8 mm	Single (4 mm)	(0.246)		4.0 ±0.10 (0.157 ±0.004)		8.3 (0.327)			

1. The cavity defined by  $A_{\alpha}$ ,  $B_{\alpha}$  and T shall surround the component with sufficient clearance that:

a) the component does not protrude beyond either surface of the carrier tape.

b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.

c) rotation of the component is limited to 20° maximum (see Figure 3).

d) lateral movement of the component is restricted to 0.5 mm maximum (see Figure 4).

e) see Addendum in EIA Standard 481 for standards relating to more precise taping requirements.

2. The tape with or without components shall pass around R without damage (see Figure 6).



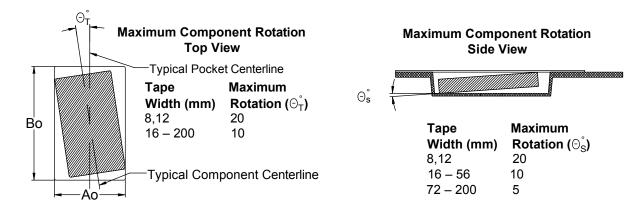
### **Packaging Information Performance Notes**

- 1. Cover Tape Break Force: 1.0 Kg minimum.
- 2. Cover Tape Peel Strength: The total peel strength of the cover tape from the carrier tape shall be:

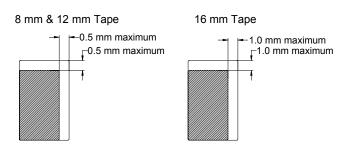
Tape Width	Peel Strength		
8 mm	0.1 to 1.0 Newton (10 to 100 gf)		
12 and 16 mm	0.1 to 1.3 Newton (10 to 130 gf)		

The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be  $165^{\circ}$  to  $180^{\circ}$  from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of  $300 \pm 10$  mm/minute. **3. Labeling:** Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. *Refer to EIA Standards* 556 *and* 624.

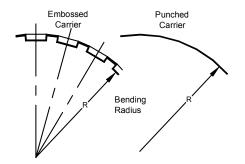
#### Figure 3 – Maximum Component Rotation



### Figure 4 – Maximum Lateral Movement

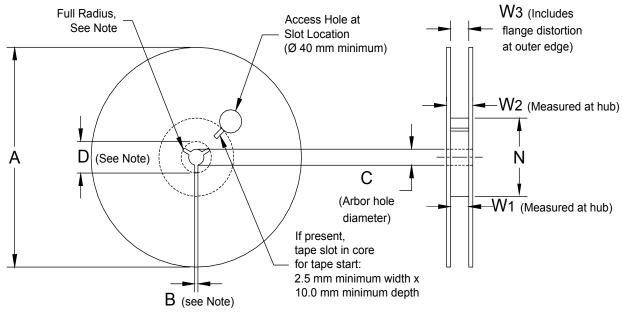


### Figure 5 – Bending Radius





### Figure 6 – Reel Dimensions



Note: Drive spokes optional; if used, dimensions B and D shall apply.

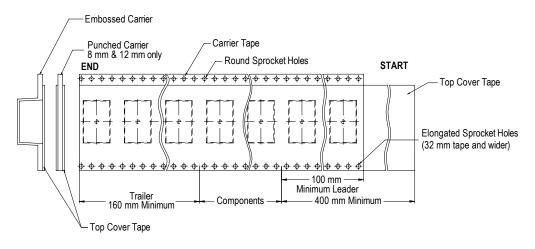
#### Table 8 – Reel Dimensions

Metric will govern

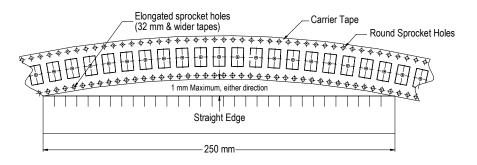
Constant Dimensions — Millimeters (Inches)								
Tape Size	A	B Minimum	С	D Minimum				
8 mm	178 ±0.20		13.0 +0.5/-0.2 (0.521 +0.02/-0.008)	20.2 (0.795)				
12 mm	(7.008 ±0.008) or	1.5 (0.059)						
16 mm	330 ±0.20 (13.000 ±0.008)		()					
Variable Dimensions — Millimeters (Inches)								
Tape Size	N Minimum	W <sub>1</sub>	W <sub>2</sub> Maximum	W <sub>3</sub>				
8 mm		8.4 +1.5/-0.0 (0.331 +0.059/-0.0)	14.4 (0.567)					
12 mm	50 (1.969)	12.4 +2.0/-0.0 (0.488 +0.078/-0.0)	18.4 (0.724)	Shall accommodate tape width without interference				
16 mm		16.4 +2.0/-0.0 (0.646 +0.078/-0.0)	22.4 (0.882)					



### Figure 7 – Tape Leader & Trailer Dimensions



#### Figure 8 – Maximum Camber





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