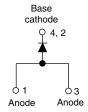


Vishay Semiconductors

Schottky Rectifier, 5.5 A





D-PAK	(TO-252AA)	
D . A	(10 202777)	

PRODUCT SUMMARY						
Package	D-PAK (TO-252AA)					
I _{F(AV)}	5.5 A					
V_{R}	30 V					
V _F at I _F	See Electrical table					
I _{RM}	58 mA at 125 °C					
T _J max.	150 °C					
Diode variation	Single die					
E _{AS}	10 mJ					

FEATURES

- Popular D-PAK outline
- Small foot print, surface mountable



- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- \bullet Meets MSL level 1, per J-STD-020, LF maximum peak of 260 $^{\circ}\text{C}$

DESCRIPTION

The VS-50WQ03FNPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS										
SYMBOL	CHARACTERISTICS	VALUES	UNITS							
I _{F(AV)}	Rectangular waveform	5.5	A							
V _{RRM}		30	V							
I _{FSM}	t _p = 5 µs sine	320	A							
V _F	5 Apk, T _J = 125 °C	0.35	V							
TJ	Range	- 40 to 150	°C							

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-50WQ03FNPbF	UNITS					
Maximum DC reverse voltage	V_{R}	30	V					
Maximum working peak reverse voltage	V_{RWM}	30	V					

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL TEST CONDITIONS							
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 136 °C	5.5	Α				
Maximum peak one cycle non-repetitive surge current		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	320	А			
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	130				
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{A}, L = 5 \text{mH}$	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{A}, L = 5 \text{mH}$		mJ			
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero Frequency limited by T _J maximum	2.0	Α				

VS-50WQ03FNPbF

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Revision: 14-Jan-11

ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS						
		5 A	T _{.1} = 25 °C	0.46	V				
Maximum forward voltage drop	V _{FM} ⁽¹⁾	10 A	1]=25 0	0.53					
See fig. 1	V FM ('')	5 A	T _{.1} = 125 °C	0.35					
		10 A	1J = 125 C	0.46					
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _B = Rated V _B	3	- mA				
See fig. 2		T _J = 125 °C	VR = Nated VR	58					
Threshold voltage	V _{F(TO)}	$T_{.1} = T_{.1}$ maximum		0.19	V				
Forward slope resistance	r _t	ij=ijmaximum		22.22	mΩ				
Typical junction capacitance	C _T	V _R = 5 V _{DC} (test signal ran	590	pF					
Typical series inductance	L _S	Measured lead to lead 5 n	nm from package body	5.0	nH				

Note

 $^{^{(1)}}$ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 40 to 150	°C					
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	3.0	°C/W					
Approximate weight			0.3	g					
Approximate weight			0.01	oz.					
Marking device		Case style D-PAK (similar to TO-252AA)	50WC	03FN					

Note

(1)
$$\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$$
 thermal runaway condition for a diode on its own heatsink



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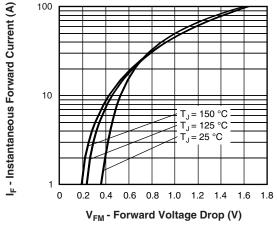


Fig. 1 - Maximum Forward Voltage Drop Characteristics

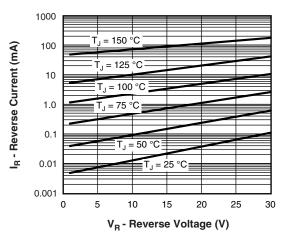


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

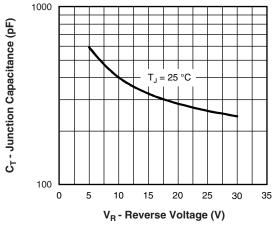


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

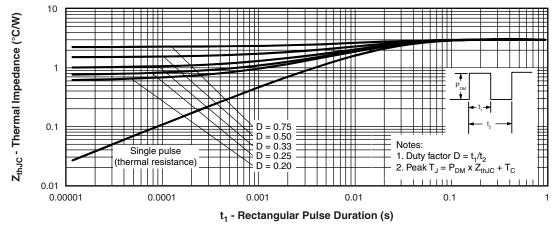


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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Schottky Rectifier, 5.5 A



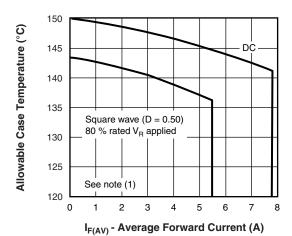


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

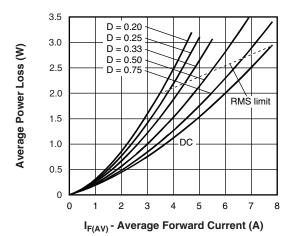
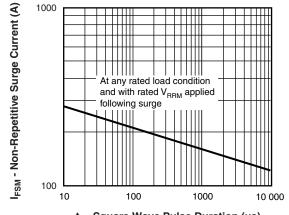


Fig. 6 - Forward Power Loss Characteristics



t_p - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80 \%$ rated V_R

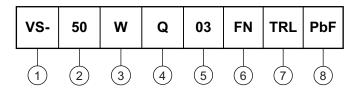


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Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

Current rating (5.5 A)

Package identifier:

W = D-PAK

4 - Schottky "Q" series

5 - Voltage rating (03 = 30 V)

6 - FN = TO-252AA (D-PAK)

7 • None = Tube (50 pieces)

• TR = Tape and reel

• TRL = Tape and reel (left oriented)

• TRR = Tape and reel (right oriented)

PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95016					
Part marking information	www.vishay.com/doc?95059					
Packaging information	www.vishay.com/doc?95033					
SPICE model	www.vishay.com/doc?95408					



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NOTES

3

2

MAX.

0.410

0.070

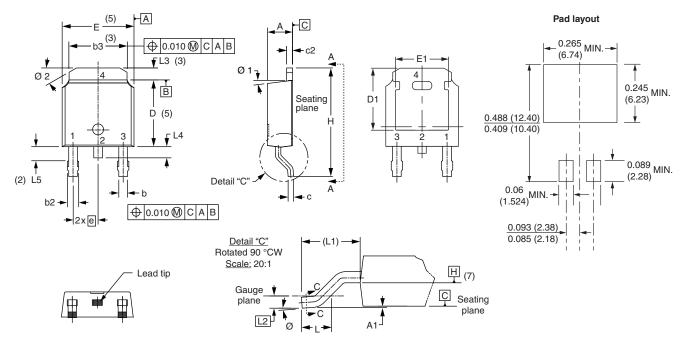
0.050

0.040

0.060

D-PAK (TO-252AA)

DIMENSIONS in millimeters and inches



Ī	SYMBOL	MILLIMETERS		INCHES		NOTES	CVMDOL	MILLIMETERS		INCHES		
	STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES		SYMBOL	MIN.	MAX.	MIN.	MAX
ſ	Α	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC
ſ	A1	-	0.13		0.005			Н	9.40	10.41	0.370	0.41
Ī	b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.07
Ī	b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.
ſ	b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020	BSC
Ī	С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.05
Ī	c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.04
ſ	D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.06
Ī	D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°
ſ	Е	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°
Ī	E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- Lead dimension uncontrolled in L5
- Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- Outline conforms to JEDEC outline TO-252AA



Legal Disclaimer Notice

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