# VS-1N3208 Series

Vishay Semiconductors



## Silicon Rectifier Diodes, (Stud Version) 15 A

### FEATURES

- Low thermal impedance
- High case temperature
- Excellent reliability
- Maximum design flexibility
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>





DO-203AB (DO-5)

PRODUCT SUMMARY		
I <sub>F(AV)</sub>	15 A	
Package	DO-203AB (DO-5)	
Circuit configuration	Single diode	

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
I <sub>F(AV)</sub>		15 <sup>(1)</sup>	A	
	T <sub>C</sub>	150 <sup>(1)</sup>	°C	
I <sub>FSM</sub>	50 Hz	239	٨	
	60 Hz	250 <sup>(1)</sup>	A	
l <sup>2</sup> t	50 Hz	286	A <sup>2</sup> s	
1-t	60 Hz	260	A-5	
l²√t		3870	A²√s	
V <sub>RRM</sub>	Range	50 to 600	V	
TJ		-65 to +175	C°	

#### Note

<sup>(1)</sup> JEDEC<sup>®</sup> registered values

### **ELECTRICAL SPECIFICATIONS**

### **VOLTAGE RATINGS**

TYPE NUMBER	$V_{RRM},$ MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE (T_J = -65 °C TO 175 °C) V	$V_{RM}$ , MAXIMUM DIRECT REVERSE VOLTAGE (T <sub>J</sub> = -65 °C TO 175 °C) V		
VS-1N3208	50 <sup>(1)</sup>	50 <sup>(1)</sup>		
VS-1N3209	100 (1)	100 (1)		
VS-1N3210	200 (1)	200 (1)		
VS-1N3211	300 (1)	300 (1)		
VS-1N3212	400 (1)	400 (1)		
VS-1N3213	500 <sup>(1)</sup>	500 (1)		
VS-1N3214	600 <sup>(1)</sup>	600 <sup>(1)</sup>		

#### Notes

<sup>(1)</sup> JEDEC registered values

• Basic type number indicates cathode to case. For anode to case, add "R" to part number, e.g. 1N3208R, 1N3209R

Revision: 12-Nov-15 1 Document Number: 93496 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



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FORWARD CONDUCTION					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	180° sinusoidal conduction		15 <sup>(1)</sup>	A
at case temperature	. ,			150 <sup>(1)</sup>	°C
Maximum peak one cycle non-repetitive surge current	I <sub>FSM</sub>	Half cycle 50 Hz sine wave or 6 ms rectangular pulse	Following any rated load condition and with rated V <sub>RRM</sub> applied	239	A
		Half cycle 60 Hz sine wave or 5 ms rectangular pulse		250 <sup>(1)</sup>	
		Half cycle 50 Hz sine wave or 6 ms rectangular pulse	Following any rated load condition and with V <sub>RRM</sub> applied following surge = 0	284	
		Half cycle 60 Hz sine wave or 5 ms rectangular pulse		297	
Maximum I <sup>2</sup> t for fusing	- l <sup>2</sup> t	t = 10 ms	With rated V <sub>RRM</sub> applied following surge, initial T <sub>J</sub> = 150 °C	286	A <sup>2</sup> s
		t = 8.3 ms		260	
Maximum I <sup>2</sup> t for individual device fusing		t = 10 ms	With V <sub>RRM</sub> = 0 following surge, initial T <sub>J</sub> = 150 °C	403	
		t = 8.3 ms		368	
Maximum I²√t for individual device fusing	l²√t (2)	t = 0.1 ms to 10 ms, V <sub>RRM</sub> = 0 following surge		3870	A²√s
Maximum forward voltage drop	V <sub>FM</sub>	I <sub>F(AV)</sub> = 15 A (47.1 A peak), T <sub>C</sub> = 150 °C		1.5 <sup>(1)</sup>	V
Maximum average reverse current	I <sub>R(AV)</sub>	Maximum rated $I_{F(AV)}$ and $T_{C} = 150 \text{ °C}$		10 <sup>(1)</sup>	mA

#### Notes

(1) JEDEC registered values

<sup>(2)</sup> I<sup>2</sup>t for time  $t_x = I^2 \sqrt{t} x \sqrt{t_x}$ 

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction operating and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65 to 175 <sup>(1)</sup>	°C	
Maximum internal thermal resistance, junction to case	R <sub>thJC</sub>	DC operation	0.65	• °C/W	
Thermal resistance, case to sink	R <sub>thCS</sub>	Mounting surface, smooth, flat and greased	0.25		
		Not lubricated thread, tighting on nut <sup>(2)</sup>	3.4	(30)	
Maximum allowable mounting torque (+0 %, -10 %)		Lubricated thread, tighting on nut <sup>(2)</sup>	2.3 (20)		
		Not lubricated thread, tighting on hexagon (3)	4.2	(37)	
		Lubricated thread, tighting on hexagon <sup>(3)</sup>	3.2	(28)	
Weight			28.5	g	
			1	oz.	
Case style		JEDEC	DO-203AB (DO-5)		

Notes

<sup>(1)</sup> JEDEC registered values

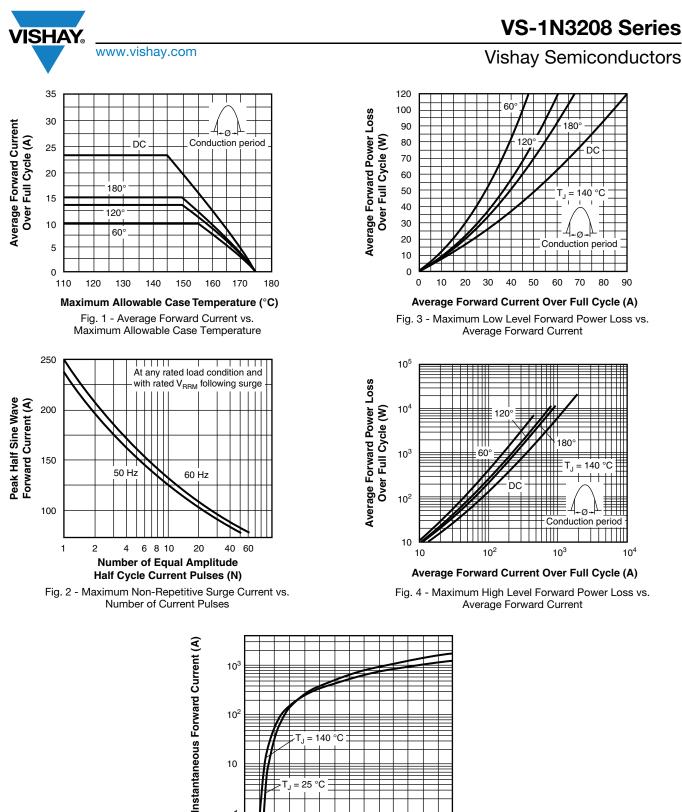
<sup>(2)</sup> Recommended for pass-through holes

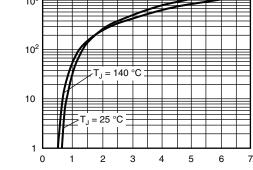
<sup>(3)</sup> Recommended for holed threaded heatsinks

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Instantaneous Forward Voltage (V)

Fig. 5 - Maximum Forward Voltage vs. Forward Current

LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95360		

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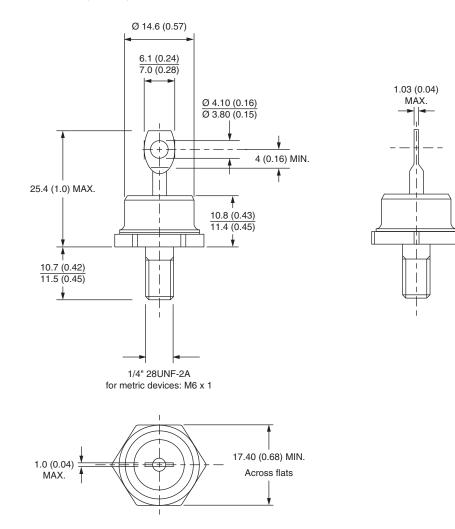
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**Vishay Semiconductors** 

## DO-203AB (DO-5) for 1N1183, 1N3765, 1N1183A, 1N2128A, 1N3208 Series

**DIMENSIONS** in millimeters (inches)

SHA





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