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Sı	pecification	
Part Number:	MCT043D6W480272LSL	
Version:		
Date:		
	Revision	

# Midas Active Matrix Display Part Number System

MC T 057 A 6 \* W 320240 L M L \* \*
1 2 3 4 5 6 7 8 9 10 11 12 13

- 1 = MC: Midas Components
  2 = T: TFT A: Active Matrix OLED
- 3 = Size
- 4 = Series
- 5 = Viewing Angle: 6: 6 O'clock 12: 12 O'clock
- 6 = Blank: No Touch T: Resistive Touchscreen C: Capacitive Touchscreen
- 8 = No of Pixels
- 9 = **Orientation: P:** Portrait **L:** Landscape
- 10 = Mode: R: Reflective M: Transmissive T: Transflective S: Sunlight Readable (transmissive)
  W: White on Black (Monochrome)
- 11 = **Backlight: Blank:** None **L:** LED **C:** CCFL
- 12 = **Blank:** No Module/board **C:** Controller board module
- 13 = **Blank:** None **OB:** Optically Bonded

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## 2. Outline Specifications

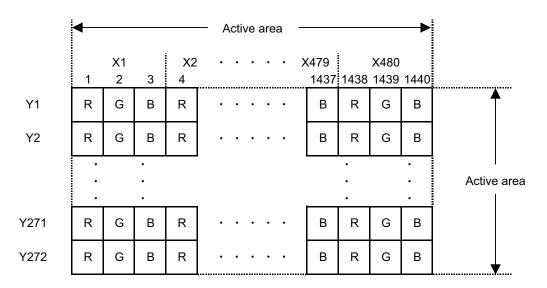
## 2.1 Features of the Product

- 4.3 inch diagonal display, 1,440 [H] x 272 [V] dots.
- 8-bit 16,777,216 color display capability.
- Single power supply operation of 3.3V.
- Built in Timing generator (TG), Counter-electrode driving circuitry and power supply circuit.
- High bright white LED back-light.
- Blanview TFT-LCD, improved outdoor readability.

	Inde	oor	Outdoor			
	Readability	Power Efficiency (Battery Life)	Readability	Power Efficiency (Battery Life)		
Transmissive	Good	Good	Fair	Poor		
Transflective	Fair	Poor	Good	Good		
Blanview	Good	Good	Good	Good		

## 2.2 Display Method

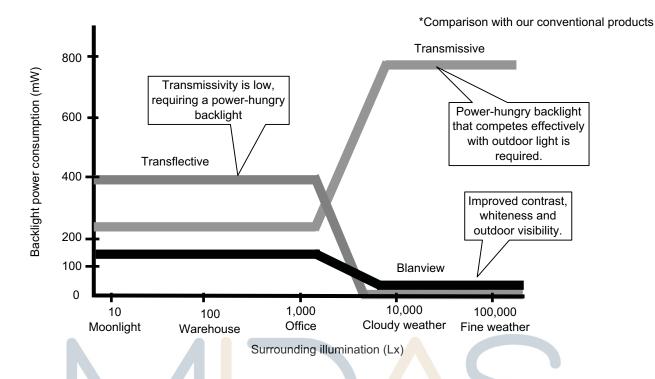
Items	Specifications	Remarks
Display type	TN type 16,7 <mark>77</mark> ,216 colors.	
	Blanview, No <mark>rm</mark> ally white.	
Driving method		
	Line-scanning, Non-interlace.	
Dot arrangement	RGB stripe arrangement.	Refer to "Dot arrangement".
Signal input method	8-bit RGB, parallel input.	
Backlight type	High bright white LED.	SUDDLV



Dot arrangement (FPC cable placed downside)

## <Features of Blanview>

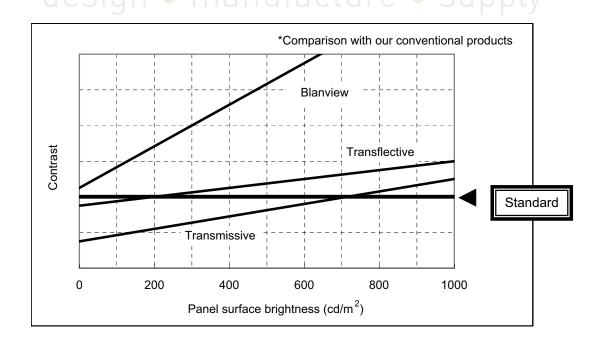
- Backlight power consumption required to assure visibility. (equivalent to 3.5"QVGA)



- Contrast characteristics under 100,000Lx. (same condition as direct sunlight.)

With better contrast (higher contrast ratio), Blanview TFT-LCD has the best outdoor readability in three different types of TFT-LCD.

Below chart shows contrast value against panel surface brightness. (Horizontal: Panel surface brightness/ Vertical: Contrast value) LCD panel has enough outdoor readability above our Standard line.

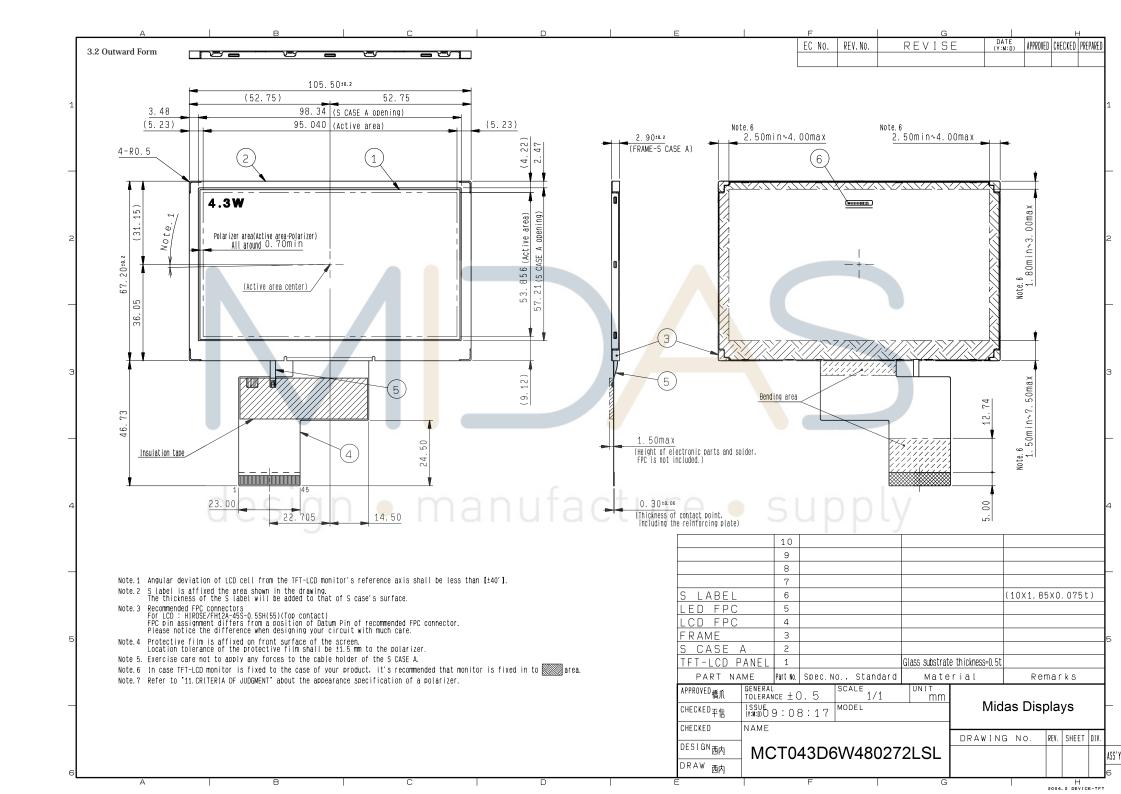


## 3. Dimensions and Shape

## 3.1 Dimensions

Items	Specifications	Unit	Remarks
Outline dimensions	105.50[H] × 67.20[V] × 2.90[D]	mm	Exclude FPC cable.
Active area	95.040[H] × 53.856[V]	mm	10.9cm diagonal.
Number of dots	1,440[H] × 272[V]	dot	
Dot pitch	66.0[H] × 198.0[V]	μm	
Surface hardness of the polarizer	3	Н	Load:2.0N
Weight	40.0	g	Include FPC cable.





## 4. Pin Assignment

No.	Symbol	Function
1	VSS	GND.
2	VSS	GND.
3	VDD	Power supply.
4	VDD	Power supply.
5	D00	
6	D01	Display data(R).
7	D02	00h: Black
8	D03	D00:LSB D07:MSB
9	D04	
10	D05	Driver has internal gamma conversion.
11	D06	
12	D07	
13	D10	
14	D11	Display data(G).
15	D12	00h: Black
16	D13	D10:LSB D17:MSB
17	D14	
18	D15	Driver has internal gamma conversion.
19	D16	
20	D17	
21	D20	
22	D21	Display data(B).
23	D22	00h: Black
24	D23	D20:LSB D27:MSB
25	D24	
26	D25	Driver has internal gamma conversion.
27	D26	
28	D27	
29	VSS	GND.
30	CLK	Clock signal.Latching data at the falling edge.
31	STBYB	Standby signal input. (Hi:Normal operation, Lo:Standby operation)
32	HSYNC	Horizontal sync signal input. (Low active)
33	VSYNC	Vertical sync signal input. (Low active)
34	DE	Input data effective signal. (It is effective for the period of "Hi")
35	NC	OPEN.
36	VSS	GND.
37	NC NC	OPEN.
38	NC NC	OPEN
39	NC NC	OPEN.
40	VSS	OPEN.
41		GND.  Backlight drive (cathode side)
	BLL	Backlight drive (cathode side)  Backlight drive (anode side)
43	BLH NC	
		OPEN.
45	NC	OPEN.

- Recommended connector: HIROSE ELECTRIC FH12 series [FH12A-45S-0.5SH(55)]
- Please make sure to check a consistency between pin assignment in "3.2 Outward Form" and your connector pin assignment when designing your circuit.

  Inconsistency in input signal assignment may cause a malfunction.
- Since FPC cable has gold plated terminals, gilt finish contact shoe connector is recommended.

## 5. Absolute Maximum Rating

VSS=0V

Item	Symbol	Condition	Rating		Unit	Applicable terminal
			MIN	MAX		
Supply voltage	VDD	Ta=25° C	-0.3	5.0	V	VDD
Input voltage for logic	VI		-0.3	VDD+0.3	V	CLK,VSYNC,HSYNC,DE D[27:20],D[17:10],D[07:00], STBYB
LED direction current	IL	Ta=25° C		35	mA	BLH - BLL
of order		Ta=70° C		15		
Storage temperature range	Tstg		-30	80	°C	
Storage humidity range	Non condensing in an environmental moisture at or less than 40 ° C90%RH.					

## 6. Recommended Operating Conditions

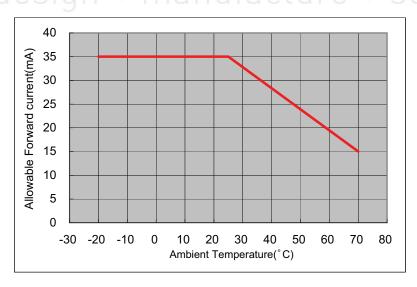
VSS=0V

Item	Symbol	Condition		Rating			Applicable terminal
	- <b>,</b>		MIN	TYP	MAX	Unit	. трризовите то по
Supply voltage	VDD		3.0	3.3	3.6	V	VDD
Input voltage for logic	VI	VDD=3.0~	0		VDD	V	CLK,VSYNC,HSYNC,
		3.6V					DE,D[27:20],D[17:10],
							D[07:00],STBYB
Operating temperatur	Тор	Note 1,2	-20	25	70	°C	Panel surface
range							temperature
Operating humidity		T <mark>a</mark> ≦30° C	20		80	%	
range	Нор	Ta>30° C	Non conder	Non condensing in			
			an environmen <mark>tal moisture at o</mark> r				
			less than 30	°C80%RH			

Note1: This monitor is operatable in this temperature range. With regard to optical characteristics, refer to Item "10. CHARACTERISTICS".

Note2: Acceptable Forward Current to LED is up to 15mA, when Ta=+70 ° C.

Do not exceed Allowable Forward Current shown on the chart below.



## 7. Characteristics

## 7.1 DC Characteristics

## 7.1.1 Display Module

(Unless otherwise noted, Ta=25 °C,VDD=3.3V,VSS=0V)

Item	Symbol	Condition		Rating		Unit	Applicable terminal
			MIN	TYP	MAX		
Input voltage	VIH	VDD=3.0~3.6V	0.7×VDD		VDD	V	CLK,VSYNC,HSYNC,
for logic							DE,D[27:20],D[17:10],
	VIL		0		0.3×VDD	V	D[07:00],STBYB
Pull down	Rpd			200		kΩ	DE,D[27:20],D[17:10],
resister value	Пра			200		11.2.2	D[07:00]
Pull up	Rpu			200		kΩ	VSYNC,HSYNC,
resister value							STBYB
Current	IDD	fCLK=9MHz	-	17	34	mA	VDD
consumption		Color bar display					
Standby Current	IDDs	Other input with constant		100	200	μΑ	
		voltage					

## 7.1.2 Backlight

Item	Symbol	C <mark>on</mark> dition		Rating		Unit	Applicable terminal
			MIN	TYP	MAX		
Forward current	IL25	Ta=25° C		10.0	35.0	mA	BLH - BLL
	IL70	Ta=70° C			15.0	mA	
Forward voltage	VL	Ta=25° C, IL=10.0mA		27.0	29.7	V	
Estimated Life	LL	Ta=25° C, IL=10.0mA		(20,000)		hr	
of LED		Note					

Note: - The lifetime of the LED is defined as a period till the brightness of the LED decreases to the half of its initial value.

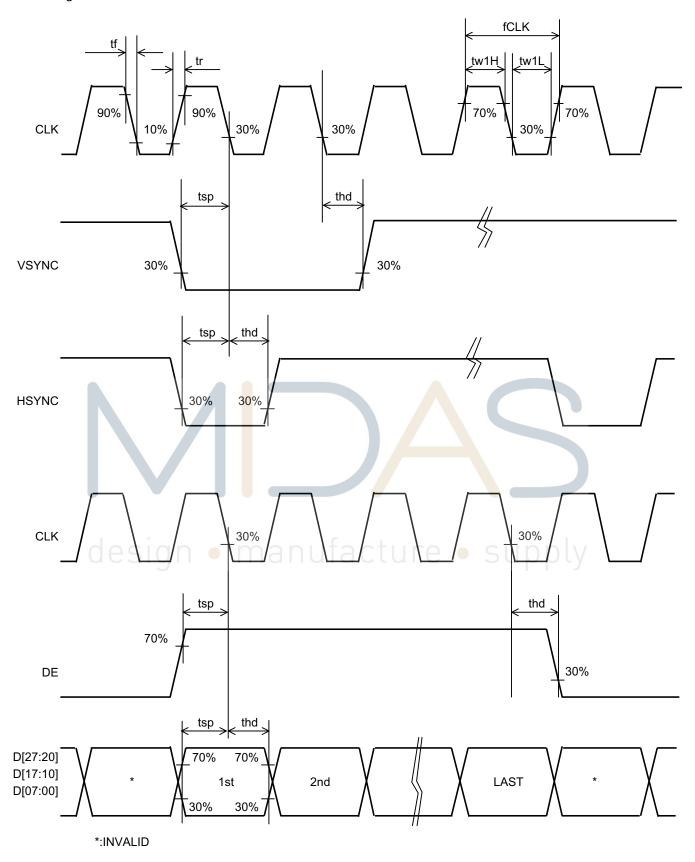
- This figure is given as a reference purpose only, and not as a guarantee.
- This figure is estimated for an LED operating alone.
   As the performance of an LED may differ when assembled as a monitor together with a TFT panel due to different environmental temperature.
- Estimated lifetime could vary on a different temperature and usually higher temperature could reduce the life significantly.

## 7.2 AC Characteristics

(Unless otherwise noted, Ta=25°C,VDD=3.3V,VSS=0V)

Item	Symbol	Condition		Rating		Unit	Applicable terminal
			MIN	TYP	MAX		
CLK frequency	fCLK		5.0	9.0	12.0	MHz	CLK
CLK rising time	tr	10%→90%		1	9	ns	
CLK falling time	tf	90%→10%		1	9	ns	
CLK Low period	tw1L	0.3×VDD or less.	0.4/fCLK	1	0.6/fCLK	ns	
CLK High period	tw1H	0.7×VDD or more.	0.4/fCLK	1	0.6/fCLK	ns	
Setup time	tsp		12.0	1		ns	CLK,VSYNC,HSYNC,
Hold time	thd		12.0	-	-	ns	DE,D[27:20],D[17:10],
							D[07:00]

## **Switching Waveform Characteristics**



## 7.3 Input Timing Characteristics

(Unless otherwise noted, Ta=25 °C,VDD=3.3V,VSS=0V)

Item	Symbol		Rating		Unit	Applicable terminal
		MIN	TYP	MAX		
VSYNC frequency Note	fVSYNC	54	60	66	Hz	VSYNC
VSYNC signal cycle time	tv	277	288	400	Н	VSYNC,HSYNC
VSYNC pulse width	tw2H	1	-	-	Н	
Vertical back porch	tvb	3	8	31	Н	
Vertical front porch	tvf	2	8	93	Н	
Vertical display period	tvdp		272		Н	VSYNC,HSYNC,DE,D[27:20], D[17:10],D[07:00]
HSYNC frequency	fHSYNC	15.38	16.67	18.18	Khz	HSYNC
HSYNC signal cycle time	th	520	525	800	CLK	HSYNC,CLK
HSYNC pulse width	tw3H	1	-	1	CLK	
Horizontal back porch	thb	36	40	255	CLK	HSYNC,DE,CLK
Horizontal front porch	thf	4	5	65	CLK	
Horizontal display period	thdp		480		CLK	DE,D[27:20],D[17:10],D[07:00], CLK
DE pulse width	tw4H		480	/	CLK	DE,CLK

Note: The characteristic of this item is recommended standard.

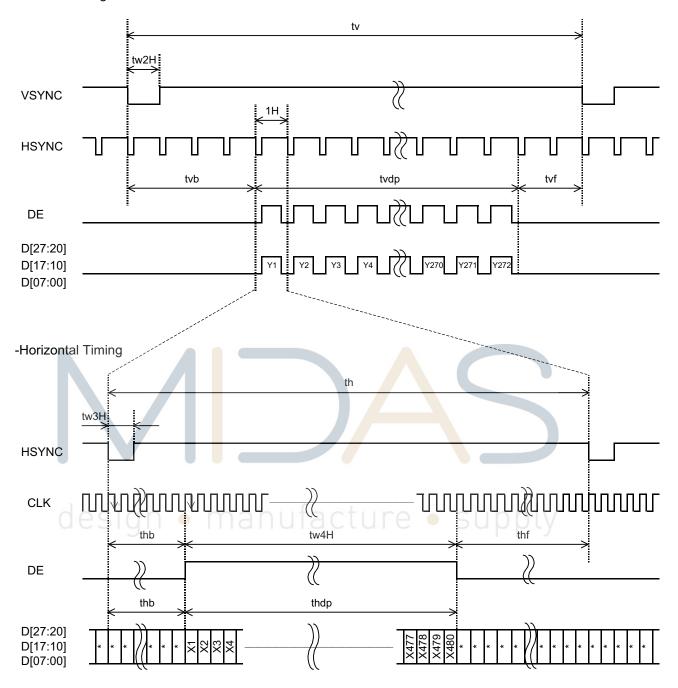
Please use it after it confirms it enough like the display fineness etc.

When it comes off from this characteristic and it is used.



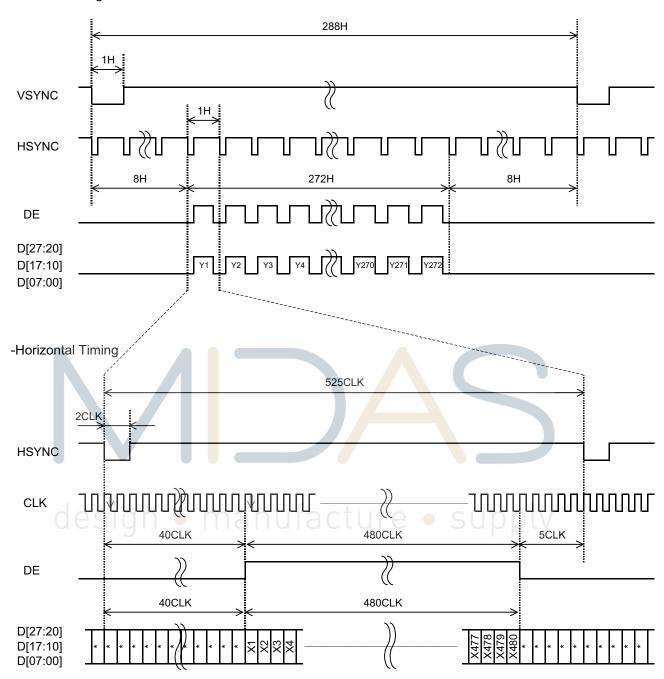
## 7.4 Driving Timing Chart

## -Vertical Timing



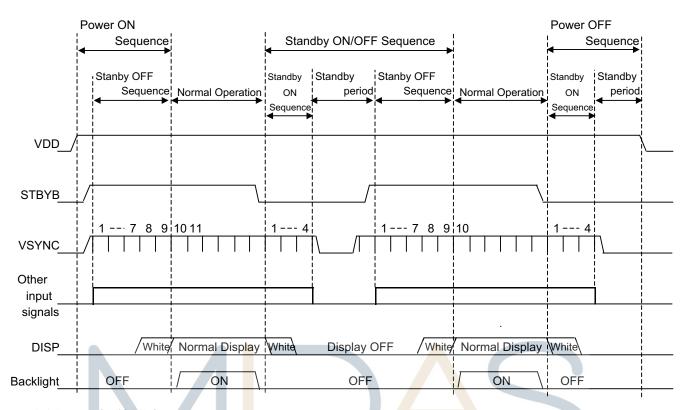
## 7.5 Example of Driving Timing Chart (fCLK=9.0MHz)

## -Vertical Timing



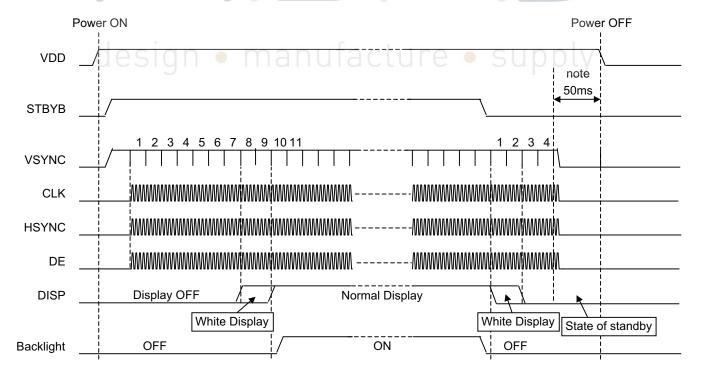
## 8. Description of Sequence

The outline of "Power ON/OFF Sequence" and "Standby ON/OFF Sequence" is shown below.



## 8.1 Power ON/OFF Sequence

The sequence of the Power On/Off and the signal input must defend the following conditions.



Note: For Power OFF,please turn off VDD since 50msec after the standby state shifts.

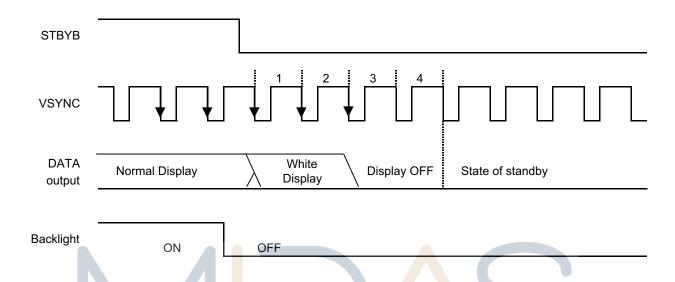
When CLK and the VSYNC signal are stopped or the power supply is turned off to a regulated frame or less, the afterimage might remain.

## 8.2 Stanby ON/OFF Sequence

It explains Standby ON/OFF sequence by the STBYB signal.

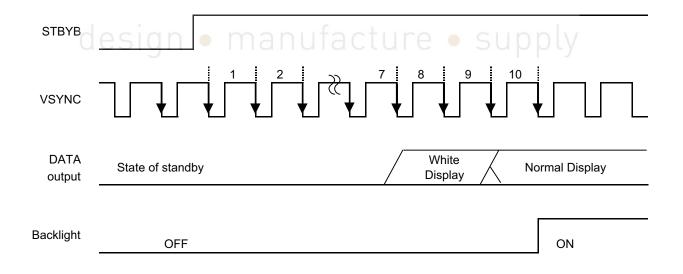
The following time will be needed by the shift in the state of the standby from the standby setting according to the STBYB signal.

Meanwhile, VSYNC signal and the CLK signal should keep being supplied.

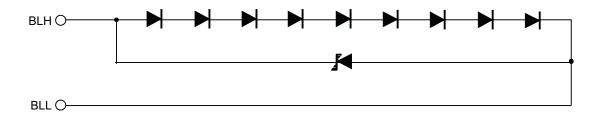


Similarly, the time of nine frames will be needed by the time a usual display is begun from the standby release by the STBYB signal.

Please begin outputting in the 8th frame on the Display Data.



## 9. LED Circuit



# MDAS

design • manufacture • supply

## 10. Characteristics

## 10.1 Optical Characteristics

< Measurement Condition >

Measuring instruments: CS1000 (KONICA MINOLTA), LCD7000(OTSUKA ELECTRONICS),

EZcontrast160D (ELDIM)

Driving condition: VDD = 3.3V,VSS=0V

Optimized VCOMDC

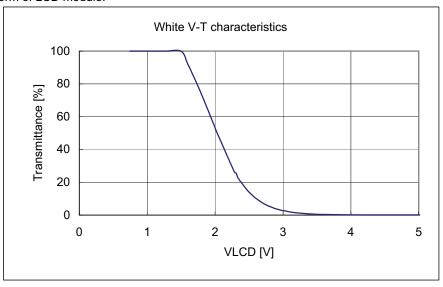
VLCD= | Vsigpp±Vcompp | /2

Backlight: IL=10mA Measured temperature: Ta=25° C

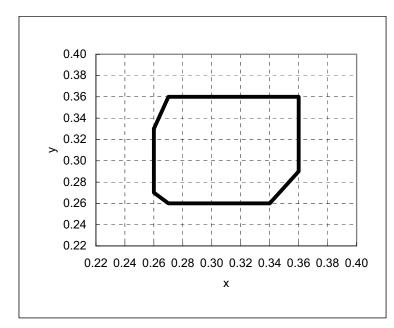
Measured temperature. Ta-25 C									
	Item	Symbol	Condition	MIN	TYP	MAX	Unit	Note No.	Remark
<b>a</b>		TON	VLCD=	_	_	40	ms	1	*
Response time	Rise time		0.5V→4.8V						
espon	F = 0	TOFF	VLCD=	_	_	60	ms	1	
ά	Fall time		4.8V→0.5V						
پ	Dooklight ON	CR	VLCD=	240	400	_		2	
Contrast ratio	Backlight ON		0.5V/4.8V						
on	Backlight OFF			_	7.5	_			
0	Backlight OFF								
D	Left	θL	VLCD=	80		_	deg	3	*
Viewing angle	Right	θR	0.5V/4.8V	80	_	_	deg		
/ie/	Up	φU	CR≧10	80		_	deg		
	Down	φD		80	_	_	deg		
\/_T +I	nreshold	V90		1.2	1.5	1.8	V	4	*
voltag		V50		1.7	2.0	2.3	V		
voltas	<b>J</b>	V10		2.2	2.5	2.8	V		
Whi	te V-T Curve			White V-	T Curve				Reference
White	Chromaticity	Х	VLCD=0.5V	White ch	romaticit	y range		5	
VVIIIC	y y								
	Burn-in desig			No noticeable burn-in image				6	
					oe observ				
			$\mathbf{n} \bullet \mathbf{m}$	of window pattern display.				<ul><li>S</li></ul>	UDDLV
_	Center brightness		VLCD=0.5V	315	450	_	cd/m <sup>2</sup>	7	1 1 7
Brigh	tness distribution	on	VLCD=0.5V	70	_	_	%	8	

<sup>\*</sup> Note number 1 to 8: Refer to the APPENDIX of "Reference Method for Measuring Optical Characteristics".

Measured in the form of LCD module.



White V-T Curve



## [White Chromaticity Range]

Х	У
0.26	0.33
0.26	0.27
0.27	0.26
0.34	0.26
0.36	0.29
0.36	0.36
0.27	0.36

White Chromaticity Range

## 10.2 Temperature Characteristics

< Measurement Condition >

Measuring instruments: CS1000 (KONICA MINOLTA), LCD7000 (OTSUKA ELECTRONICS)

Driving condition:  $VDD = \frac{3.3V}{VSS=0V}$ 

Optimized VCOMDC

VLCD= | Vsigpp±Vcompp | /2

Backlight: IL=10mA

Item		000000000	Specif	ication	Remark
	esian •	m	Ta=-10°C	Ta=7 <mark>0</mark> °C	Remark
Contrast ratio		CR	40 or more	40 or more	Backlight ON
Response time	Rise time	TON	200 msec or less	30 msec or less	*
Response time	Fall time		300 msec or less	50 msec or less	*
Display Quality			No noticeable display defect or ununiformity should be observed.		Use the criteria for judgment specified in the section 11.

<sup>\*</sup> Measured in the form of LCD module.

## 11. Criteria of Judgment

## 11.1 Defective Display and Screen Quality

Test Condition: Observed TFT-LCD monitor from front during operation with the following conditions

Driving Signal Raster Patter (RGB in monochrome, white, black)

Signal condition VLCD:0.5V, 2.2V, 4.8V (3 steps)

Observation distance 30 cm
Illuminance 200 to 350 lx
Backlight IL=10mA

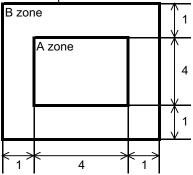
De	fect item	Defect content		Criteria
	Line defect	Black, white or color	r line, 3 or more neighboring defective dots	Not exists
Ϊξ		Uneven brightness	on dot-by-dot base due to defective	
Quality		TFT or CF, or dust i	s counted as dot defect	
S	Dot defect	(brighter dot, darker	dot)	Refer to table 1
Display	Dot delect	High bright dot: Visi	ble through 2% ND filter at VLCD=4.8V	Refer to table 1
Dis		Low bright dot: Visi	ble through 5% ND filter at VLCD=4.8V	
		Dark dot: Appear da	ark through white display at VLCD=2.2V	
	Dirt	Point-like uneven br	ightness (white stain, black stain etc)	Invisible through 1% ND filter
\ <u>&gt;</u>		Point-like	0.25mm<φ	N=0
Quality	Faraian		0.20<φ≦0.25mm	N≦2
g	Foreign particle		φ≦ <mark>0.2</mark> 0mm	Ignored
en	particle	Liner	3.0mm <length 0.08mm<width<="" and="" td=""><td>N=0</td></length>	N=0
Screen			leng <mark>th</mark> ≦3.0mm or width≦0.08mm	Ignored
0	Others			Use boundary sample
	Outers			for judgment when necessary

φ(mm): Average diameter = (major axis + minor axis)/2
Permissible number: N

Table 1

Table I					
Area	High bright dot	Low bright dot	Dark dot	Total	anufacture supply
А	0	2	2	3	Permissible distance between same color bright dots (includes neighboring dots): 3 mm or more
В	2	4	4	6	Permissible distance between same color high bright dots (includes neighboring dots): 5 mm or more
Total	2	4	4	7	

<Landscape model>



Division of A and B areas

B area: Active area

Dimensional ratio between A and B areas: 1: 4: 1 (Refer to the left figure)

## 11.2 Screen and Other Appearance

Testing conditions

Observation distance Illuminance

30cm 1200~2000 lx

	Item	Criteria	Remark
Polarizer	Flaw Stain Bubble Dust Dent	Ignore invisible defect when the backlight is on.	Applicable area: Active area only (Refer to the section 3.2 "Outward form")
	S-case	No functional defect occurs	
	FPC cable	No functional defect occurs	



## 12. Reliability Test

	Test item	Test condition	number of failures
	Lligh tomporature atomage	Ta=80° C 240H	/number of examinations 0/3
	High temperature storage  Low temperature storage	Ta=-30° C 240H	0/3
	' '	Ta=60° C, RH=90% 240H	0/3
Durability test	High temperature & high humidity storage		0/3
ity		ü	0/3
abil	High temperature operation	Tr. Tr. Tr.	0/3
Jura	Low temperature operation	Tp=-20° C 240H	
	High temp & humid operation	Tp=40°C, RH=90% 240H	0/3
	<del>-</del>	non condensing	0. (0.
	Thermal shock storage	-30 ← →80° C(30min/30min) 100 cycles	0/3
		Confirms to EIAJ ED-4701/300	0/3
	Electrostatic discharge test	C=200pF,R=0Ω,V=±200V	
	(Non operation)	Each 3 times of discharge on and power supply	
Mechanical environmental test		and other terminals.	
	Surface discharge test	C=250pF, R=100Ω, V=±12kV	0/3
	(Non operation)	Each 5 times of discharge in both polarities	
alt	( ) ()	on the center of screen with the case grounded.	
ent		Pull the FPC with the force of 3N for 10 sec.	0/3
пп	FPC tension test	in the direction - 90-degree to its	
/iro		original direction.	
en		Pull the FPC with the force of 3N for 10 sec.	0/3
sal	FPC bend test	in the direction -180-degree to its	
anic		o <mark>rig</mark> inal direction. Reciprocate it 3 times.	
ch	Vibration test	T <mark>ot</mark> al amplitude 1.5mm, f=10∼55 <mark>H</mark> z, X, <mark>Y,</mark> Z	0/3
Me	Vibration test	directions for each 2 hours	
		Use T ãaæ original jig (see next	0/3
		page) and make an impact with peak acceleration	
	Impact test	of 1000m/s <sup>2</sup> for 6 msec with half sine-curve at	
		3 times to each X, Y, Z directions in	
		conformance with JIS 60068-2-27-1995.	
, t	1	Acceleration of 19.6m/s <sup>2</sup> with frequency of	0/1 Packing
tes	Packing vibration-proof test	10→55→10Hz, X,Y, Zdirection for each	pply
Packing test	300.9	30 minutes	
ack	Dealine deserted	Drop from 75cm high.	0/1 Packing
٣	Packing drop test	1 time to each 6 surfaces, 3 edges, 1 corner	

Note:Ta=ambient temperature

Tp=Panel temperature

X The profile of high temperature/humidity storage and High Temperature/humidity operation (Pure water of over 10M $\Omega$ ·cm shall be used.)

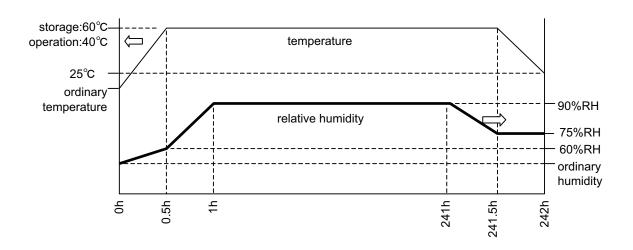
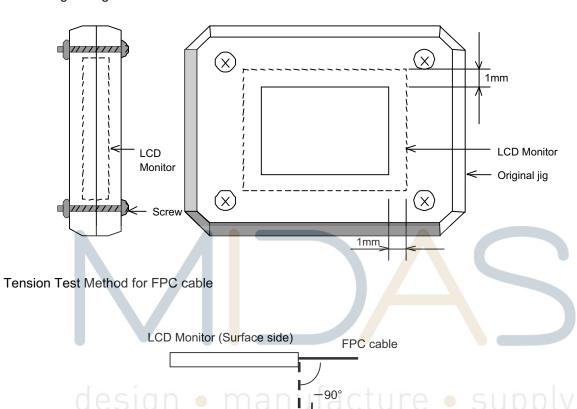


Table2.Reliability Criteria

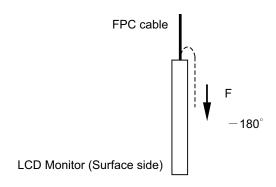
Measure the parameters after leaving the monitor at the ordinary temperature for 2 hours or more after the test completion.

item	Standard	Remarks
Display quality	No visible abnormality shall be seen.	As criteria of
		"11 Criteria of Judgment".
Contrast ratio	40 or more	Backlight ON

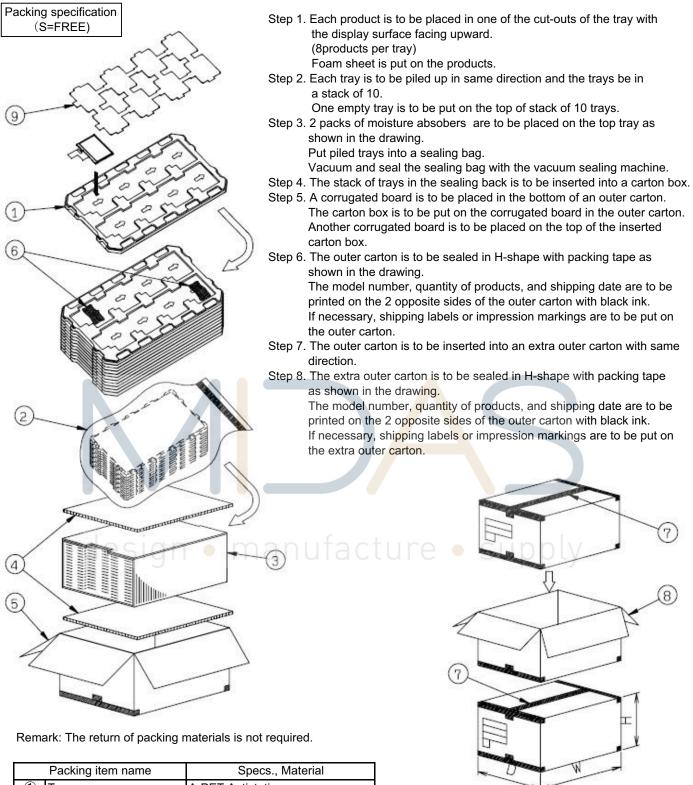
## Tãaæ Original Jig



## Bend Test Method for FPC cable



## 13. Packing Specifications



	Packing item name	Specs., Material
1	Tray	A-PET Antistatic
2	Sealing bag	
3	Carton box	Corrugated cardboard
4	Inner board	Corrugated cardboard
(5)	Outer carton	Corrugated cardboard
6	Drier	Moisture absorber
7	Packing tape	
8	Extra outer carton	Corrugated cardboard
9	Foam sheet	PE Anti-static

Dimension of extra outer carton				
D : Approx.	(338mm)			
W : Approx.	(549mm)			
H : Approx.	(198mm)			
Quantity of products pack	ked in one carton:	80		
Gross weight : App	rox. 6.7Kg			



## Caution

- (1) Do not make an impact on the LCD panel glass because it may break and you may get injured from it.
- (2) If the glass breaks, do not touch it with bare hands.(Fragment of broken glass may stick you or you cut yourself on it.
- (3) If you get injured, receive adequate first aid and consult a medial doctor.
- (4) Do not let liquid crystal get into your mouth.
  (If the LCD panel glass breaks, try not let liquid crystal get into your mouth even toxic property of liquid crystal has not been confirmed.
- (5) If liquid crystal adheres, rinse it out thoroughly.
  (If liquid crystal adheres to your cloth or skin, wipe it off with rubbing alcohol or wash it thoroughly with soap. If liquid crystal gets into eyes, rinse it with clean water for at least 15 minutes and consult an eye doctor.
- (6) If you scrape this products, follow a disposal standard of industrial waste that is legally valid in the community, country or territory where you reside.
- (7) Do not connect or disconnect this product while its application products is powered on.
- (8) Do not attempt to disassemble or modify this product as it is precision component.
- (9) If a part of soldering part has been exposed, and avoid contact (short-circuit) with a metallic part of the case etc. about FPC of this model, please. Please insulate it with the insulating tape etc. if necessary. The defective operation is caused, and there is a possibility to generation of heat and the ignition.
- (10) Since excess current protection circuit is not built in this TFT module, there is the possibility that LCD module or peripheral circuit become feverish and burned in case abnoramal operation is generated. We recommend you to add excess current protection circuit to power supply.



Caution

This mark is used to indicate a precaution or an instruction which, if not correctly observed, may result in bodily injury, or material damages alone.

## 14.2 Precautions for Handling

- Wear finger tips at incoming inspection and for handling the TFT monitors to keep display quality and keep the working area clean. Do not touch the surface of the monitor as it is easily scratched.
- 2) Wear grounded wrist-straps and use electrostatic neutralization blowers to prevent static charge and discharge when handling the TFT monitors as the LED in this TFT monitors is damageable to electrostatic discharge, Properly set up equipment, jigs and machines, and keep working area clean and tidy for handling the TFT monitors.
- Avoid strong mechanical shock including knocking, hitting or dropping to the TFT monitors for protecting their glass parts. Do not use the TFT monitors that have been experienced dropping or strong mechanical shock.
- 4) Do not use or storage the TFT monitors at high temperature and high humidity environment. Particularly, never use or storage the TFT monitors at a location where condensation builds up.
- 5) Avoid using and storing TFT monitors at a location where they are exposed to direct sunlight or ultraviolet rays to prevent the LCD panels from deterioration by ultraviolet rays.
- 6) Do not stain or damage the contacts of the FPC cable . Otherwise, it may cause poor contact or deteriorate reliability of the FPC cable.
- Do not bend or pull the FPC cable or carry the TFT monitor by holding the FPC cable. 7)
- 8) Peel off the protective film on the TFT monitors during mounting process. Refer to the section 14.5 on how to peel off the protective film. We are not responsible for electrostatic discharge failures or other defects occur when peeling off the protective film.

## 14.3 Precautions for Operation

- 1) Since this TFT monitors are not equipped with light shielding for the driver IC, do not expose the driver IC to strong lights during operation as it may cause functional failures.
- 2) When turning off the power, turn off the input signal before or at the same timing of switching off the power.
- 3) Do not plug in or out the FPC cable while power supply is switch on. Plug the FPC cable in and out while power supply is switched off.
- Do not operate the TFT monitors in the strong magnetic field. It may break the TFT monitors. 4)
- Do not display a fixed image on the screen for a long time. 5) Use a screen-saver or other measures to avoid a fixed image displayed on the screen for a long time. Otherwise, it may cause burn-in image on the screen due the characteristics of liquid crystal.

## 14.4 Storage Condition for Shipping Cartons

Storage environment

Temperature 0 to 40° CHumidity 60%RH or less

No-condensing occurs under low temperature with high humidity condition.

Atmosphere No poisonous gas that can erode electronic components and/or wiring

materials should be detected.

Time period 3 months

Unpacking
To protect the TFT monitors from static damage during unpacking, keep room

humidity more than 50%RH and implement effective countermeasures against static electricity such as establishing a ground (an earth) before unpacking.

Maximum piling up 7 cartons

## 14.5 Precautions for Peeling off the Protective film

The followings work environment and work method are recommended to prevent the TFT monitors from static damage or adhesion of dust when peeling off the protective films.

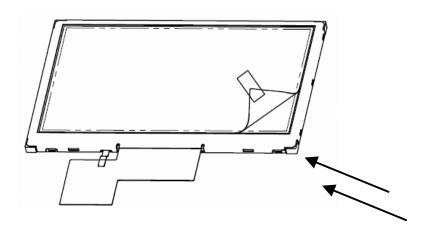
## A) Work Environment

- a) Humidity: 50 to 70 %RH, Temperature15 to 27°C
- b) Operators should wear conductive shoes, conductive clothes, conductive finger tips and grounded wrist-straps. Anti-static treatment should be implemented to work area's floor.
- c) Use a room shielded against outside dust with sticky floor mat laid at the entrance to eliminate dirt.

## B) Work Method

The following procedures should taken to prevent the driver ICs from charging and discharging.

- a) Use an electrostatic neutralization blower to blow air on the TFT monitors to its lower right when the FPC cable is facing to the downside. Optimize direction of the blowing air and the distance between the TFT monitors and the electrostatic neutralization blower.
- b) Put an adhesive tape (Scotch tape, etc) at the lower right corner area of the protective film to prevent scratch on surface of TFT monitor.
- c) Peel off the adhesive tape slowly (spending more than 2 seconds to complete) by pulling it to opposite direction.



Direction of blowing air (Optimize air direction and the distance)

## **APPENDIX**

Reference Method for Measuring Optical Characteristics and Performance

## 1. Measurement Condition (Backlight ON)

Measuring instruments: CS1000 (KONICA MINOLTA), LCD7000(OTSUKA ELECTRONICS), EZcontrast160D (ELDIM)

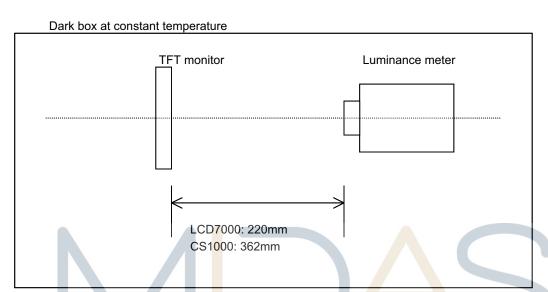
Driving condition: Refer to the section "Optical Characteristics"

Measured temperature: 25°C unless specified

Measurement system: See the chart below. The luminance meter is placed on the normal line of

measurement system.

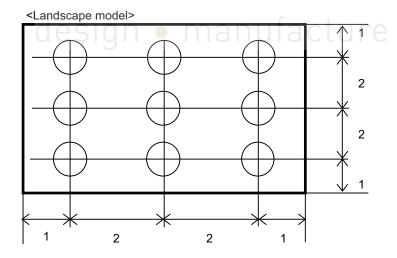
Measurement point: At the center of the screen unless otherwise specified



Measurement is made after 30 minutes of lighting of the backlight.

Measurement point: At the center point of the screen

Brightness distribution: 9 points shown in the following drawing.



Dimensional ratio of active area

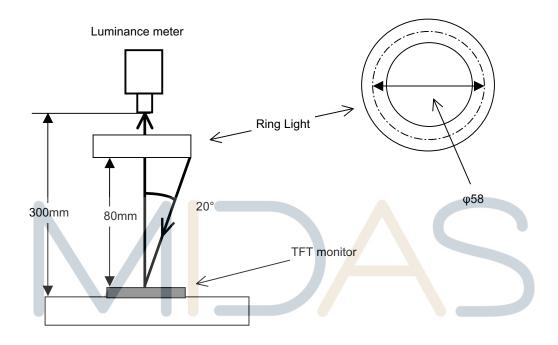
Backlight IL=10mA

Measurement Condition (Contrast ratio Backlight OFF only)

Measuring instruments: LCD7000(OTSUKA ELECTRONICS),Ring Light(40,000 lx,φ58)

Driving condition: Refer to the section "Optical Characteristics"

Measured temperature: 25°C unless specified
Measurement system: See the chart below.
Measurement point: At the center of the screen.



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## 2. Test Method

Notice	Item	Test method	Measuring instrument	Remark
1	Response time	Measure output signal waveform by the luminance meter when raster of window pattern is changed from white to black and from black to white.  White Black White	LCD7000	Black display VLCD=4.8V White display VLCD=0.5V TON Rise time
		White		TOFF Fall time
		90%		
		10% 0% Black		
2	Contrast ratio	Measure maximum luminance Y1(VLCD=0.5V) and	CS1000	Backlight ON
	desi	minimum luminance Y2(VLCD=4.8V) at the center of the screen by displaying raster or window pattern.  Then calculate the ratio between these two values.  Contrast ratio = Y1/Y2  Diameter of measuring point: 8mmφ	SUPP	Backlight OFF
3	Viewing angle Horizontalθ	Move the luminance meter from right to left and up and down and determine the angles where contrast ratio is 10.	EZcontrast160D	
4	Verticalφ V-T threshold value	Change VLCD by 0.1V step and plot the points where the luminance is 90% as V90, 50% as V50 and 10% as V10 of maximum luminance.	LCD7000	
		100% Luminance 50% 10% 0 V90 V50 V10		
5	White chromaticity	Measure chromaticity coordinates x and y of CIE1931 colorimetric system at VLCD = 0.5V  Color matching faction: 2°view	CS1000	

Notice	Item	Test method	Measuring instrument	Remark
6	Burn-in	Visually check burn-in image on the screen after 2 hours of "window display" (VLCD=0.5V/4.8V).		At optimized VCOMDC
7	Center brightness	Measure the brightness at the center of the screen.	CS1000	
8	_	(Brightness distribution) = 100 x B/A % A: max. brightness of the 9 points B: min. brightness of the 9 points	CS1000	



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