

For More Information

The complete schematic and user's guide for the PICDEM PIC18 Explorer Board, as well as the data sheet for the PIC18F46J11 family of microcontrollers are available on the Microchip web site: <http://www.microchip.com/PIC18J>

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PIC18F46J11 Plug-in Module for PICDEM™ PIC18 Explorer Board

Overview

The PIC18F46J11 Plug-in Module (PIM) is an accessory to the PICDEM™ PIC18 Explorer Board that allows users to easily experiment with the PIC18F46J11 family of microcontrollers. PIC18F46J11 is the superset member of the family and this PIM can be used to evaluate and develop with the PIC18F46J11, PIC18F45J11, PIC18F44J11, PIC18F26J11, PIC18F25J11 and PIC18F24J11 products. The PIM takes the place of the on-board PIC18F8722 device, and changes the output of the voltage regulator on the board to the proper voltage level for these parts. This enables users to quickly evaluate the new PIC18FJ Flash devices without having to buy a completely new demo board.

Getting Started with the PIM

1. Make sure the on-board PIC18F8722 is programmed to allow the MCLR Reset pin to function as a Reset pin. If the MCLR is configured to be used for general purpose I/O, the on-board PIC18F8722 may interfere with PIM usage, even when the board is switched to ICE mode.
2. Verify that the PICDEM PIC18 Explorer Board is not powered.
3. Set switch, S4, to the "ICE" position. This will hold the on-board PIC18F8722 in Reset, allowing the PIM to function instead.
4. Line up the PIM so its 3-pin female header aligns with the 3-pin riser on the PICDEM PIC18 Explorer Board, then plug the PIM into the demo board.
5. Apply power to the demo board. Be sure that VDD is correct for the device being used (3.3V for PIC18F46J11). If it is not correct, disconnect power and check that the 3-pin female header is aligned properly with the demo board's 3-pin riser.

Changes to PICDEM PIC18 Explorer Board Configuration

The difference of available I/O pins between the PICDEM PIC18 Explorer Board's PIC18F8722 device and the PIM's PIC18F46J11 device causes some changes in the operation of the PICDEM PIC18 Explorer Board.

1. Most I/O lines connected to the PICDEM PIC18 Explorer Board's PICtail connector, J3/J5/J7/J11 silkscreen, will map 1:1 with PIC18F46J11 I/O pins.
2. Port I/O pins RHx, RGx, RFx, RJx, RE3 through RE7 and RA4 will be left unconnected because these pins do not exist on the PIC18F46J11 devices.
3. Because RF6 is not implemented on PIC18F46J11 devices, the MCP23S17 I/O expander Reset signal can not be driven by the PIM example code unless a jumper wire is connected between RB5 and RF6 on the PIC18 Explorer Board's female headers. The example code should still work fine using power-off/Power-on Reset, even without the jumper.

PIC18F46J11 Plug-in Module for PICDEM™ PIC18 Explorer Board

Board Schematic

