

60 V, 600 mA PNP switching transistor 12 August 2016

Product data sheet

1. General description

PNP switching transistor in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

NPN complement: PMST2222A

2. Features and benefits

- General purpose switching transistor
- AEC-Q101 qualified

3. Applications

Switching and linear amplification

4. Quick reference data

Table 1. Quid	ck reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-60	V
I _C	collector current		-	-	-600	mA
h _{FE}	DC current gain	V_{CE} = -10 V; I_C = -150 mA; pulsed; t_p \leq 300 μ s; $\delta \leq~0.02~$; T_amb = 25 °C	100	-	300	

5. Pinning information

Table 2.	Pinning int	formation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	C
2	E	emitter		в
3	С	collector		
				E sym132
			1 2	
			SC-70 (SOT323)	



6. Ordering information

Table 3. Ordering information							
Type number	Package						
	Name	Description	Version				
PMST2907A	SC-70	plastic surface-mounted package; 3 leads	SOT323				

7. Marking

Table 4. Marking codes	
Type number	Marking code[1]
PMST2907A	%2F

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

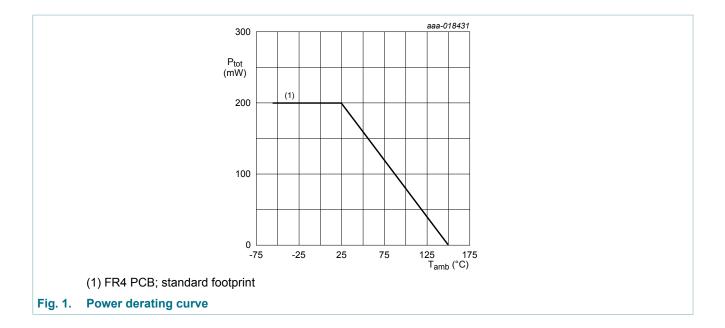
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-60	V
V _{CEO}	collector-emitter voltage	open base		-	-60	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-600	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-800	mA
I _{BM}	peak base current			-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

PMST2907A

60 V, 600 mA PNP switching transistor

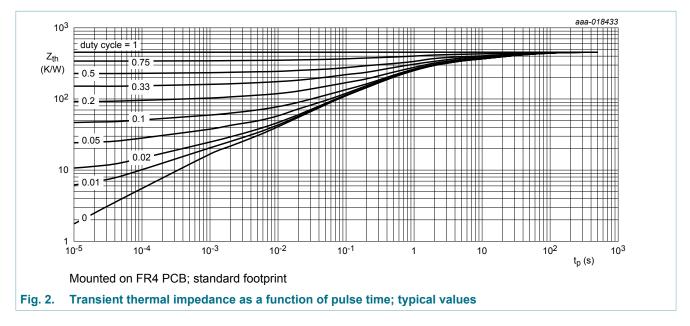


9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.



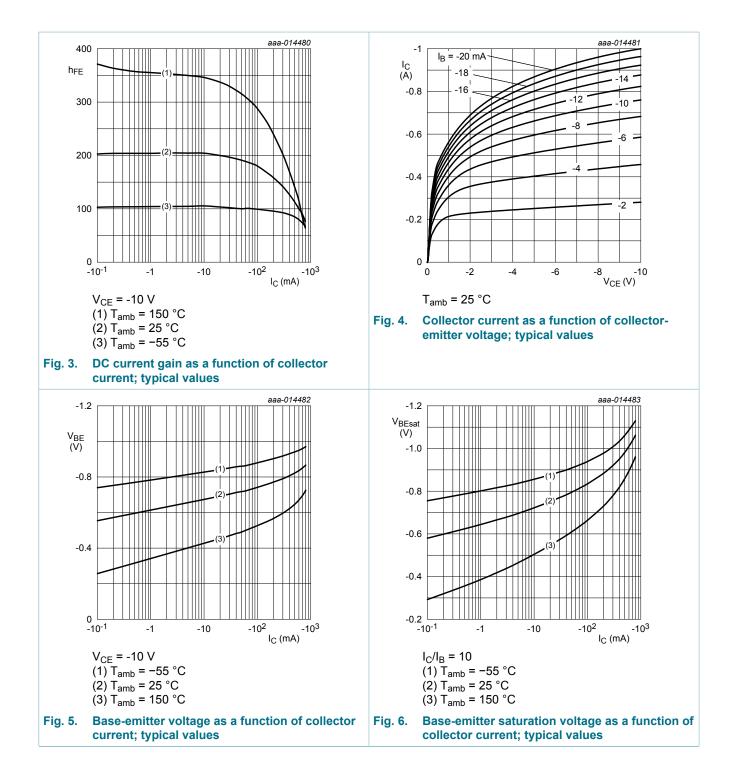
60 V, 600 mA PNP switching transistor

10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
I _{CBO}	collector-base cut-off	V_{CB} = -50 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-10	nA	
	current	V _{CB} = -50 V; I _E = 0 A; T _j = 125 °C	-	-	-10	μA	
I _{EBO}	emitter-base cut-off current	V_{EB} = -3 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-50	nA	
h _{FE}	DC current gain	V_{CE} = -10 V; I _C = -0.1 mA; T _{amb} = 25 °C	75	-	-		
		V_{CE} = -10 V; I _C = -1 mA; T _{amb} = 25 °C	100	-	-		
		$ V_{CE} = -10 \ V; \ I_C = -10 \ mA; \ pulsed; \ t_p \leq \\ 300 \ \mu s; \ \delta \leq \ 0.02 \ ; \ T_{amb} = 25 \ ^\circ C $	100	-	-		
			$ \begin{array}{l} V_{CE} = -10 \; V; \; I_C = -150 \; mA; \; pulsed; \; t_p \leq \\ 300 \; \mu s; \; \delta \leq \; 0.02 \; \; ; \; T_{amb} = 25 \; ^\circ C \end{array} $	100	-	300	
		V_{CE} = -10 V; I_C = -500 mA; pulsed; t_p ≤ 300 $\mu s; \delta \le 0.02 $; T_amb = 25 °C	50	-	-		
V _{CEsat}	collector-emitter saturation voltage	I_C = -150 mA; I_B = -15 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02 ; T_{amb} = 25 °C	-	-	-400	mV	
		I _C = -500 mA; I _B = -50 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02 ; T _{amb} = 25 °C	-	-	-1.6	V	
V _{BEsat}	base-emitter saturation voltage	I_C = -150 mA; I_B = -15 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02 ; T_{amb} = 25 °C	-	-	-1.3	V	
		I_C = -500 mA; I_B = -50 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02 ; T_{amb} = 25 °C	-	-	-2.6	V	
t _d	delay time	$I_{\rm C}$ = -150 mA; $I_{\rm Bon}$ = -15 mA;	-	-	15	ns	
t _r	rise time	I _{Boff} = 15 mA; T _{amb} = 25 °C	-	-	35	ns	
t _{on}	turn-on time		-	-	45	ns	
t _s	storage time		-	-	250	ns	
t _f	fall time		-	-	50	ns	
t _{off}	turn-off time		-	-	300	ns	
C _C	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	8	pF	
C _E	emitter capacitance		-	-	30	pF	
f _T	transition frequency	V _{CE} = -20 V; I _C = -50 mA; f = 100 MHz; T _{amb} = 25 °C; Pulse test: t _p ≤ 300 µs; $\overline{0}$ ≤ 0.02	200	-	-	MHz	

PMST2907A

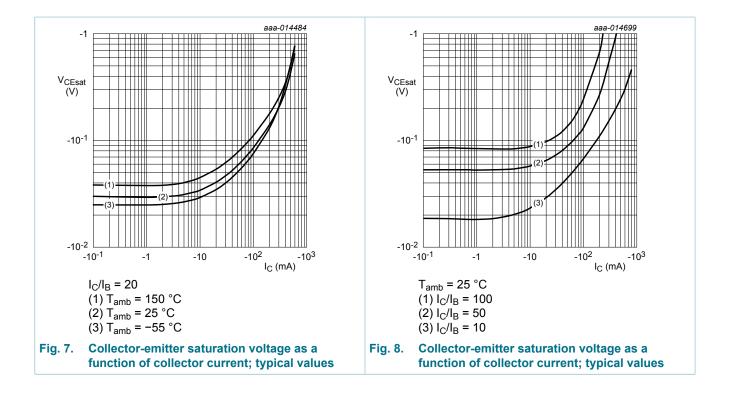
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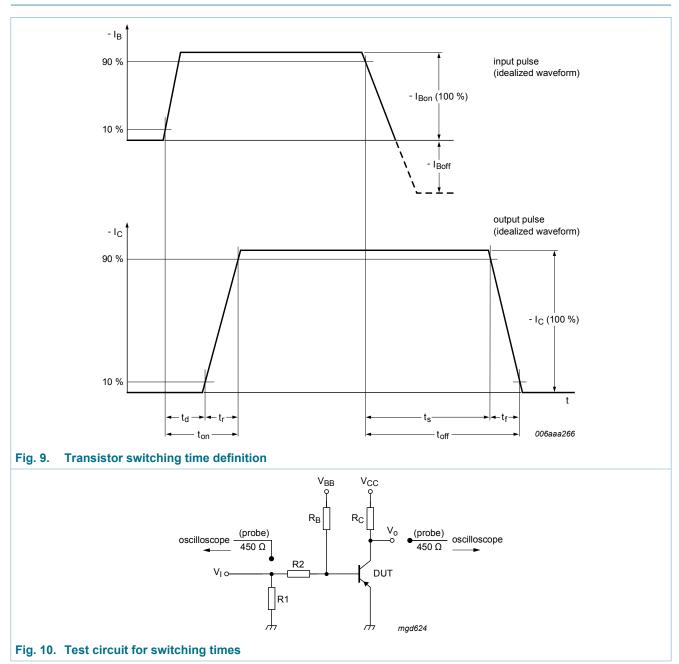
PMST2907A

60 V, 600 mA PNP switching transistor



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60 V, 600 mA PNP switching transistor



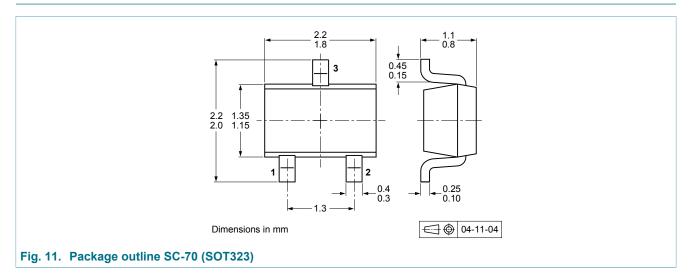
11. Test information

Quality information

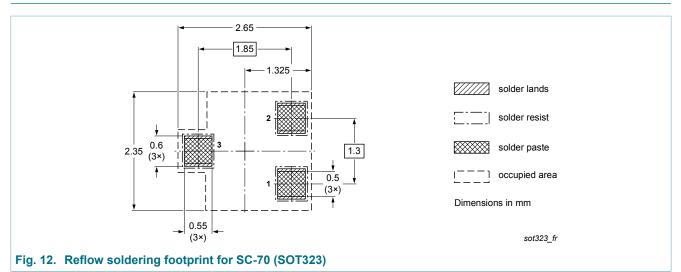
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

60 V, 600 mA PNP switching transistor

12. Package outline

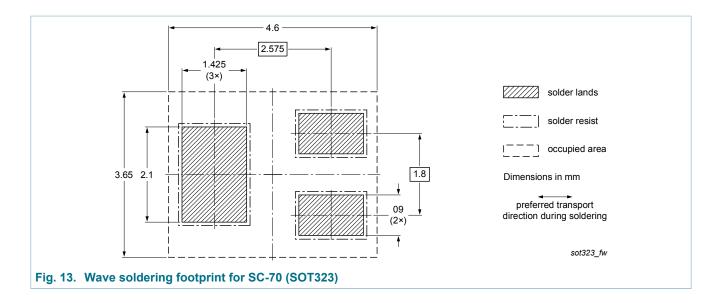


13. Soldering



PMST2907A

60 V, 600 mA PNP switching transistor



PMST2907A

60 V, 600 mA PNP switching transistor

14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMST2907A v.4	20160812	Product data sheet	-	PMST2907A v.3
Modifications:	of NXP Semico Legal texts hav Figures 1 to 8: Section 11. Tes Package outline Section 13. Sol	e been adapted to the new o added at information: added e: updated	0 17	, ,
PMST2907A v.3	20011119	Product data sheet	-	PMST2907A v.2
PMST2907A v.2	19990422	Product data sheet	-	PMST2907A v.1
PMST2907A v.1	19970708	Product data sheet		

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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PMST2907A

60 V, 600 mA PNP switching transistor

16. Contents

1.	General description	1
2.	Features and benefits	. 1
3.	Applications	. 1
4.	Quick reference data	1
5.	Pinning information	1
6.	Ordering information	2
7.	Marking	. 2
8.	Limiting values	. 2
9.	Thermal characteristics	. 3
10.	Characteristics	4
11.	Test information	. 7
12.	Package outline	. 8
13.	Soldering	. 8
14.	Revision history	10
15.	Legal information	11

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