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Specification	
Part Number:	MCT043LCA0C1W480272LML
Version:	1
Date:	25/05/2016

Revision

VERSION DA	TE	REVISED PAGE NO.	Note
0	2016/04/13		First issue

Display Size	4.3”
Resolution	480 x 272
VGA Size	HVGA
Orientation	Landscape
Appearance	RGB
Logic Voltage	3.1V
Interface	Parallel
Brightness	300 cd/m^2
Touchscreen	Capacitive
Module Size W x H x D	106.70 x 83.98 x 9.15 mm
Operating Temperature	-20°C ~ +70°C
Pin Out	36 - Way

RoHS
2011/65/EU

Contents

Midas Active Matrix Display Part Number System

MC	T	057	A	6	*	W	320240	L	M	L	*	*
1	2	3	4	5	6	7	8	9	10	11	12	13

1 = **MC:** Midas Components

2 = **T:** TFT **A:** Active Matrix OLED **M:** Monitor

3 = **Size**

4 = **Series**

5 = **Viewing Angle: 6:** 6 O'clock **12:** 12 O'clock **O:** All Round Viewing Angle

6 = **Blank:** No Touch **T:** Resistive Touchscreen **C:** Capacitive Touchscreen

7 = **Operating Temp Range:** **S:** 0+50Deg C **B:** -20+60Deg C **W:** -20+70Deg C **E:** -30+85Deg C **X:** -30+80Deg C

8 = **No of Pixels**

9 = **Orientation:** **P:** Portrait **L:** Landscape

10 = **Mode:** **R:** Reflective **M:** Transmissive **T:** Transflective **S:** Sunlight Readable (Transmissive) **W:** White on Black (Monochrome)

11 = **Backlight:** **Blank:** None **L:** LED **C:** CCFL

12 = **Blank:** No Module/board **C:** Controller board module (E-Tech)

13 = **Blank:** None **OB:** Optically Bonded **IPS:** In-plane switching

2.Summary

This technical specification applies to 4.3’ color TFT-LCD panel. The 4.3’ color TFT-LCD panel is designed for camcorder, digital camera application and other electronic products which require high quality flat panel displays. This module follows RoHS.

3.General Specifications

- Size: 4.3 inch
- Dot Matrix: 480 x RGBx272(TFT) dots
- Module dimension: 106.7 x 83.98 x 9.15 mm
- Active area: 95.04 x 53.86 mm
- Dot pitch: 0.066 x 0.198 mm
- LCD type: TFT, Normally White, Transmissive
- View Direction: 12 o'clock
- Gray Scale Inversion Direction: 6 o'clock
- Backlight Type: LED, Normally White
- Controller IC: SSD1963
- CTP FW Version: 08
- Interface: Digital 8080 family MPU 8bit/16bit
- With /Without TP: With CTP
- Surface: Glare

*Color tone slight changed by temperature and driving voltage.

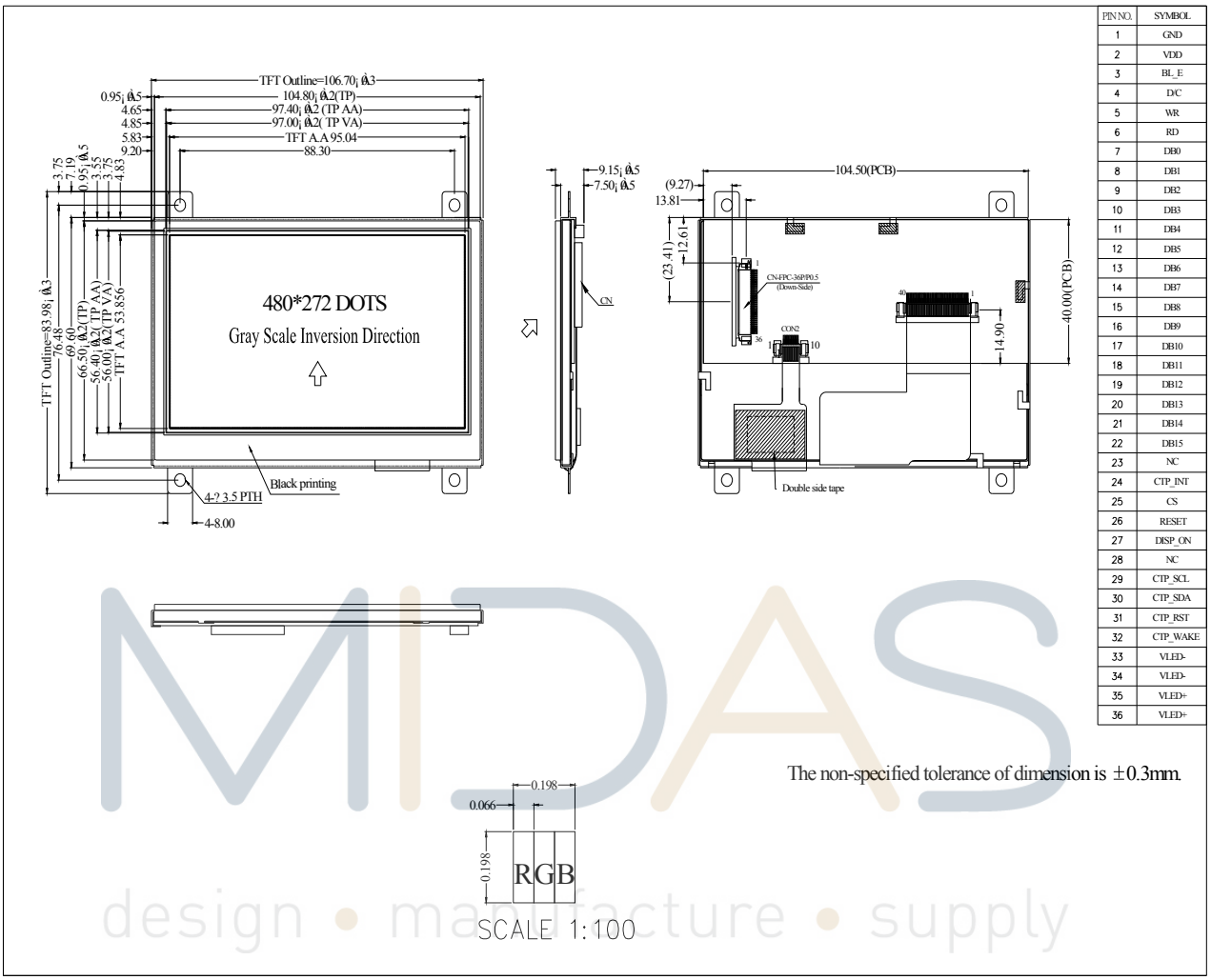
4.Interface

4.1. LCM PIN Definition (CON3)

Pin S	ymbol	Function	Remark
1	GND	System round pin of the IC. Connect to system ground.	
2	VDD	Power Supply : +3.3V	
3	BL_E	Backlight control signal , H: On \ L: Off	
4	D/C	Data/Command select	
5	WR	Write strobe signal	
6	RD	Read strobe signal	
7	D0	Data bus	
8	D1	Data bus	
9	D2	Data bus	
10	D3	Data bus	
11	D4	Data bus	
12	D5	Data bus	
13	D6	Data bus	
14	D7	Data bus	
15	D8	Data bus (When select 8bits mode, this pin is NC)	Note1
16	D9	Data bus (When select 8bits mode, this pin is NC)	Note1
17	D10	Data bus (When select 8bits mode, this pin is NC)	Note1
18	D11	Data bus (When select 8bits mode, this pin is NC)	Note1
19	D12	Data bus (When select 8bits mode, this pin is NC)	Note1
20	D13	Data bus (When select 8bits mode, this pin is NC)	Note1
21	D14	Data bus (When select 8bits mode, this pin is NC)	Note1
22	D15	Data bus (When select 8bits mode, this pin is NC)	Note1
23	NC	No connection	
24	CTP_INT	External interrupt to the host	
25	CS	Chip select	
26	RESET	Hardware reset	
27	DIP ON	Display control H: On \ L:Off	
28	NC	No connection	
29	CTP_SCL	Active low I2C clock input	
30	CTP_SDA	Data input I2C data input and output	
31	CTP_RST	External Reset, Low is active	
32	CTP_WAKE	External interrupt from the host	
33	VLED-	VLED- for B/L LED inverter (GND)	
34	VLED-	VLED- for B/L LED inverter (GND)	
35	VLED+	VLED+ for B/L LED inverter (+3.3V)	
36	VLED+	VLED+ for B/L LED inverter (+3.3V)	

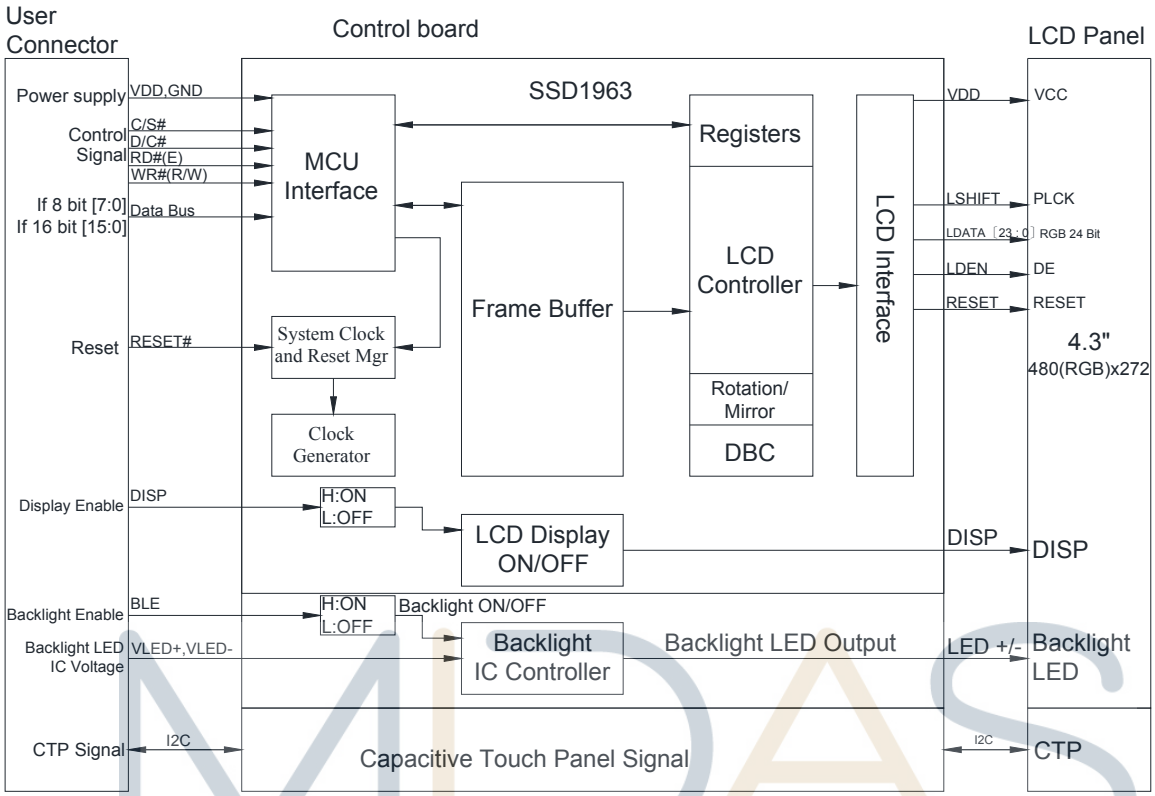
Note1: When select 8bit mode, DB0~DB7 be used, DB8~DB15 no connect
When select 16bit mode, DB0~DB15 be used

5.Contour Drawing



PIN NO.	SYMBOL
1	GND
2	VDD
3	BL_E
4	D/C
5	WR
6	RD
7	DB0
8	DB1
9	DB2
10	DB3
11	DB4
12	DB5
13	DB6
14	DB7
15	DB8
16	DB9
17	DB10
18	DB11
19	DB12
20	DB13
21	DB14
22	DB15
23	NC
24	CTP_INT
25	CS
26	RESET
27	DISP_ON
28	NC
29	CTP_SCL
30	CTP_SDA
31	CTP_RST
32	CTP_WAKE
33	VLED-
34	VLED-
35	VLED+
36	VLED+

6.Block Diagram

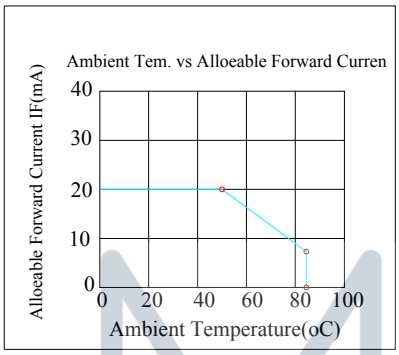


7.Absolute Maximum Ratings

Item Sy	mbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	□
Storage Temperature	TST	-30	—	+80	□

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp. ≦60℃, 90% RH MAX. Temp.＞60□, Absolute humidity shall be less than 90% RH at 60□



8.Electrical Characteristics

8.1. Operating conditions: (CON3.Pin1=GND, Pin2=VDD)

Item	Symbol	Condition	Min	Typ	Max	Unit	Remark
Supply Voltage For LCM	VDD	—	3.0	3.1	3.3	V	-
Supply Current For LCM	IDD	—	—	210	350	mA	Note1

Note 1 : This value is test for VDD=3.3V , Ta=25□ only

8.2. Backlight driving conditions (CON3.Pin33,34=VLED-, Pin35,36=VLED+)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Operation Current For LED Driver	VLED+=3.3V	270	-	405	mA	Note 1,2
Power Consumption	VLED+=3.3V	891	-	1337	mW	Note 1,2
Supply Voltage For LED Driver	VLED+	3.3	-	5	V	Note 1,2
LED Life Time		-	50,000	-	Hr	Note 2,3,4

Note 1 : Base on VLED= 3.3V for the back light driver IC specification
Note 2 : Ta = 25 □
Note 3 : Brightness to be decreased to 50% of the initial value
Note 4 : The single LED lamp case

9.DC CHARATERISTICS

Parameter Sy	mbol	Rating			Unit	Cond ition
		Min T	yp	Max		
Low level input voltage	V_{IL}	0	-	0.3VDD	V	
High level input voltage	V_{IH}	0.7VDD	-	VDD	V	

10.Interface timing

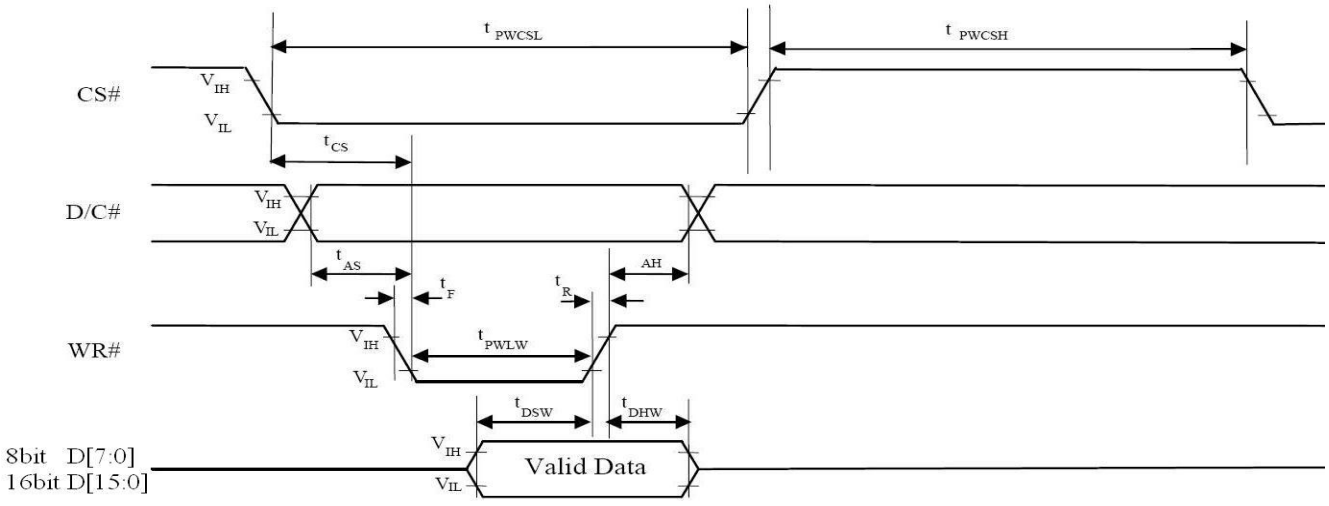
10.1. 8080 Mode 8bit/16bit

The 8080 mode MCU interface consist of CS#, D/C#, RD#, WR#, Data Bus signals. This interface use WR# to define a write cycle and RD# for read cycle. If the WR# goes low when the CS# signal is low, the data or command will be latched into the system at the rising edge of WR#. Similarly, the read cycle will start when RD# goes low and end at the rising edge of RD#.

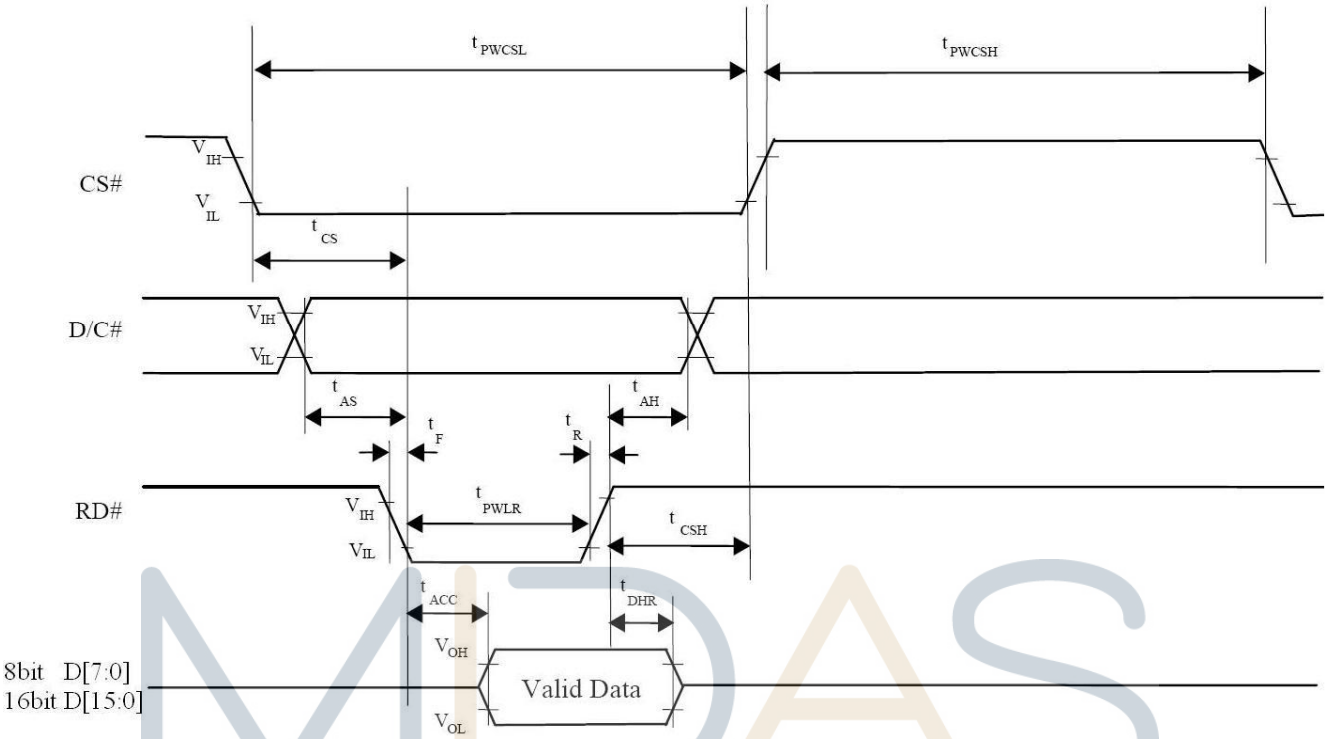
10.2. 8080 Mode Write Cycle

Symbol P	arameter	Min	Typ	Max	Unit
fMCLK	System Clock Frequency	1	-	110	MHz
tMCLK	System Clock Period	1/ fMCLK	-	-	ns
tPWCSH	Control Pulse High Width Write Read	13 30	1.5* **t**MCLK 3.5* **t**MCLK	-	ns
tPWCSL	Control Pulse Low Width Write (next write cycle) Write (next read cycle) Read	13 80 80	1.5* **t**MCLK 9* **t**MCLK 9* **t**MCLK	-	ns
tAS	Address Setup Time	1	-	-	ns
tAH	Address Hold Time	2	-	-	ns
tDSW	Write Data Setup Time	4			ns
tDHW	Write Data Hold Time	1	-	-	ns
tPWLW	Write Low Time	12			ns
tDHR	Read Data Hold Time	1	-	-	ns
tACC	Access Time	32			ns
tPWLR	Read Low Time	36	-	-	ns
tR	Rise Time	-		0.5	ns
tF	Fall Time	-	-	0.5	ns
tCS	Chip select setup time	2		-	ns
tCSH	Chip select hold time to read signal	3	-	-	ns

10.3. Parallel 8080-series Interface Timing Diagram(Write Cycle)



10.4. Parallel 8080-series Interface Timing Diagram(Read Cycle)



10.5. Pixel Data Format

Interface	Cycle	D[15]	D[14]	D[13]	D[12]	D[11]	D[10]	D[9]	D[8]	D[7]	D[6]	D[5]	D[4]	D[3]	D[2]	D[1]	D[0]
16 bits (565 format)	1st	R5	R4	R3	R2	R1	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1
16 bits	1st	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0
	2nd	B7	B6	B5	B4	B3	B2	B1	B0	R7	R6	R5	R4	R3	R2	R1	R0
	3rd	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
8 bits	1st									R7	R6	R5	R4	R3	R2	R1	R0
	2nd									G7	G6	G5	G4	G3	G2	G1	G0
	3rd									B7	B6	B5	B4	B3	B2	B1	B0

11.Optical Characteristics

Item		Symbol	Condition.	Min	Typ.	Max.	Unit	Remark
Response time		Tr+ Tf	θ=0°、Φ=0°	-	30	45	.ms	Note 3
Contrast ratio		CR	At optimized viewing angle	250	350	-	-	Note 4
Color Chromaticity	White	Wx	θ=0°、Φ=0	0.28	0.30	0.33		Note 2,5
		Wy		0.31	0.33	0.36		
Viewing angle (Gray Scale Inversion Direction)	Hor.	ΘR	CR≧10	-	75	-	Deg.	Note 1
		ΘL		-	75	-		
	Ver.	ΦT		-	75	-		
		ΦB		-	75	-		
Brightness		-	-	250	300	-	cd/m^2	Center of display

Ta=25±2□, VLED /ILED = 3.3V /270mA

Note 1: Definition of viewing angle range

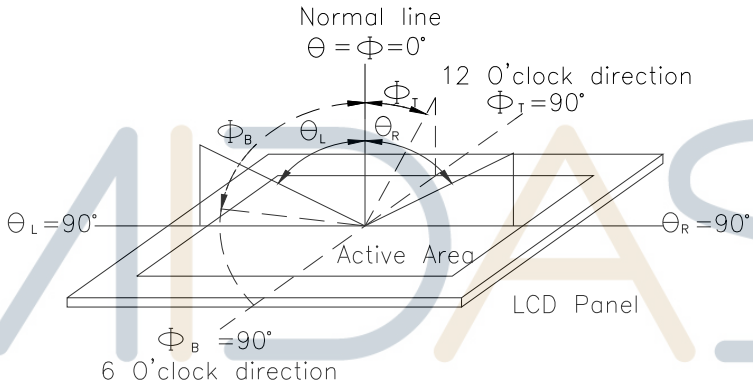


Fig. 11.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7 or BM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.

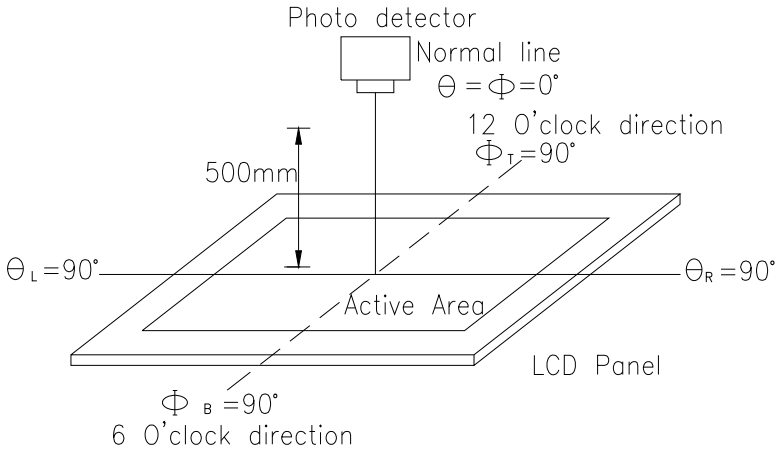
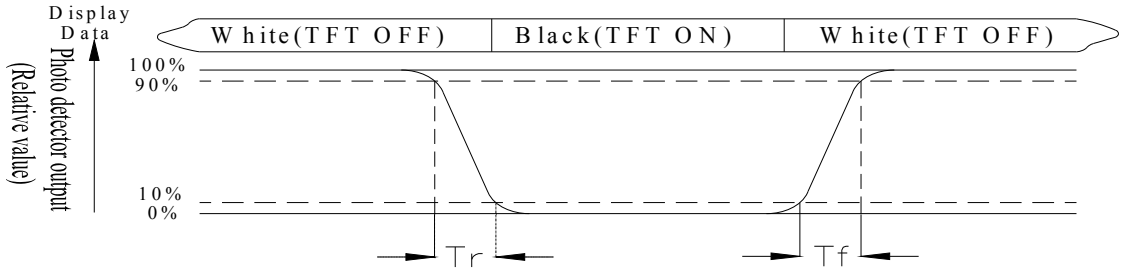


Fig. 11.2. Optical measurement system setup

Note 3: Definition of Response time:
The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time, Tr, is the time between photo detector output intensity changed from 90%to 10%. And fall time, Tf, is the time between photo detector output intensity changed from 10%to 90%



Note 4: Definition of contrast ratio:
The contrast ratio is defined as the following expression.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: White Vi = V_{i50} ± 1.5V
Black Vi = V_{i50} ± 2.0V
“±” means that the analog input signal swings in phase with VCOM signal.
“±” means that the analog input signal swings out of phase with VCOM signal.
The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

Note 6: Definition of color chromaticity (CIE 1931)
Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

12.Reliability

Content of Reliability Test (Wide temperature, -20□~70□)

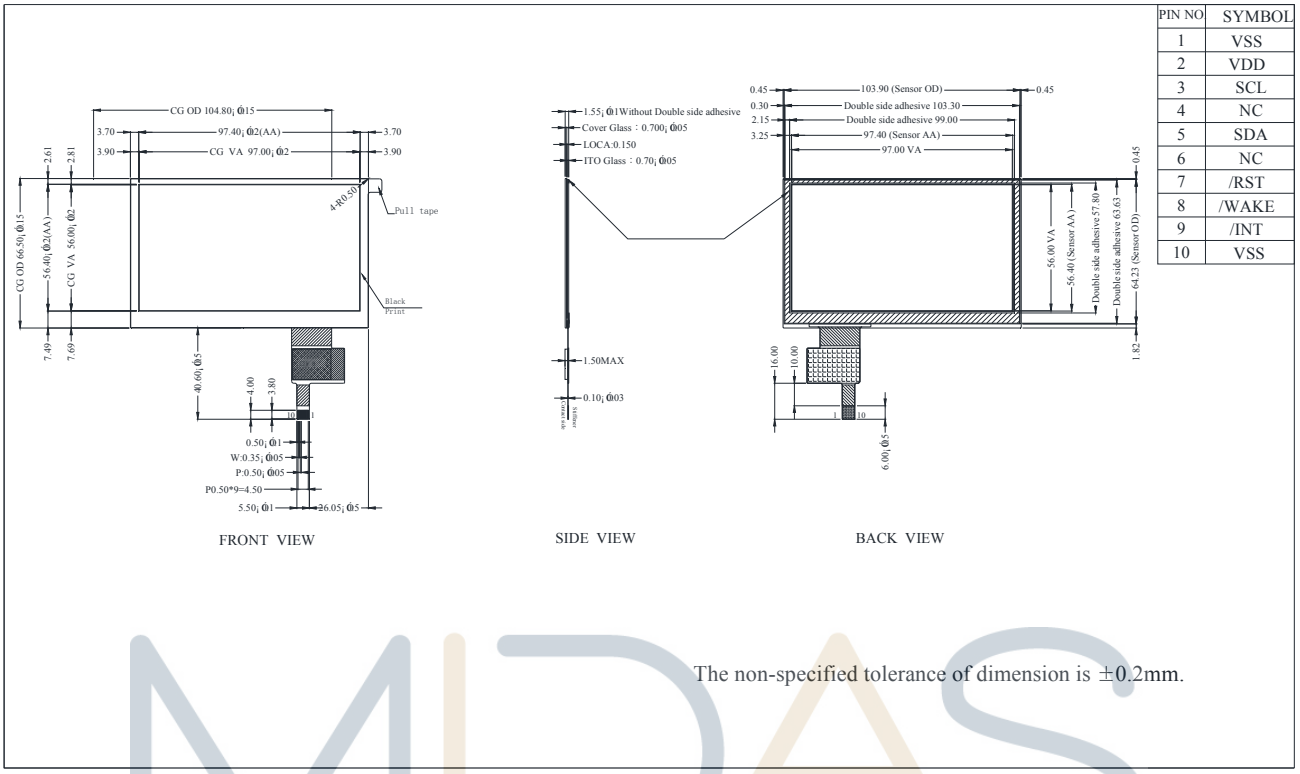
Environmental Test			
Test Item	**Content of Test**	**Test Condition**	**Note**
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80□ 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30□ 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70□ 200hrs	——
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20□ 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60□,90%RH max	60□,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -20□ 25□ 70□ 30min 5min 30min 1 cycle	-20□/70□ 10 cycles	——
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 15mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact) ,±800v(air), RS=330Ω CS=150pF 10 times	——

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

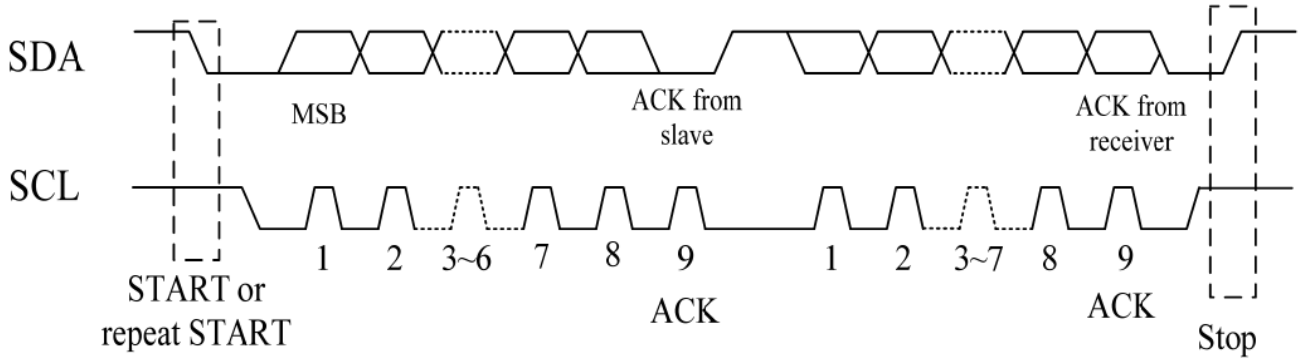
13.Touch Panel Information



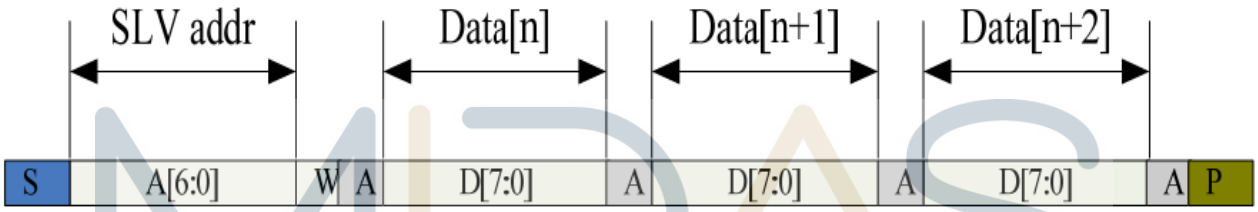
PIN NO	SYMBOL
1	VSS
2	VDD
3	SCL
4	NC
5	SDA
6	NC
7	/RST
8	/WAKE
9	/INT
10	VSS

The non-specified tolerance of dimension is ±0.2mm.

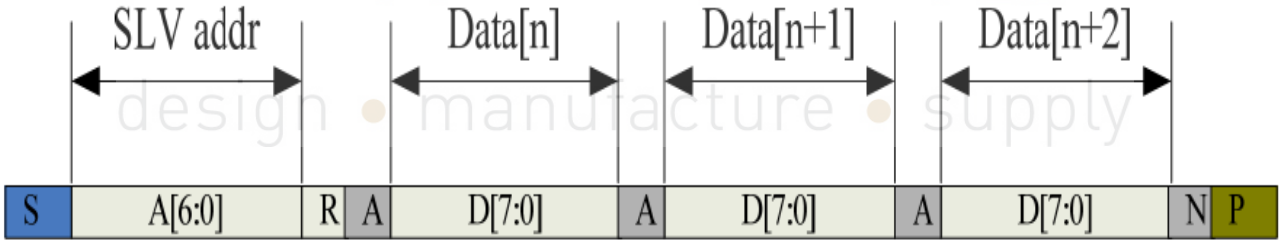
13.1. CTP I2C Timing:



I2C Serial Data Transfer Format



I2C master write, slave read



I2C master read, slave write

Mnemonics	Description
S	12C Start or 12C Restart
A[6:0]	Slave address
R/W	READ/WRITE bit, ‘1' for read, ‘0' for write
A(N)	ACK(NACK) bit
P	STOP: the indication of the end of a packet(if this bit is missing, S will indicate the end of the current packet and beginning of the next packet)

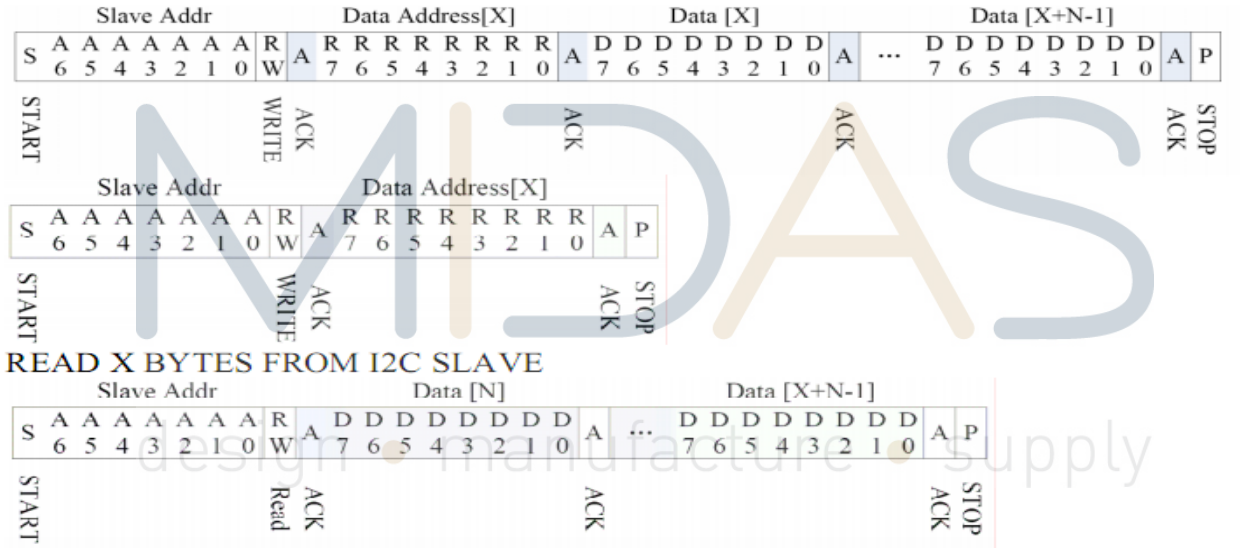
Lists the meanings of the mnemonics used in the above figures

Parameter Unit		Min	Max
SCL frequency	KHz	0	400
Bus free time between a STOP and START condition	us	4.7	\
Hold time (repeated) ST ART condition	us	4.0	\
Data setup time	ns	250	\
Setup time for a repeated START condition	us	4.7	\
Setup time for STOP condition	us	4.0	\

Interface Timing Characteristics

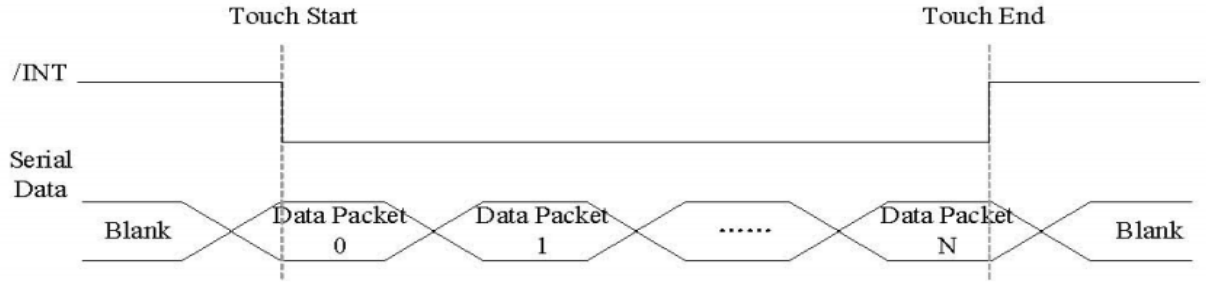
AS FOR STANDARD CTPM, HOST NEED TO USE BOTH INTERRUPT CONTROL SIGNAL AND SERIAL DATA INTERFACE TO GET THE TOUCH DATA.HERE IS THE TIMING TO GET TOUCH DATA.

13.2. WRITE BYTES TO I2C SLAVE



READ X BYTES FROM I2C SLAVE

AS FOR STANDARD CTPM, HOST NEED TO USE BOTH INTERRUPT CONTROL SIGNAL AND SERIAL DATA INTERFACE TO GET THE TOUCH DATA, HERE IS THE TIMING TO GET TOUCH DATA.



Address: 0x38

13.3. TOUCH DATA READ PROTOCOL

NAME	VALUE	DESCRIPTION
START CH	0X00	START COMMAND FOR CTPM TOUCH DATA PACKET,HOST MUST SEND CTPM A START CH COMMAND BEFORE READ TOUCH DATA
Lst READ BYTE～ LAST READ BYTE		TOUCH DATA PACKET SENT BY CTPM,EACH BYTE HAS 8-BIT DATA ,A TOUCH DATA PACKET CONSISTS OF N BYTE

A DATA PACKET STARTS WITH A HEADER AND ENDS WITH CRC CODE,AS FOR 5 POINTS DATA PACKET,THE LENGTH OF THE PACKET IS ALWAYS 26 BYTES IN SPITE OF ACTUAL TOUCH POINTS.

Address	Name	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0	Host Access
00h	Devide__Mode		Device Model[2:0]							RW
01h	Gest__ID	Gesture ID[7:0]								R
02h	TD__Status					Number of touch points[3:0]				R
03h	Touch1__XH	1st Event Flag				1st Touch X Position[11:8]				R
04h	Touch1__XL	1st Touch X Position[7:0]								R
05h	Touch1__YH	1st Touch ID[3:0]				1st Touch Y Position[11:8]				R
06h	Touch1__YL	1st Touch Y Position[7:0]								R
09h	Touch2__XH	2nd Event Flag				2nd Touch X Position[11:8]				R
0Ah	Touch2__XL	2nd Touch X Position[7:0]								R
0Bh	Touch2__YH	2nd Touch ID[3:0]				2ndTouch Y Position[11:8]				R
0Ch	Touch2__YL	2nd Touch Y Position[7:0]								R
0Fh	Touch3__XH	3rdEvent Flag				3rdTouch X Position[11:8]				R
10h	Touch3__XL	3rd Touch X Position[7:0]								R
11h	Touch3__YH	3rdTouch ID[3:0]				3rdTouch Y Position[11:8]				R

12h	Touch3__YL	3rd Touch Y Position[7:0]			R
15h	Touch4__XH	4thEvent Flag		4thTouch X Position[11:8]	R
16h	Touch4__XL	4th Touch X Position[7:0]			R
17h	Touch4__YH	4thTouch ID[3:0]		4thTouch Y Position[11:8]	R
18h	Touch4__YL	4th Touch Y Position[7:0]			R
1Bh	Touch5__XH	5thEvent Flag		5thTouch X Position[11:8]	R
1Ch	Touch5__XL	5th Touch X Position[7:0]			R
1Dh	Touch5__YH	5thTouch ID[3:0]		5thTouch Y Position[11:8]	R
1Eh	Touch5__YL	5th Touch Y Position[7:0]			R

14.PACKAGE SPECIFICATION

LCM Model	MCT043LCA0C1W
Drawing NO.	

LCM 包裝規格書
LCM Packaging Specifications

Approve	Check	Contact
DATE	初版	版次 Ver
14’02/17	13’12/16	A

1.包裝材料規格表（Packaging Material）:(per carton)

NO.	Item	Model	Dimensions	Quantity
1	成品（LCM）	MCT043LCA0C1W480272LML		144
2	TRAY 盤 (2)	PKCA1XXXXXXXXXXX0351	315 x 265 x 18.5	24+6
3	BP01 內盒(3)Product Box	PK3Y1XXXXXXXXXXX0001 332*280*100mm		6
4	泡棉(4)Foam	-----	----- 6	
5	外紙箱(5)Carton	PK4X1XXXXXXXXXXX0000 565*340*320mm		1
6				
7				
8				
9				

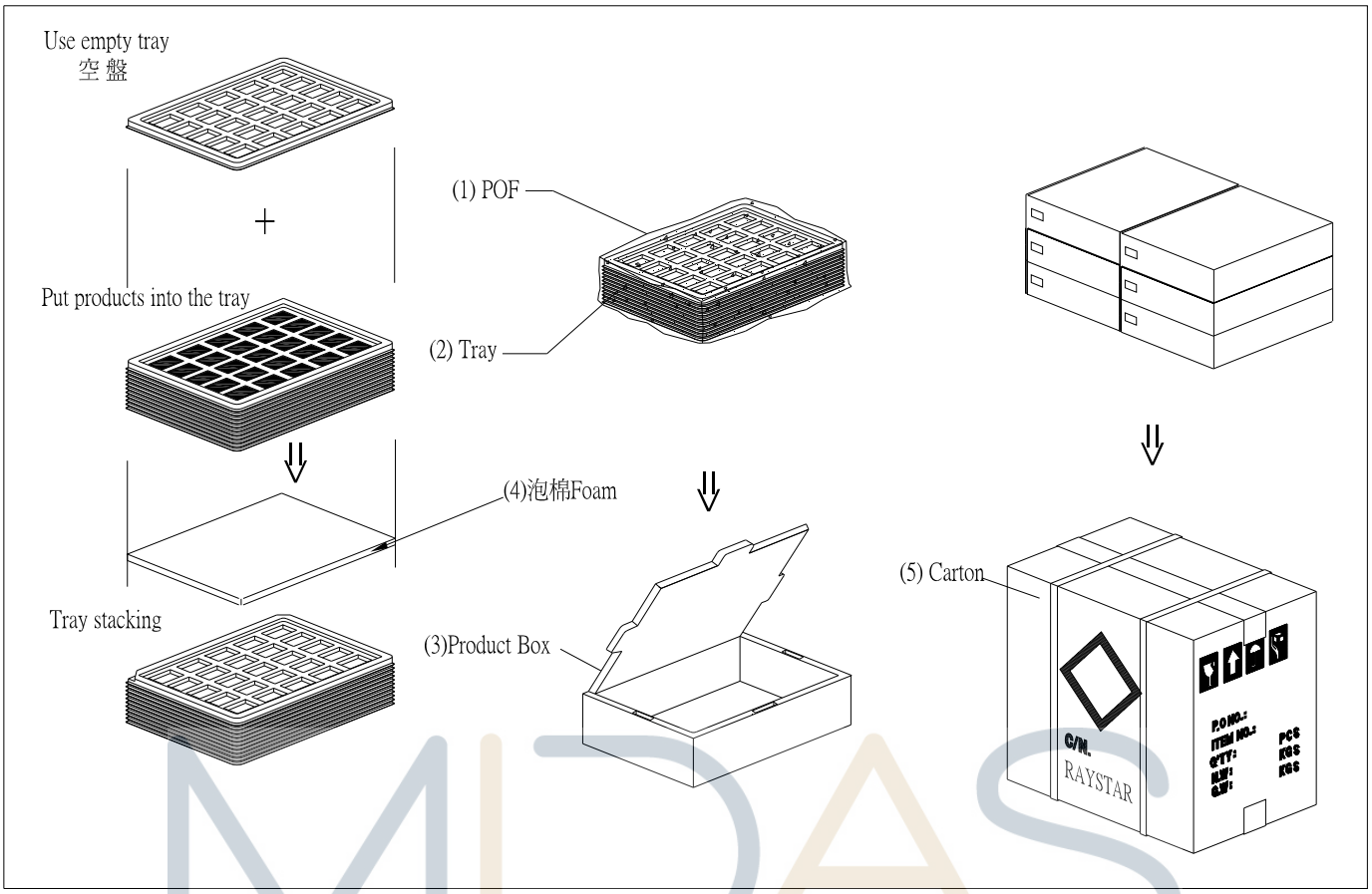
2.單箱數量規格表(Packaging Specifications and Quantity) :
(1)LCM quantity per box : no per tray 4 x no of tray 6 = 24
(2)Total LCM quantity in carton : quantity per box 24 x no of boxes 6 = 144

特 記 事 項 (REMARK)

1. Label Specifications :

MODEL:
LOT NO :
QUANTITY:
CHECK:

2.Rotate tray 180 degrees and place on top of stack. (TRAY 盤相疊時,需旋轉 180 度)



15.Initial Code For Reference

```
void Initial_code()
{
        Write_Command(0x01);
        Delay_ms(10);
        Write_Command(0xe0);
        Write_Parameter(0x01);
        Delay_ms(50);
        Write_Command(0xe0);
        Write_Parameter(0x03);
        Delay_ms(5);

        Write_Command(0xb0);
        Write_Parameter(0x08);
        Write_Parameter(0x80);
        Write_Parameter(0x01);
        Write_Parameter(0xdf);
        Write_Parameter(0x01);
        Write_Parameter(0x0f);
        Write_Parameter(0x00);


        Write_Command(0xf0);
        Write_Parameter(0x03); //0x03 is 16bit(565 format);0x00 is for 8-bit,pixel data format

        //Set the MN of PLL
        Write_Command(0xe2);
        Write_Parameter(0x1d);
        Write_Parameter(0x02);
        Write_Parameter(0x54);

        Write_Command(0xe6);
        Write_Parameter(0x01);
        Write_Parameter(0x99);
        Write_Parameter(0x9a);


        //Set front porch and back porch
        Write_Command(0xb4);
        Write_Parameter(0x02);
        Write_Parameter(0x0d);
        Write_Parameter(0x00);
        Write_Parameter(0x14);
        Write_Parameter(0x05);
        Write_Parameter(0x00);
        Write_Parameter(0x00);
        Write_Parameter(0x00);

        Write_Command(0xb6);
```

```
        Write_Parameter(0x01);
        Write_Parameter(0x24);
        Write_Parameter(0x00);
        Write_Parameter(0x0a);
        Write_Parameter(0x05);
        Write_Parameter(0x00);
        Write_Parameter(0x00);

        Write_Command(0x2a);
        Write_Parameter(0x00);
        Write_Parameter(0x00);
        Write_Parameter(0x01);
        Write_Parameter(0xdf);

        Write_Command(0x2b);
        Write_Parameter(0x00);
        Write_Parameter(0x00);
        Write_Parameter(0x01);
        Write_Parameter(0x0f);

        Write_Command(0x29);
        Write_Command(0x2c);
}
```