

BV _{CES}	400±30V
ا _C	30A
V _{CE(sat) (Typ.)}	1.6V
E _{AS}	300mJ

Features

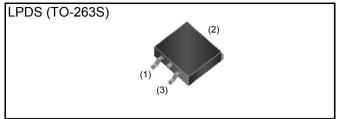
- 1) Low Collector Emitter Saturation Voltage
- 2) High Self-Clamped Inductive Switching Energy
- 3) Built in Gate-Emitter Protection Diode
- 4) Built in Gate-Emitter Resistance
- 5) Qualified to AEC-Q101
- 6) Pb free Lead Plating ; RoHS Compliant

Applications

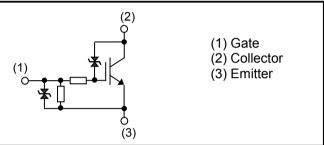
Ignition Coil Driver Circuits

Solenoid Driver Circuits

Outline



Inner Circuit



Packaging Specifications

	Packaging	Taping
	Reel Size (mm)	330
Tuno	Tape Width (mm)	24
Туре	Basic Ordering Unit (pcs)	1,000
	Packing Code	TL
	Marking	RGPR30NS40

•Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit	
Collector - Emitter Voltage	V _{CES}	430	V	
Emitter-Collector Voltage (V _{GE} = 0)	V _{EC}	25	V	
Gate - Emitter Voltage	V _{GES}	±10	V	
Collector Current	۱ _C	30	А	
Avalancha Energy (Single Dulae)	$T_j = 25^{\circ}C$	E _{AS}	300	mJ
Avalanche Energy (Single Pulse)	T _j = 150°C	E _{AS} *2	180	mJ
Power Dissipation	P _D	125	W	
Operating Junction Temperature	Tj	-40 to +175	°C	
Storage Temperature	T _{stg}	–55 to +175	°C	

•Thermal Resistance

Parameter	Symbol	Values			Unit
	Symbol	Min.	Тур.	Max.	Onit
Thermal Resistance IGBT Junction - Case	R _{θ(j-c)}	-	-	1.20	°C/W

•Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Deremeter	Symbol	Conditions	Values			L lus it	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
		I _C = 2mA, V _{GE} = 0V					
Collector - Emitter Breakdown Voltage	BV_{CES}	T _j = 25°C	370	400	430	V	
		$T_j = -40$ to $175^{\circ}C^{*2}$	365	-	435	V	
Emitter - Collector Breakdown Voltage	BV _{EC}	I _C = –10mA, V _{GE} = 0V	25	35	-	V	
Gate - Emitter Breakdown Voltage		$I_G = \pm 5 mA$, $V_{CE} = 0V$	±12	-	±17	V	
		V _{CE} = 250V, V _{GE} = 0V					
Collector Cut - off Current	I _{CES}	$T_j = 25^{\circ}C$	-	-	7	μA	
		$T_{j} = 150^{\circ}C^{*2}$	-	-	100	μA	
Gate - Emitter Leakage Current	I _{GES}	V_{GE} = ±10V, V_{CE} = 0V	±0.4	±0.6	±1.2	mA	
		V _{CE} = 5V, I _C = 12mA					
Gate - Emitter Threshold Voltage	$V_{\text{GE(th)}}$	T _j = 25°C	1.3	1.7	2.1	V	
		$T_{j} = 150^{\circ}C^{*2}$	-	1.3	-	V	
		I _C = 12A, V _{GE} = 5V					
Collector - Emitter Saturation Voltage	$V_{CE(sat)}$	T _j = 25°C	-	1.60	2.00	V	
		T _j = 150°C	-	1.80	-	V	
		I _C = 5A, V _{GE} = 4.5V					
Collector - Emitter Saturation Voltage	$V_{CE(sat)}$	T _j = 25°C	-	1.17	1.50	V	
-		T _j = 150°C	-	1.19	-	V	

•Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Devenuetor	Cumhal	Conditions	Values			L Locit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
		I _C = 12A, V _{GE} = 4V					
Collector - Emitter Saturation Voltage	V _{CE(sat)}	T _j = 25°C	-	1.70	2.10	V	
5		T _j = 150°C	-	1.90	-	V	
Input Capacitance	C _{ies}	V _{CE} = 10V	-	1330	-		
Output Capacitance	C _{oes}	V _{GE} = 0V	-	220	-	pF	
Reverse Transfer Capacitance	C _{res}	f = 1MHz	-	71	-		
Total Gate Charge	Qg	V _{CE} = 12V, I _C = 10A, V _{GE} = 5V	-	22	-	nC	
Turn - on Delay Time ^{*1,*2}	t _{d(on)}		0.11	0.19	0.50		
Rise Time ^{*1,*2}	t _r	$I_{\rm C} = 8A, V_{\rm CC} = 300V,$	0.10	0.18	0.50	μs	
Turn - off Delay Time ^{*1,*2}	t _{d(off)}	V _{GE} = 5V, R _G = 100Ω, L=5mH, T _j =25°C	0.9	1.4	4.0		
Fall Time ^{*1,*2}	t _f	f		1.8	5.5		
Turn - on Delay Time ^{*1}	t _{d(on)}		-	0.18	-	μs	
Rise Time ^{*1}	t _r	I _C = 8A, V _{CC} = 300V, V _{GE} = 5V, R _G = 100Ω,	-	0.21	-		
Turn - off Delay Time ^{*1}	t _{d(off)}	L=5mH, T_j =150°C	-	1.7	-		
Fall Time ^{*1}	t _f		-	3.0	-		
	E _{AS}	L = 5mH, V _{GE} = 5V, V _{CC} = 30V, R _G = 1kΩ,					
Avalanche Energy (Single Pulse)		T _j = 25°C	300	-	-	mJ	
		$T_{j} = 150^{\circ}C^{*2}$	180	-	-	mJ	
Gate Series Resistance	R _G		70	100	130	Ω	
Gate - Emitter Resistance	R_{GE}		8	16	24	kΩ	

*1) Assurance items according to our measurement definition (Fig.18)

*2) Design assurance items

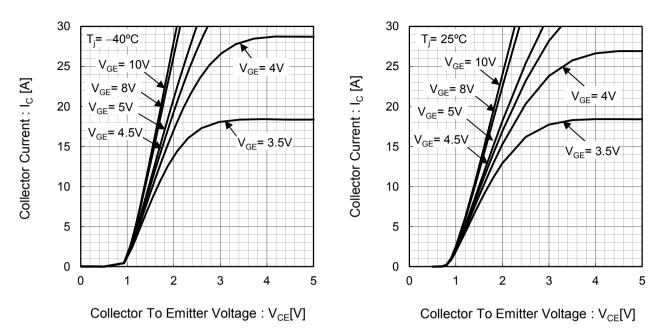
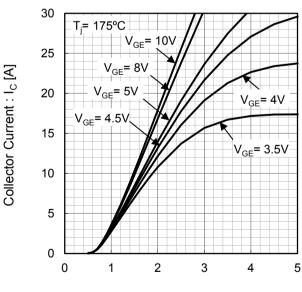


Fig.1 Typical Output Characteristics

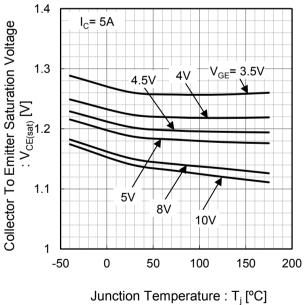
Fig.2 Typical Output Characteristics

Fig.3 Typical Output Characteristics



Collector To Emitter Voltage : $V_{CE}[V]$

Fig.4 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature



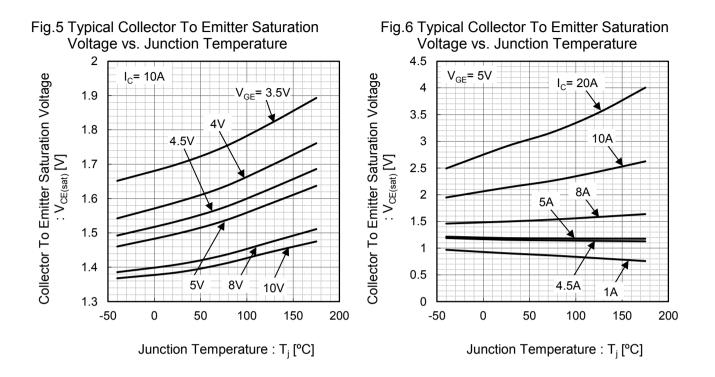
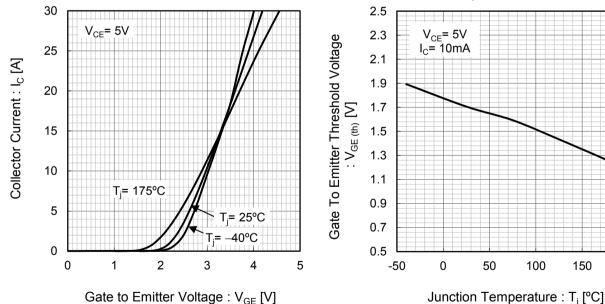


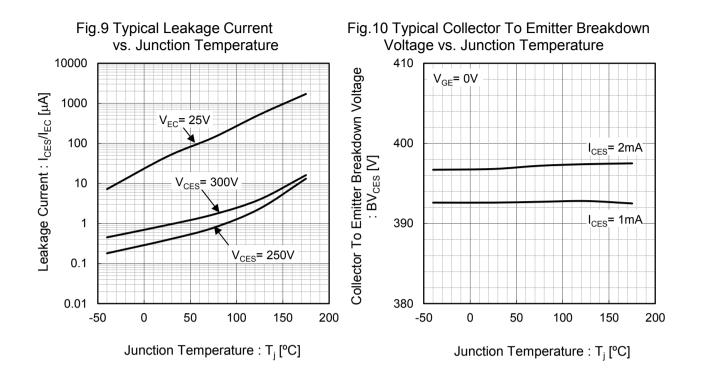
Fig.7 Typical Transfer Characteristics

Fig.8 Typical Gate To Emitter Threshold Voltage vs. Junction Temperature



150

200



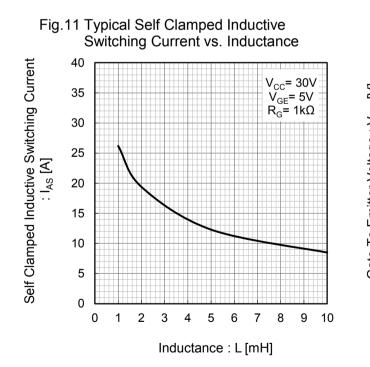
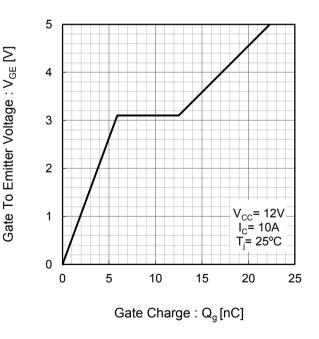


Fig.12 Typical Gate Charge



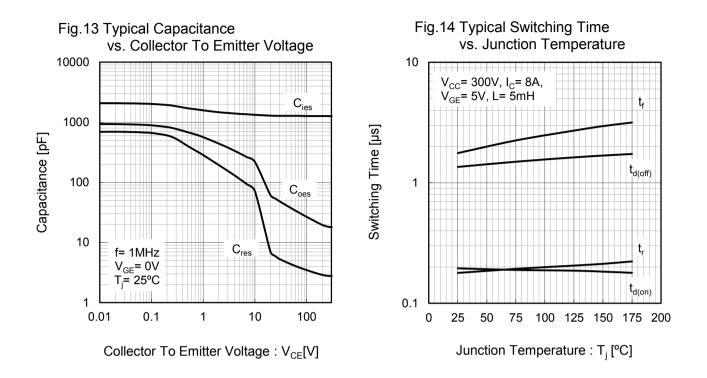
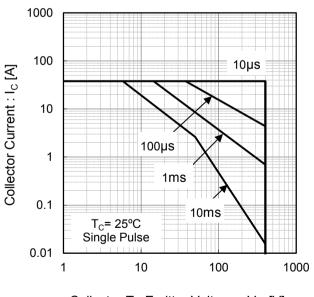


Fig.15 Forward Bias Safe Operating Area



Collector To Emitter	Voltage :	V _{CE} [V]
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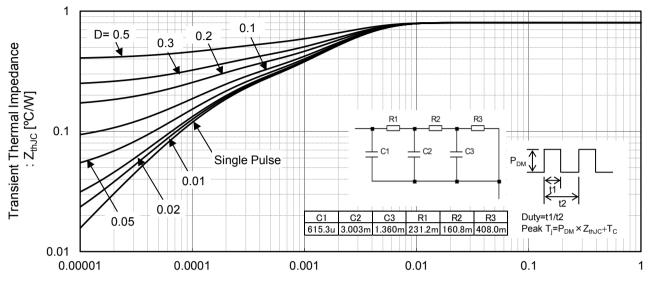


Fig.16 Transient Thermal Impedance

Pulse Width : t1[s]

Inductive Load Switching Circuit and Waveform

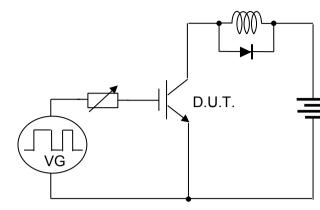


Fig.17 Inductive Load Switching Circuit

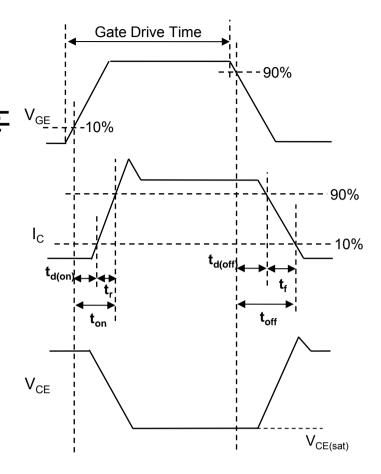


Fig.18 Inductive Load Switching Waveform

•Self Clamped Inductive Switching Circuit and Waveform

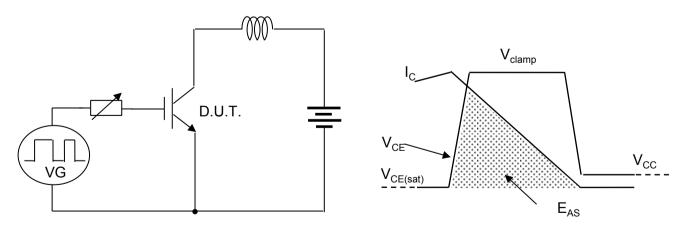


Fig.19 Self Clamped Inductive Switching Ciruit

Fig.20 Self Clamped Inductive Switching Waveform

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RGPR30NS40HR - Web Page

Part Number	RGPR30NS40HR
Package	LPDS
Unit Quantity	1000
Minimum Package Quantity	1000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes