



# LPC8N04 MCUs with Integrated NFC Technology

The LPC8N04 MCU offers a unique, tailored experience with flexible product customization and a cost-effective design. As the industry's first broad-market MCU with integrated NFC, it also broadens development possibilities across various manufacturing, safety and consumer goods applications.

## TARGET APPLICATIONS

- ▶ Configurable LED strips
- ▶ Data logger
- ▶ Smart toys
- ▶ Buttonless and contactless control panels
- ▶ Contactless diagnostics
- ▶ NFC e-Locker
- ▶ Smart manufacturing
- ▶ NFC OTA\*

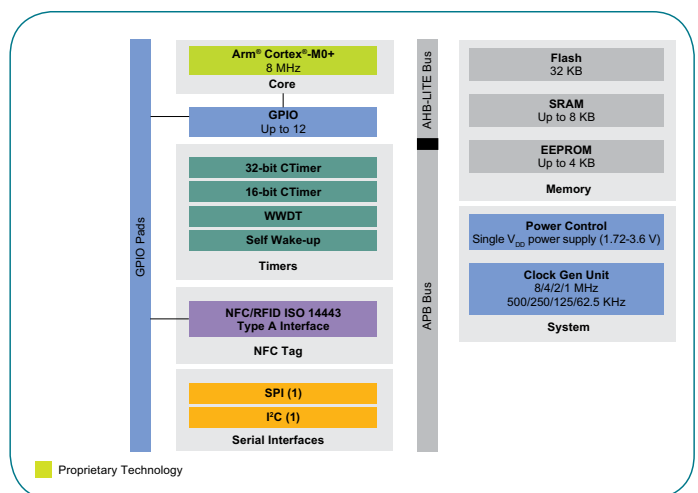
## OVERVIEW

Based on the Arm® Cortex®-M0+ core, the LPCN04 MCU is intentionally designed with simple integration on a small, low-cost and easy-to-use package (QFN24). This cost-effective 8- to 32-bit MCU upgrade serves as an entry-level connectivity solution for embedded applications with NFC connectivity for single-chip energy communication data harvesting via smartphone.

## ENABLEMENT

- ▶ OM40002 LPC8N04 development board
- ▶ Board support package (BSP) for IAR, Arm Keil® and MCUXpresso IDEs
- ▶ Supporting app notes to simplify design

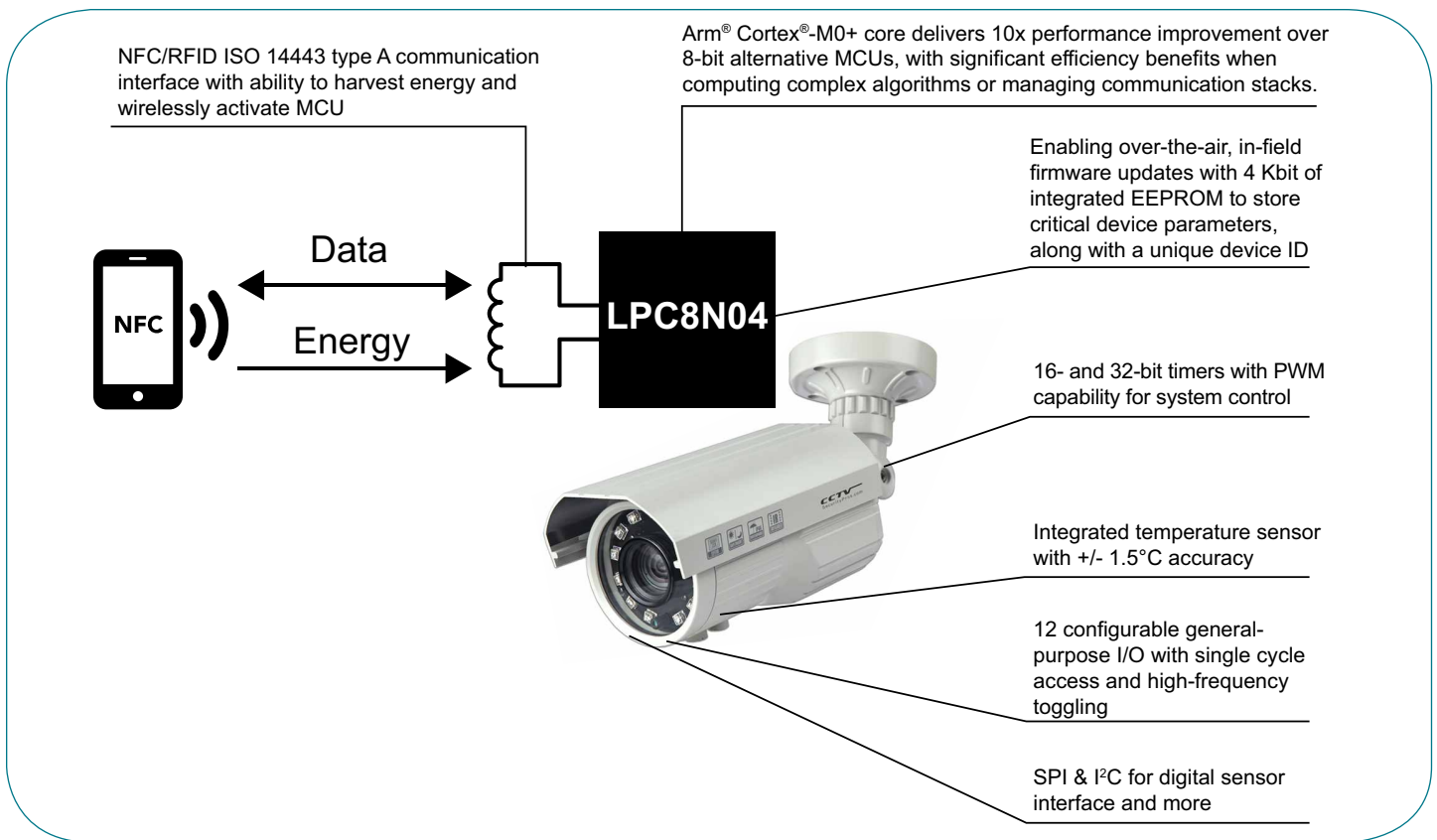
## LPC8N04 MCU BLOCK DIAGRAM



\*under development



## LPC8N04 MCU: CLOSE PROXIMITY, LOW-POWER WIRELESS CONNECTIVITY



## BENEFITS OF INTEGRATING NFC INTO THE BROAD MARKET LPC800 MCU SERIES

- ▶ In-field device reconfiguration and remote OTA updates via NFC
- ▶ Close proximity wireless connectivity to embedded applications at a fraction of the cost and power budget
- ▶ Contactless system control and monitoring
- ▶ Embedded data visualization through the rich phone user interface
- ▶ Secure and flexible product customization through cell phone connection, providing a uniquely tailored customer experience

[www.nxp.com/LPC8N04](http://www.nxp.com/LPC8N04)

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. Arm, Cortex and Keil are registered trademarks of Arm Limited (or its subsidiaries) in the EU and/or elsewhere. All rights reserved. © 2017 NXP B.V.

Date of Release: October 2017  
Document Number: LPC8N04FS REV 0