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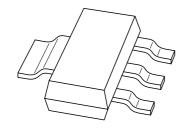
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Kind regards,

Team Nexperia

DISCRETE SEMICONDUCTORS

DATA SHEET



PZT2222ANPN switching transistor

Product data sheet Supersedes data of 1997 Jun 02 1999 Apr 14



NPN switching transistor

PZT2222A

FEATURES

- High current (max. 600 mA)
- Low voltage (max. 40 V).

APPLICATIONS

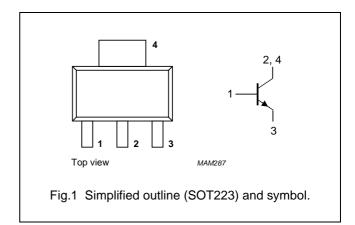
· Switching and linear amplification.

DESCRIPTION

NPN switching transistor in a SOT223 plastic package. PNP complement: PZT2907A.

PINNING

PIN	DESCRIPTION
1	base
2, 4	collector
3	emitter



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	75	V
V_{CEO}	collector-emitter voltage	open base	_	40	V
V _{EBO}	emitter-base voltage	open collector	_	6	V
I _C	collector current (DC)		_	600	mA
I _{CM}	peak collector current		_	800	mA
I _{BM}	peak base current		_	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	1.15	W
T _{stg}	storage temperature		-65	+150	°C
T _j	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Device mounted on a printed-circuit board, single-sided copper, tinplated, mounting pad for collector 1 cm². For other mounting conditions, see *"Thermal considerations for SOT223 in the General Part of associated Handbook"*.

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	109	K/W
R _{th j-s}	thermal resistance from junction to soldering point		28	K/W

Note

1. Device mounted on a printed-circuit board, single-sided copper, tinplated, mounting pad for collector 1 cm². For other mounting conditions, see *"Thermal considerations for SOT223 in the General Part of associated Handbook"*.

CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

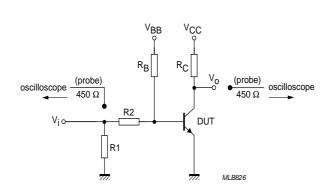
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 60 V	_	10	nA
		I _E = 0; V _{CB} = 60 V; T _{amb} = 125 °C	_	10	μΑ
I _{EBO}	emitter cut-off current	I _C = 0; V _{EB} = 5 V	_	10	nA
h _{FE}	DC current gain	$I_C = 0.1 \text{ mA}; V_{CE} = 10 \text{ V}$	35	_	
		$I_C = 1 \text{ mA}; V_{CE} = 10 \text{ V}$	50	_	
		$I_C = 10 \text{ mA}; V_{CE} = 10 \text{ V}$	75	_	
		$I_C = 10 \text{ mA}; V_{CE} = 10 \text{ V};$ $T_{amb} = -55 \text{ °C}$	35	_	
		I _C = 150 mA; V _{CE} = 1 V; note 1	50	_	
		I _C = 150 mA; V _{CE} = 10 V; note 1	100	300	
		I _C = 500 mA; V _{CE} = 10 V; note 1	40	_	
V _{CEsat}	collector-emitter saturation voltage	I _C = 150 mA; I _B = 15 mA	_	300	mV
		I _C = 500 mA; I _B = 50 mA	_	1	V
V _{BEsat}	base-emitter saturation voltage	I _C = 150 mA; I _B = 15 mA	0.6	1.2	V
		$I_C = 500 \text{ mA}; I_B = 50 \text{ mA}$	_	2	٧
C _c	collector capacitance	I _E = i _e = 0; V _{CB} = 10 V; f = 1 MHz	_	8	pF
C _e	emitter capacitance	$I_C = i_C = 0$; $V_{EB} = 500 \text{ mV}$; $f = 1 \text{ MHz}$	_	25	pF
f _T	transition frequency	$I_C = 20 \text{ mA}; V_{CE} = 20 \text{ V}; f = 100 \text{ MHz}$	300	_	MHz
Switching t	imes (between 10% and 90% levels); (see Fig.2)			
t _{on}	turn-on time	I _{Con} = 150 mA; I _{Bon} = 15 mA;	_	35	ns
t _d	delay time	$I_{Boff} = -15 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$	_	10	ns
t _r	rise time		_	25	ns
t _{off}	turn-off time		_	250	ns
t _s	storage time		_	200	ns
t _f	fall time		_	60	ns

Note

1. Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

NPN switching transistor

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$$\begin{split} &V_i = 9.5 \; V; \; T = 500 \; \mu s; \; t_p = 10 \; \mu s; \; t_r = t_f \leq 3 \; n s. \\ &R1 = 68 \; \Omega; \; R2 = 325 \; \Omega; \; R_B = 325 \; \Omega; \; R_C = 160 \; \Omega. \\ &V_{BB} = -3.5 \; V; \; V_{CC} = 29.5 \; V. \\ &Oscilloscope input impedance \; Z_i = 50 \; \Omega. \end{split}$$

Fig.2 Test circuit for switching times.

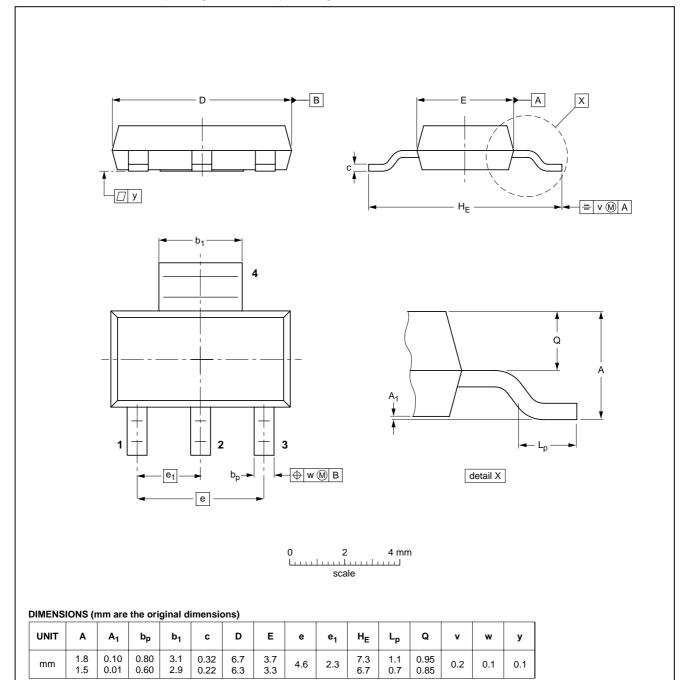
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PACKAGE OUTLINE

Plastic surface mounted package; collector pad for good heat transfer; 4 leads

SOT223



OUTLINE		REFERENCES			EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	EIAJ		PROJECTION	1330E DATE
SOT223			SC-73			97-02-28 99-09-13

NPN switching transistor

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DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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1999 Apr 14

NXP Semiconductors

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

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