



**Winstar Display Co., LTD**

**華凌光電股份有限公司**



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## SPECIFICATION

**CUSTOMER :** \_\_\_\_\_

**MODULE NO.:** **WG12864A-TMI-V#N**

|  |                     |              |
|--|---------------------|--------------|
| <b>APPROVED BY:</b><br><br>( FOR CUSTOMER USE ONLY ) | <b>PCB VERSION:</b> | <b>DATA:</b> |
|--|---------------------|--------------|

| SALES BY | APPROVED BY | CHECKED BY | PREPARED BY |
|----------|-------------|------------|-------------|
|          |             |            |             |

| VERSION | DATE     | REVISED<br>PAGE NO. | SUMMARY                          |
|---------|----------|---------------------|----------------------------------|
| B       | 2008/8/5 | 16                  | Modify backlight<br>information. |



DOC. FIRST ISSUE

## VERSION

REVISED  
PAGE NO.

## SUMMARY

8

16

2008/8/5

## Modify pin18= Vout

## Modify backlight information.

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# 1. Module Classification Information

W G    1 2 8 6 4    A—T M I—    V#N  
 ① ②                      ③                      ④    ⑤ ⑥ ⑦                      ⑧

① Brand : WINSTAR DISPLAY CORPORATION

② Display Type : H→Character Type, G→Graphic Type

③ Display Font : 128 x 64 dots

④ Model serials no.

⑤ Backlight Type :

|                     |               |
|---------------------|---------------|
| N→Without backlight | T→LED, White  |
| B→EL, Blue green    | A→LED, Amber  |
| D→EL, Green         | R→LED, Red    |
| W→EL, White         | O→LED, Orange |
| F→CCFL, White       | G→LED, Green  |
| Y→LED, Yellow Green |               |

⑥ LCD Mode :

|                              |                 |
|------------------------------|-----------------|
| B→TN Positive, Gray          | T→FSTN Negative |
| N→TN Negative,               |                 |
| G→STN Positive, Gray         |                 |
| Y→STN Positive, Yellow Green |                 |
| M→STN Negative, Blue         |                 |
| F→FSTN Positive              |                 |

⑦ LCD Polarize Type/ Temperature range/ View direction

|                            |                            |
|----------------------------|----------------------------|
| A→Reflective, N.T, 6:00    | H→Transflective, W.T,6:00  |
| D→Reflective, N.T, 12:00   | K→Transflective, W.T,12:00 |
| G→Reflective, W. T, 6:00   | C→Transmissive, N.T,6:00   |
| J→Reflective, W. T, 12:00  | F→Transmissive, N.T,12:00  |
| B→Transflective, N.T,6:00  | I→Transmissive, W. T, 6:00 |
| E→Transflective, N.T,12:00 | L→Transmissive, W.T,12:00  |

⑧ Special Code

V : Build in Negative Voltage ;

N : IC NT7107,NT7108;

#:Fit in with the ROHS Directions and regulations

## 2.Precautions in use of LCD Modules

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- (3) Don't disassemble the LCM.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.
- (8). Winstar have the right to change the passive components
- (9). Winstar have the right to change the PCB Rev.

## 3.General Specification

| Item                 | Dimension  | Unit |
|----------------------|--|------|
| Number of Characters | 128 x 64 dots  | —    |
| Module dimension     | 93 x 70.0 x 13.6(MAX)  | mm   |
| View area            | 72.0 x 40.0  | mm   |
| Active area          | 66.52 x 33.24  | mm   |
| Dot size             | 0.48 x 0.48  | mm   |
| Dot pitch            | 0.52 x 0.52  | mm   |
| LCD type             | STN, Negative, Transmissive ,Blue<br><br>(In LCD production, It will occur slightly color difference. We can only guarantee the same color in the same batch.) |      |
| Duty                 | 1/64   |      |
| View direction       | 6 o'clock  |      |
| Backlight Type       | LED White  |      |

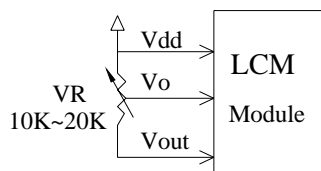
## 4. Absolute Maximum Ratings

| Item                     | Symbol           | Min | Typ | Max      | Unit |
|--------------------------|------------------|-----|-----|----------|------|
| Operating Temperature    | $T_{OP}$         | -20 | —   | +70      | °C   |
| Storage Temperature      | $T_{ST}$         | -30 | —   | +80      | °C   |
| Input Voltage            | $V_I$            | 0   | —   | $V_{DD}$ | V    |
| Supply Voltage For Logic | $V_{DD}$         | 0   | —   | 6.7      | V    |
| Supply Voltage For LCD   | $V_{DD}-V_{SS}$  | 0   | —   | 16.7     | V    |
| Supply Voltage For LCD   | $V_{DD}-V_{OUT}$ | —   | —   | -10      | V    |

## 5. Electrical Characteristics

| Item                            | Symbol          | Condition                 | Min | Typ  | Max      | Unit |
|---------------------------------|-----------------|---------------------------|-----|------|----------|------|
| Supply Voltage For Logic        | $V_{DD}-V_{SS}$ | —                         | 4.5 | 5.0  | 5.5      | V    |
| Supply Voltage For LCD<br>*Note | $V_{DD}-V_0$    | $T_a=-20^{\circ}\text{C}$ | —   | —    | 10.6     | V    |
|                                 |                 | $T_a=25^{\circ}\text{C}$  | 8.8 | 8.95 | 9.1      | V    |
|                                 |                 | $T_a=+70^{\circ}\text{C}$ | 7.6 | —    | —        | V    |
| Input High Volt.                | $V_{IH}$        | —                         | 2.0 | —    | $V_{DD}$ | V    |
| Input Low Volt.                 | $V_{IL}$        | —                         | 0   | —    | 0.8      | V    |
| Output High Volt.               | $V_{OH}$        | —                         | 2.4 | —    | $V_{DD}$ | V    |
| Output Low Volt.                | $V_{OL}$        | —                         | —   | —    | 0.4      | V    |
| Supply Current                  | $I_{DD}$        | —                         | —   | 18   | —        | mA   |

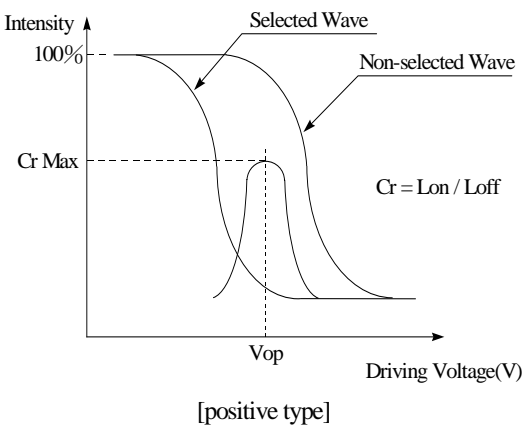
\* Note: Please design the VOP adjustment circuit on customer's main board



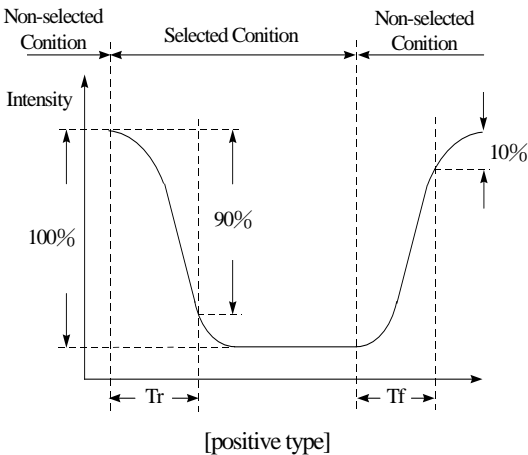
# 6.Optical Characteristics

| Item           | Symbol        | Condition   | Min | Typ | Max | Unit |
|----------------|---------------|-------------|-----|-----|-----|------|
| View Angle     | (V) $\theta$  | $CR \geq 2$ | 20  | —   | 40  | deg  |
|                | (H) $\varphi$ | $CR \geq 2$ | -30 | —   | 30  | deg  |
| Contrast Ratio | CR            | —           | —   | 3   | —   | —    |
| Response Time  | T rise        | —           | —   | 200 | 300 | ms   |
|                | T fall        | —           | —   | 200 | 300 | ms   |

## Definition of Operation Voltage (Vop)



## Definition of Response Time ( Tr , Tf )



### Conditions :

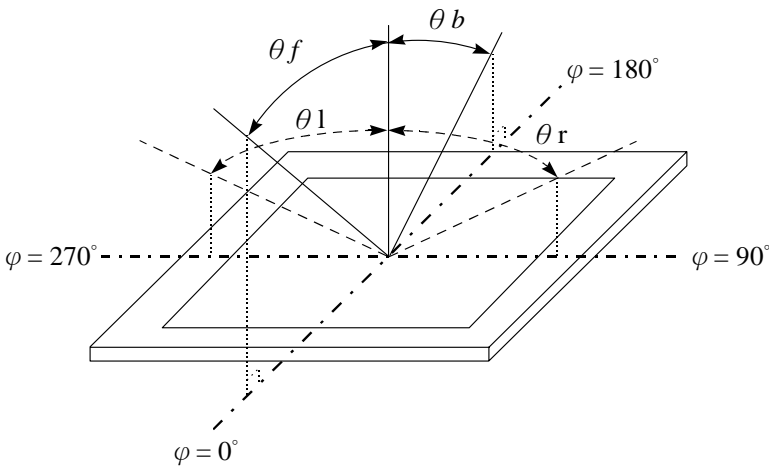
Operating Voltage : Vop

Viewing Angle(  $\theta$  ,  $\varphi$  ) : 0° , 0°

Frame Frequency : 64 HZ

Driving Waveform : 1/N duty , 1/a bias

## Definition of viewing angle( $CR \geq 2$ )

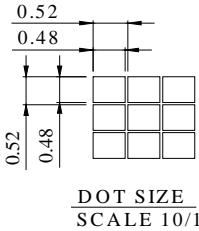
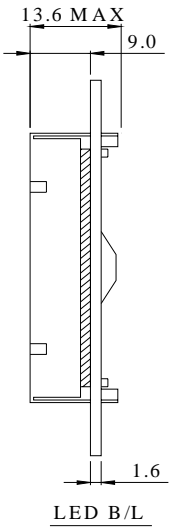
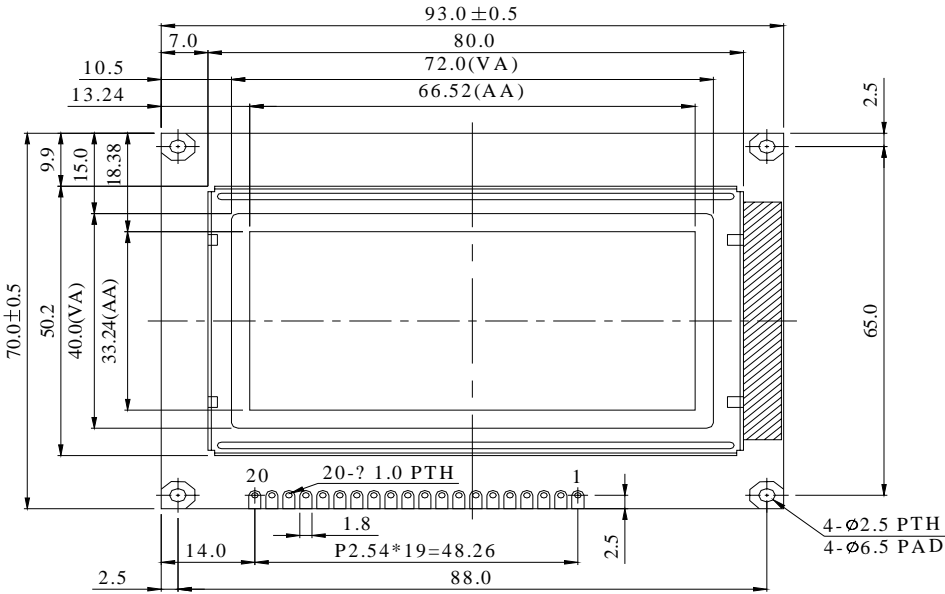


## 7.Interface Description

| Pin No. | Symbol          | Level      | Description                                  |
|---------|-----------------|------------|--|
| 1       | V <sub>SS</sub> | 0V         | Ground                                       |
| 2       | V <sub>DD</sub> | 5.0V       | Supply voltage for logic                     |
| 3       | V <sub>O</sub>  | (Variable) | Operating voltage for LCD                    |
| 4       | D/I             | H/L        | H: Data , L : Instruction                    |
| 5       | R/W             | H/L        | H: Read (MPU←Module) , L: Write (MPU→Module) |
| 6       | E               | H          | Enable signal                                |
| 7       | DB0             | H/L        | Data bus line                                |
| 8       | DB1             | H/L        | Data bus line                                |
| 9       | DB2             | H/L        | Data bus line                                |
| 10      | DB3             | H/L        | Data bus line                                |
| 11      | DB4             | H/L        | Data bus line                                |
| 12      | DB5             | H/L        | Data bus line                                |
| 13      | DB6             | H/L        | Data bus line                                |
| 14      | DB7             | H/L        | Data bus line                                |
| 15      | CS1             | H          | Select Column 1~ Column 64                   |
| 16      | CS2             | H          | Select Column 65~ Column 128                 |
| 17      | RES             | L          | Reset signal                                 |
| 18      | Vout            | —          | Negative voltage output                      |
| 19      | A               | —          | Power Supply for LED backlight ( + )         |
| 20      | K               | —          | Power Supply for LED backlight ( - )         |

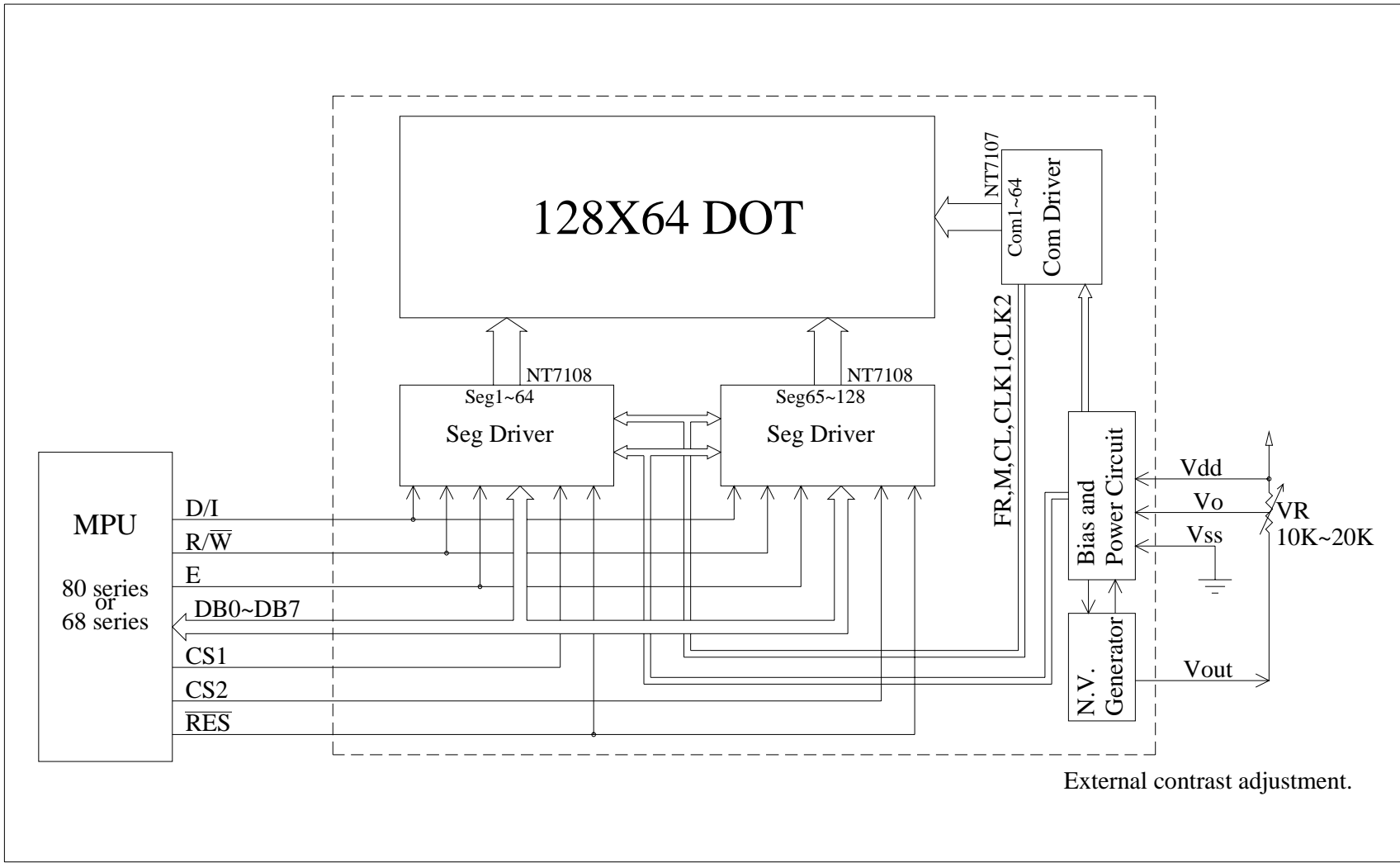


# 8.Contour Drawing &Block Diagram



| PIN NO. | SYMBOL            |
|---------|-------------------|
| 1       | V <sub>ss</sub>   |
| 2       | V <sub>dd</sub>   |
| 3       | V <sub>o</sub>    |
| 4       | D/I               |
| 5       | R/ $\overline{W}$ |
| 6       | E                 |
| 7       | DB0               |
| 8       | DB1               |
| 9       | DB2               |
| 10      | DB3               |
| 11      | DB4               |
| 12      | DB5               |
| 13      | DB6               |
| 14      | DB7               |
| 15      | CS1               |
| 16      | CS2               |
| 17      | $\overline{RES}$  |
| 18      | V <sub>out</sub>  |
| 19      | A                 |
| 20      | K                 |

The non-specified tolerance of dimension is ±0.3mm.

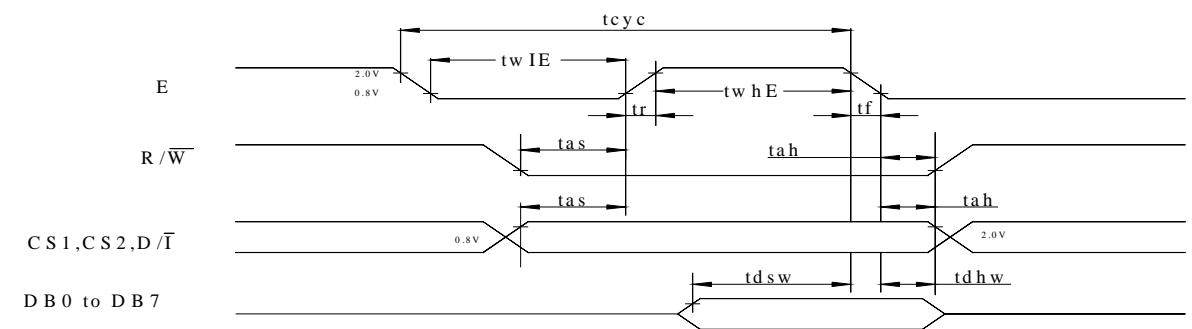


# 9. Timing Characteristics

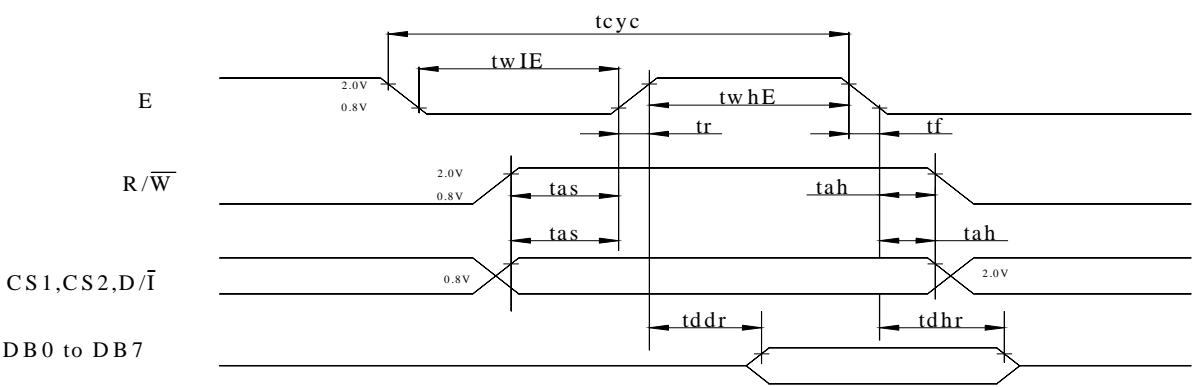
MPU Interface

(T=25°C, VDD=+5.0V±0.5)

| Characteristic         | Symbol           | Min  | Typ | Max | Unit |
|------------------------|------------------|------|-----|-----|------|
| E cycle                | t <sub>cy</sub>  | 1000 | —   | —   | ns   |
| E high level width     | t <sub>whE</sub> | 450  | —   | —   | ns   |
| E low level width      | t <sub>wlE</sub> | 450  | —   | —   | ns   |
| E rise time            | t <sub>r</sub>   | —    | —   | 25  | ns   |
| E fall time            | t <sub>f</sub>   | —    | —   | 25  | ns   |
| Address set-up time    | t <sub>as</sub>  | 140  | —   | —   | ns   |
| Address hold time      | t <sub>ah</sub>  | 10   | —   | —   | ns   |
| Data set-up time       | t <sub>dsw</sub> | 200  | —   | —   | ns   |
| Data delay time        | t <sub>ddr</sub> | —    | —   | 320 | ns   |
| Data hold time (write) | t <sub>dhw</sub> | 10   | —   | —   | ns   |
| Data hold time (read)  | t <sub>dhr</sub> | 20   | —   | —   | ns   |



MPU Write Timing



MPU Read Timing

# 10. Display Control Instruction

The display control instructions control the internal state of the NT7108. Instruction is received from MPU to NT7108 for the display control. The following table shows various instructions.

| Instruction                    | RS | R/W | DB7        | DB6 | DB5                       | DB4   | DB3 | DB2        | DB1 | DB0 | Function  |
|--------------------------------|----|-----|------------|-----|---------------------------|-------|-----|------------|-----|-----|---|
| Display on/off                 | L  | L   | L          | L   | H                         | H     | H   | H          | H   | L/H | Controls the display on or off. Internal status and display RAM data is not affected.<br>L: OFF, H: ON                    |
| Set address (Y address)        | L  | L   | L          | H   | Y address (0-63)          |       |     |            |     |     | Sets the Y address in the Y address counter.  |
| Set page (X address)           | L  | L   | H          | L   | H                         | H     | H   | Page (0-7) |     |     | Sets the X address at the X address register.   |
| Display Start line (Z address) | L  | L   | H          | H   | Display start line (0-63) |       |     |            |     |     | Indicates the display data RAM displayed at the top of the screen.  |
| Status read                    | L  | H   | Busy       | L   | On/Off                    | Reset | L   | L          | L   | L   | Read status.<br>BUSY L: Ready<br>H: In operation<br>ON/OFF L: Display ON<br>H: Display OFF<br>RESET L: Normal<br>H: Reset |
| Write display data             | H  | L   | Write data |     |                           |       |     |            |     |     | Writes data (DB0: 7) into display data RAM. After writing instruction, Y address is increased by 1 automatically.         |
| Read display data              | H  | H   | Read data  |     |                           |       |     |            |     |     | Reads data (DB0: 7) from display data RAM to the data bus.  |

# 11.Detailed Explanation

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 0  | 0   | 0   | 0   | 1   | 1   | 1   | 1   | 1   | D   |

**The display data appears when D is 1 and disappears when D is 0. Though the data is not on the**

screen with D=0, it remains in the display data RAM. Therefore, you can make it appear by changing D=0 into D=1.

## **SET ADDRESS (Y ADDRESS)**

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 0  | 0   | 0   | 1   | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 |

Y address (AC0-AC5) of the display data RAM is set in the Y address counter. An address is set by instruction and increased by 1 automatically by read or write operations of display data.

## **SET PAGE (X ADDRESS)**

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 0  | 0   | 1   | 0   | 1   | 1   | 1   | AC2 | AC1 | AC0 |

X address (AC0-AC2) of the display data RAM is set in the X address register. Writing or reading to or from MPU is executed in this specified page until the next page is set.

## **DISPLAY START LINE (Z ADDRESS)**

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 0  | 0   | 1   | 1   | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 |

Z address (AC0-AC5) of the display data RAM is set in the display start line register and displayed at the top of the screen. When the display duty cycle is 1/64 or others (1/32-1/64), the data of total line number of LCD screen, from the line specified by display start line instruction, is displayed.

## **STATUS READ**

| RS | R/W | DB7  | DB6 | DB5    | DB4   | DB3 | DB2 | DB1 | DB0 |
|----|-----|------|-----|--------|-------|-----|-----|-----|-----|
| 0  | 1   | BUSY | 0   | ON/OFF | RESET | 0   | 0   | 0   | 0   |

- BUSY

When BUSY is 1, the Chip is executing internal operation and no instructions are accepted.

When BUSY is 0, the Chip is ready to accept any instructions.

- ON/OFF

When ON/OFF is 1, the display is OFF.

When ON/OFF is 0, the display is ON.

- RESET

When RESET is 1, the system is being initialized.

In this condition, no instructions except status read can be accepted.

When RESET is 0, initializing has finished and the system is in usual operation condition.

## WRITE DISPLAY DATA

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 1  | 0   | D7  | D6  | D5  | D4  | D3  | D2  | D1  | D0  |

Writes data (D0-D7) into the display data RAM. After writing instruction, Y address is increased by 1 automatically.

## READ DISPLAY DATA

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 1  | 1   | D7  | D6  | D5  | D4  | D3  | D2  | D1  | D0  |

Reads data (D0-D7) from the display data RAM. After reading instruction, Y address is increased by 1 automatically.

# 12. Reliability

## Content of Reliability Test (wide temperature, -20°C~70°C)

| Environmental Test                      |   |  |      |
|---|---|--|------|
| Test Item                               | Content of Test   | Test Condition   | Note |
| High Temperature storage                | Endurance test applying the high storage temperature for a long time.   | 80°C<br>200hrs   | 2    |
| Low Temperature storage                 | Endurance test applying the high storage temperature for a long time.   | -30°C<br>200hrs  | 1,2  |
| High Temperature Operation              | Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.  | 70°C<br>200hrs   | —    |
| Low Temperature Operation               | Endurance test applying the electric stress under low temperature for a long time.  | -20°C<br>200hrs  | 1    |
| High Temperature/<br>Humidity Operation | The module should be allowed to stand at 60 °C, 90% RH max<br>For 96hrs under no-load condition excluding the polarizer,<br>Then taking it out and drying it at normal temperature.               | 60°C, 90% RH<br>96hrs  | 1,2  |
| Thermal shock resistance                | The sample should be allowed stand the following 10 cycles of operation<br><div style="text-align: center;"> <p>-20°C      25°C      70°C</p> <p>30min    5min    30min</p> <p>1 cycle</p> </div> | -20°C/70°C<br>10 cycles  | —    |
| Vibration test                          | Endurance test applying the vibration during transportation and using.  | Total fixed amplitude : 1.5mm<br>Vibration<br>Frequency : 10~55Hz<br>One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes | 3    |
| Static electricity test                 | Endurance test applying the electric stress to the terminal.  | VS=800V, RS=1.5kΩ<br>CS=100pF<br>1 time  | —    |

**Note1: No dew condensation to be observed.**

**Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.**

**Note3: Vibration test will be conducted to the product itself without putting it in a container.**

# 13.Backlight Information

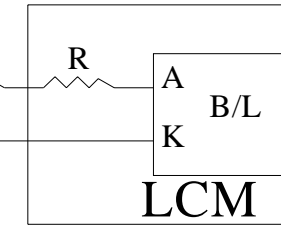
## Specification

| PARAMETER                         | SYMBOL           | MIN  | TYP | MAX | UNIT              | TEST CONDITION                                       |
|-----------------------------------|------------------|------|-----|-----|-------------------|--|
| Supply Current                    | I <sub>LED</sub> | 57.6 | 64  | 100 | mA                | V=3.5V   |
| Supply Voltage                    | V                | 3.4  | 3.5 | 3.6 | V                 | —  |
| Reverse Voltage                   | V <sub>R</sub>   | —    | —   | 5   | V                 | —  |
| Luminous Intensity                | I <sub>V</sub>   | 200  | 250 | —   | cd/m <sup>2</sup> | I <sub>LED</sub> =64mA                               |
| Life Time<br>(For Reference only) | —                | —    | 30K | —   | Hr.               | I <sub>LED</sub> ≦ 64mA<br>25℃,50-60%RH,<br>(Note 1) |
| Color                             | White            |      |     |     |                   |  |

**Note:** The LED of B/L is drive by current only, drive voltage is for reference only.  
drive voltage can make driving current under safety area (current between minimum and maximum).

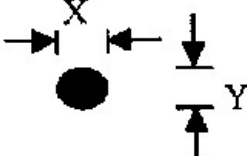
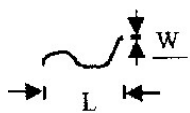
**Note 1:**The brightness will decrease to 50% of the original value after 30K hours

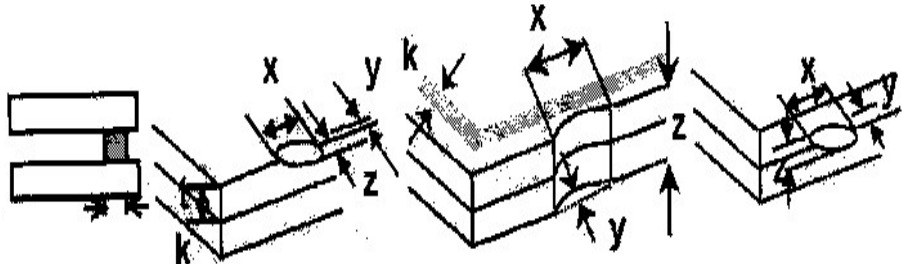
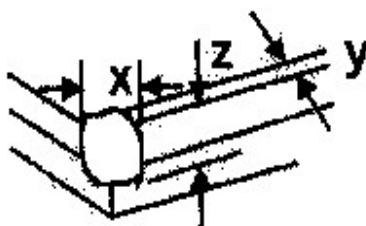
.Drive from pin19,pin20

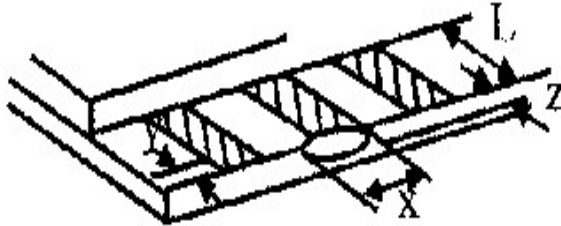
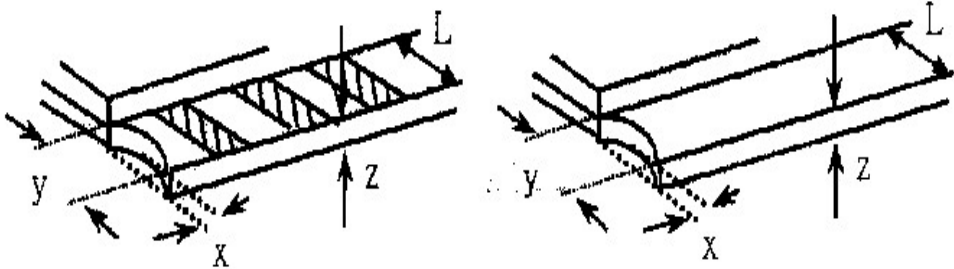
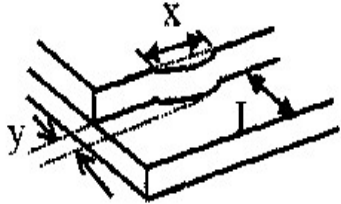


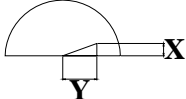


# 14. Inspection specification

| NO                      | Item  | Criterion  | AQL   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
|-------------------------|---|--|---|-----------------|------------------|------------------|-------------------------|-------------------------|-------------------------|-------------------------|----------------------|---------------|--------------|----------------------|-----|------------|---------------|-----|
| 01                      | Electrical Testing  | 1.1 Missing vertical, horizontal segment, segment contrast defect.<br>1.2 Missing character , dot or icon.<br>1.3 Display malfunction.<br>1.4 No function or no display.<br>1.5 Current consumption exceeds product specifications.<br>1.6 LCD viewing angle defect.<br>1.7 Mixed product types.<br>1.8 Contrast defect.   | 0.65  |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
| 02                      | Black or white spots on LCD (display only)                | 2.1 White and black spots on display $\leq 0.25\text{mm}$ , no more than three white or black spots present.<br>2.2 Densely spaced: No more than two spots or lines within 3mm   | 2.5   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
| 03                      | LCD black spots, white spots, contamination (non-display) | 3.1 Round type : As following drawing<br>$\Phi=(x+y)/2$<br>   | <table><tr><th>SIZE</th><th>Acceptable Q TY</th></tr><tr><td><math>\Phi \leq 0.10</math></td><td>Accept no dense</td></tr><tr><td><math>0.10 &lt; \Phi \leq 0.20</math></td><td>2</td></tr><tr><td><math>0.20 &lt; \Phi \leq 0.25</math></td><td>1</td></tr><tr><td><math>0.25 &lt; \Phi</math></td><td>0</td></tr></table>   | SIZE            | Acceptable Q TY  | $\Phi \leq 0.10$ | Accept no dense         | $0.10 < \Phi \leq 0.20$ | 2                       | $0.20 < \Phi \leq 0.25$ | 1                    | $0.25 < \Phi$ | 0            | 2.5                  |     |            |               |     |
|                         |   | SIZE   | Acceptable Q TY   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
| $\Phi \leq 0.10$        | Accept no dense   |  |   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
| $0.10 < \Phi \leq 0.20$ | 2   |  |   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
| $0.20 < \Phi \leq 0.25$ | 1   |  |   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
| $0.25 < \Phi$           | 0   |  |   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
|                         |   | 3.2 Line type : (As following drawing)<br>  | <table><tr><th>Length</th><th>Width</th><th>Acceptable Q TY</th></tr><tr><td>---</td><td><math>W \leq 0.02</math></td><td>Accept no dense</td></tr><tr><td><math>L \leq 3.0</math></td><td><math>0.02 &lt; W \leq 0.03</math></td><td rowspan="2">2</td></tr><tr><td><math>L \leq 2.5</math></td><td><math>0.03 &lt; W \leq 0.05</math></td></tr><tr><td>---</td><td><math>0.05 &lt; W</math></td><td>As round type</td></tr></table> | Length          | Width            | Acceptable Q TY  | ---                     | $W \leq 0.02$           | Accept no dense         | $L \leq 3.0$            | $0.02 < W \leq 0.03$ | 2             | $L \leq 2.5$ | $0.03 < W \leq 0.05$ | --- | $0.05 < W$ | As round type | 2.5 |
| Length                  | Width   | Acceptable Q TY  |   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
| ---                     | $W \leq 0.02$   | Accept no dense  |   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
| $L \leq 3.0$            | $0.02 < W \leq 0.03$                                      | 2  |   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
| $L \leq 2.5$            | $0.03 < W \leq 0.05$                                      |  |   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
| ---                     | $0.05 < W$  | As round type  |   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
| 04                      | Polarizer bubbles   | If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction.<br><table><tr><th>Size <math>\Phi</math></th><th>Acceptable Q TY</th></tr><tr><td><math>\Phi \leq 0.20</math></td><td>Accept no dense</td></tr><tr><td><math>0.20 &lt; \Phi \leq 0.50</math></td><td>3</td></tr><tr><td><math>0.50 &lt; \Phi \leq 1.00</math></td><td>2</td></tr><tr><td><math>1.00 &lt; \Phi</math></td><td>0</td></tr><tr><td>Total Q TY</td><td>3</td></tr></table> | Size $\Phi$   | Acceptable Q TY | $\Phi \leq 0.20$ | Accept no dense  | $0.20 < \Phi \leq 0.50$ | 3                       | $0.50 < \Phi \leq 1.00$ | 2                       | $1.00 < \Phi$        | 0             | Total Q TY   | 3                    | 2.5 |            |               |     |
| Size $\Phi$             | Acceptable Q TY   |  |   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
| $\Phi \leq 0.20$        | Accept no dense   |  |   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
| $0.20 < \Phi \leq 0.50$ | 3   |  |   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
| $0.50 < \Phi \leq 1.00$ | 2   |  |   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
| $1.00 < \Phi$           | 0   |  |   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |
| Total Q TY              | 3   |  |   |                 |                  |                  |                         |                         |                         |                         |                      |               |              |                      |     |            |               |     |

| NO                 | Item                  | Criterion   | AQL               |               |                |               |                       |               |                    |                 |               |                   |               |                |               |                       |               |                    |                 |               |     |
|--------------------|-----------------------|---|-------------------|---------------|----------------|---------------|-----------------------|---------------|--------------------|-----------------|---------------|-------------------|---------------|----------------|---------------|-----------------------|---------------|--------------------|-----------------|---------------|-----|
| 05                 | Scratches             | Follow NO.3 LCD black spots, white spots, contamination   |                   |               |                |               |                       |               |                    |                 |               |                   |               |                |               |                       |               |                    |                 |               |     |
| 06                 | Chipped glass         | <div> <div> <div>Symbols Define:</div> <div> <div>x: Chip length</div> <div>y: Chip width</div> <div>z: Chip thickness</div> <div>k: Seal width</div> <div>t: Glass thickness</div> <div>a: LCD side length</div> <div>L: Electrode pad length:</div> </div> </div> <div> <div>6.1 General glass chip :</div> <div>6.1.1 Chip on panel surface and crack between panels:</div> <div>  <table> <tr> <td>z: Chip thickness</td> <td>y: Chip width</td> <td>x: Chip length</td> </tr> <tr> <td><math>Z \leq 1/2t</math></td> <td>Not over viewing area</td> <td><math>x \leq 1/8a</math></td> </tr> <tr> <td><math>1/2t &lt; z \leq 2t</math></td> <td>Not exceed 1/3k</td> <td><math>x \leq 1/8a</math></td> </tr> </table> <div>⊙If there are 2 or more chips, x is total length of each chip.</div> <div>6.1.2 Corner crack:</div> <div>  <table> <tr> <td>z: Chip thickness</td> <td>y: Chip width</td> <td>x: Chip length</td> </tr> <tr> <td><math>Z \leq 1/2t</math></td> <td>Not over viewing area</td> <td><math>x \leq 1/8a</math></td> </tr> <tr> <td><math>1/2t &lt; z \leq 2t</math></td> <td>Not exceed 1/3k</td> <td><math>x \leq 1/8a</math></td> </tr> </table> <div>⊙If there are 2 or more chips, x is the total length of each chip.</div> </div> </div> </div> </div> | z: Chip thickness | y: Chip width | x: Chip length | $Z \leq 1/2t$ | Not over viewing area | $x \leq 1/8a$ | $1/2t < z \leq 2t$ | Not exceed 1/3k | $x \leq 1/8a$ | z: Chip thickness | y: Chip width | x: Chip length | $Z \leq 1/2t$ | Not over viewing area | $x \leq 1/8a$ | $1/2t < z \leq 2t$ | Not exceed 1/3k | $x \leq 1/8a$ | 2.5 |
| z: Chip thickness  | y: Chip width         | x: Chip length  |                   |               |                |               |                       |               |                    |                 |               |                   |               |                |               |                       |               |                    |                 |               |     |
| $Z \leq 1/2t$      | Not over viewing area | $x \leq 1/8a$   |                   |               |                |               |                       |               |                    |                 |               |                   |               |                |               |                       |               |                    |                 |               |     |
| $1/2t < z \leq 2t$ | Not exceed 1/3k       | $x \leq 1/8a$   |                   |               |                |               |                       |               |                    |                 |               |                   |               |                |               |                       |               |                    |                 |               |     |
| z: Chip thickness  | y: Chip width         | x: Chip length  |                   |               |                |               |                       |               |                    |                 |               |                   |               |                |               |                       |               |                    |                 |               |     |
| $Z \leq 1/2t$      | Not over viewing area | $x \leq 1/8a$   |                   |               |                |               |                       |               |                    |                 |               |                   |               |                |               |                       |               |                    |                 |               |     |
| $1/2t < z \leq 2t$ | Not exceed 1/3k       | $x \leq 1/8a$   |                   |               |                |               |                       |               |                    |                 |               |                   |               |                |               |                       |               |                    |                 |               |     |

| NO                    | Item           | Criterion   | AQL           |                |                   |                       |               |                |               |                |                   |            |               |                |          |           |               |            |     |
|-----------------------|----------------|---|---------------|----------------|-------------------|-----------------------|---------------|----------------|---------------|----------------|-------------------|------------|---------------|----------------|----------|-----------|---------------|------------|-----|
| 06                    | Glass crack    | <div> <div> <p>Symbols :</p> <p>x: Chip length                      y: Chip width                      z: Chip thickness</p> <p>k: Seal width                      t: Glass thickness                      a: LCD side length</p> <p>L: Electrode pad length</p> <p>6.2 Protrusion over terminal :</p> <p>6.2.1 Chip on electrode pad :</p>  <table> <tr> <td>y: Chip width</td> <td>x: Chip length</td> <td>z: Chip thickness</td> </tr> <tr> <td><math>y \leq 0.5\text{mm}</math></td> <td><math>x \leq 1/8a</math></td> <td><math>0 &lt; z \leq t</math></td> </tr> </table> <p>6.2.2 Non-conductive portion:</p>  <table> <tr> <td>y: Chip width</td> <td>x: Chip length</td> <td>z: Chip thickness</td> </tr> <tr> <td><math>y \leq L</math></td> <td><math>x \leq 1/8a</math></td> <td><math>0 &lt; z \leq t</math></td> </tr> </table> <p>           ☉ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.<br/>           ☉ If the product will be heat sealed by the customer, the alignment mark not be damaged.         </p> <p>6.2.3 Substrate protuberance and internal crack.</p>  <table> <tr> <td>y: width</td> <td>x: length</td> </tr> <tr> <td><math>y \leq 1/3L</math></td> <td><math>x \leq a</math></td> </tr> </table> </div> </div> | y: Chip width | x: Chip length | z: Chip thickness | $y \leq 0.5\text{mm}$ | $x \leq 1/8a$ | $0 < z \leq t$ | y: Chip width | x: Chip length | z: Chip thickness | $y \leq L$ | $x \leq 1/8a$ | $0 < z \leq t$ | y: width | x: length | $y \leq 1/3L$ | $x \leq a$ | 2.5 |
| y: Chip width         | x: Chip length | z: Chip thickness   |               |                |                   |                       |               |                |               |                |                   |            |               |                |          |           |               |            |     |
| $y \leq 0.5\text{mm}$ | $x \leq 1/8a$  | $0 < z \leq t$  |               |                |                   |                       |               |                |               |                |                   |            |               |                |          |           |               |            |     |
| y: Chip width         | x: Chip length | z: Chip thickness   |               |                |                   |                       |               |                |               |                |                   |            |               |                |          |           |               |            |     |
| $y \leq L$            | $x \leq 1/8a$  | $0 < z \leq t$  |               |                |                   |                       |               |                |               |                |                   |            |               |                |          |           |               |            |     |
| y: width              | x: length      |   |               |                |                   |                       |               |                |               |                |                   |            |               |                |          |           |               |            |     |
| $y \leq 1/3L$         | $x \leq a$     |   |               |                |                   |                       |               |                |               |                |                   |            |               |                |          |           |               |            |     |

| NO | Item               | Criterion   | AQL  |
|----|--------------------|---|--|
| 07 | Cracked glass      | The LCD with extensive crack is not acceptable.   | 2.5  |
| 08 | Backlight elements | 8.1 Illumination source flickers when lit.<br>8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards.<br>8.3 Backlight doesn't light or color wrong.   | 0.65<br>2.5<br>0.65  |
| 09 | Bezel              | 9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.<br>9.2 Bezel must comply with job specifications.   | 2.5<br>0.65  |
| 10 | PCB 、 COB          | 10.1 COB seal may not have pinholes larger than 0.2mm or contamination.<br>10.2 COB seal surface may not have pinholes through to the IC.<br>10.3 The height of the COB should not exceed the height indicated in the assembly diagram.<br>10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places.<br>10.5 No oxidation or contamination PCB terminals.<br>10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts.<br>10.7 The jumper on the PCB should conform to the product characteristic chart.<br>10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down.<br>10.9 The Scraping testing standard for Copper Coating of PCB<br> $X * Y \leq 2\text{mm}^2$ | 2.5<br>2.5<br>0.65<br>2.5<br>2.5<br>0.65<br>0.65<br>2.5<br>2.5 |
| 11 | Soldering          | 11.1 No un-melted solder paste may be present on the PCB.<br>11.2 No cold solder joints, missing solder connections, oxidation or icicle.<br>11.3 No residue or solder balls on PCB.<br>11.4 No short circuits in components on PCB.  | 2.5<br>2.5<br>2.5<br>0.65                                      |

| NO | Item               | Criterion  | AQL  |
|----|--------------------|--|--|
| 12 | General appearance | 12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.<br>12.2 No cracks on interface pin (OLB) of TCP.<br>12.3 No contamination, solder residue or solder balls on product.<br>12.4 The IC on the TCP may not be damaged, circuits.<br>12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever.<br>12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.<br>12.7 Sealant on top of the ITO circuit has not hardened.<br>12.8 Pin type must match type in specification sheet.<br>12.9 LCD pin loose or missing pins.<br>12.10 Product packaging must the same as specified on packaging specification sheet.<br>12.11 Product dimension and structure must conform to product specification sheet. | 2.5<br>0.65<br>2.5<br>2.5<br>2.5<br>2.5<br>2.5<br>0.65<br>0.65<br>0.65<br>0.65 |

# 15. Material List of Components for RoHs

1. WINSTAR Display Co., Ltd hereby declares that all of or part of products (with the mark “#”in code), including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

Exhibit A : The Harmful Material List

| Material   | (Cd)    | (Pb)     | (Hg)     | (Cr6+)   | PBBs     | PBDEs    |
|--|---------|----------|----------|----------|----------|----------|
| Limited Value                                    | 100 ppm | 1000 ppm | 1000 ppm | 1000 ppm | 1000 ppm | 1000 ppm |
| Above limited value is set up according to RoHS. |         |          |          |          |          |          |

2.Process for RoHS requirement :

(1) Use the Sn/Ag/Cu soldering surface ; the surface of Pb-free solder is rougher than we used before.

(2) Heat-resistance temp. :

Reflow : 250°C ,30 seconds Max. ;

Connector soldering wave or hand soldering : 320°C , 10 seconds max.

(3) Temp. curve of reflow, max. Temp. : 235±5°C ;

Recommended customer's soldering temp. of connector : 280°C , 3 seconds.



Module Number : \_\_\_\_\_

Page: 1

**1 、 Panel Specification :**

- |                            |                               |                                     |
|----------------------------|-------------------------------|-------------------------------------|
| 1. Panel Type :            | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 2. View Direction :        | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 3. Numbers of Dots :       | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 4. View Area :             | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 5. Active Area :           | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 6. Operating Temperature : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 7. Storage Temperature :   | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 8. Others :                | _____                         |                                     |

**2 、 Mechanical Specification :**

- |                             |                               |                                     |
|-----------------------------|-------------------------------|-------------------------------------|
| 1. PCB Size :               | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 2. Frame Size :             | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 3. Material of Frame :      | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 4. Connector Position :     | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 5. Fix Hole Position :      | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 6. Backlight Position :     | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 7. Thickness of PCB :       | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 8. Height of Frame to PCB : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 9. Height of Module :       | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 10. Others :                | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |

**3 、 Relative Hole Size :**

- |                             |                               |                                     |
|-----------------------------|-------------------------------|-------------------------------------|
| 1. Pitch of Connector :     | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 2. Hole size of Connector : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 3. Mounting Hole size :     | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 4. Mounting Hole Type :     | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 5. Others :                 | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |

**4 、 Backlight Specification :**

- |   |                               |                                     |
|---|-------------------------------|-------------------------------------|
| 1. B/L Type :                                     | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 2. B/L Color :                                    | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 3. B/L Driving Voltage (Reference for LED Type) : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 4. B/L Driving Current :                          | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 5. Brightness of B/L :                            | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 6. B/L Solder Method :                            | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 7. Others :                                       | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |

>> **Go to page 2** <<



winstar

Module Number : \_\_\_\_\_

Page: 2

**5、Electronic Characteristics of Module :**

- |                              |                               |                                     |
|------------------------------|-------------------------------|-------------------------------------|
| 1. Input Voltage :           | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 2. Supply Current :          | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 3. Driving Voltage for LCD : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 4. Contrast for LCD :        | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 5. B/L Driving Method :      | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 6. Negative Voltage Output : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 7. Interface Function :      | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 8. LCD Uniformity :          | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 9. ESD test :                | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 10. Others :                 | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |

**6、Summary :**

Sales signature : \_\_\_\_\_

Customer Signature : \_\_\_\_\_

Date :    /    /