

Other Information

To obtain the most recent and complete documentation for this demonstration board, including:

- User's Guide
- Board Description
- Board Schematics
- Source Code
- Application Examples
- Links to Web Seminars

Please refer to the Microchip web site: www.microchip.com

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PIC24FJ256GA110 PIM Manual

Overview

The PIC24FJ256GA110 PIM is designed to demonstrate the capabilities of the PIC24FJ256GA110 family using the Explorer 16 Demonstration Board kit and the PICtail™ Plus Daughter Boards. The Peripheral Pin Select (PPS) feature, available on this device, allows the existing peripherals to be remapped to the new I/O pins.

Features

Enables designs for the PIC24FJ256GA110 family to be debugged on the Explorer 16 board.

Signal Interface

There is no pin remapping for this PIM. Each pin on the PIC24FJ256GA110 corresponds to the same pin on the Explorer 16 socket.

The resistor R5 is used to improve the duty cycle and start-up time of the Explorer 16 Demonstration Board's 32 kHz crystal. In a production PC board, this resistor may not be necessary.



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Board Schematic Rev. B

