



180920394 Si5342-44-45-46-47 Rev D Datasheet and Crystal/TCXO/OCXO Ref. Manual Update

PCN Issue Date: 9/20/2018

Effective Date: 12/26/2018

PCN Type: Datasheet

Description of Change

Silicon Labs is pleased to announce the release of revisions to the following documents:

- Si5342/44/45 Rev D data sheet from document revision 1.1 to documents revision 1.2
- Si5346-47 Rev D data sheet from document revision 1.0 to documents revision 1.1
- Recommended Crystal, TCXO, and OCXO Reference Manual for High-Performance Jitter Attenuators and Clock Generators document revision 1.0 to document revision 1.1

A detailed description of the changes to the data sheets and the Recommended Crystal, TCXO, and OCXO Reference Manual are summarized in the change impact section of this document.

Customers are encouraged to download the most recent version of CBPro, 2.27 or later, to take advantage of the latest software features and algorithms. A detailed description of changes for each CBPro release is available at <https://www.silabs.com/documents/public/release-notes/ClockBuilder-Pro-README.pdf>.

Reason for Change

Data sheets were updated to reflect device performance and to add additional application support information. Recommended Crystal, TCXO, and OCXO Reference Manual for High-Performance Jitter Attenuators and Clock Generators was updated to support additional Silicon Labs products, update recommended parts, and add additional information on how to select the right reference device. A list of specific changes are below.

Impact on Form, Fit, Function, Quality, Reliability

There is no impact on form, fit, quality and reliability.

Function: changes

Si5342/44/45 Rev D Data Sheet revision 1.2 changes

- Updated Figure 3.3 Crystal Resonator and External Reference Clock Connection Options.
- Updated Figure 3.4 Termination of Differential and LVCMOS Input Signals.
- Updated Figure 3.13 Supported Differential Output Terminations.
- Removed Output Skew Control section.
- Updated Table 5.2 DC Characteristics.
- Updated Note 5 and LVCMOS Output Test Configuration circuit.
- Updated Table 5.3 Input Clock Specifications.
- Updated Table 5.4 Control Input Pin Specifications.
- Updated Input Capacitance specification.
- Updated Table 5.5 Differential Clock Output Specifications.
- Updated Table 5.6 LVCMOS Clock Output Specifications.
- Updated LVCMOS Output Test Configuration.
- Updated Table 5.8 Performance Characteristics.
- Changed "Input-to-Output Delay" specification to "Zero-Delay Mode Input-to-Output Delay".
- Updated Table 5.12 Crystal Specifications.
- Updated Table 5.14 Absolute Maximum Ratings.

Si5346/47 Rev D Data Sheet revision 1.1 changes, other

- Updated Figure 3.3 Crystal Resonator and External Reference Clock Connection Options.
- Updated Figure 3.5 Termination of Differential and LVCMOS Input Signals.
- Updated Figure 3.15 Supported Differential Output Terminations.
- Updated Table 5.3 Input Clock Specifications.
- Updated Output-to-Output Skew specification in Table 5.5 Differential Clock Output Specifications.
- Removed Output-to-Output Skew specification in Table 5.6 LVCMOS Clock Output Specifications.
- Removed Input-to-Output Delay Variation specification in Table 5.8 Performance Characteristics.

Recommended Crystal, TCXO, and OCXO Reference Manual for High-Performance Jitter Attenuators and Clock Generators revision 1.1 changes.

- Added Si537x/9x devices coverage.
- Added appendices explaining how to choose the right crystal and crystal oscillator for end application.
- Removed discontinued parts from recommended part tables.
- Added new parts to recommended part tables.
- Added information in recommended part tables indicating part family to make these parts easier to find on vendor website.

Product Identification

Existing Part #
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SI5342A-D-GMR
SI5342A-D05838-GM
SI5342A-D05838-GMR
SI5342A-D06445-GM
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Last Date of Unchanged Product: 12/26/2018

Qualification Samples

N/A

Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at <http://www.silabs.com>.

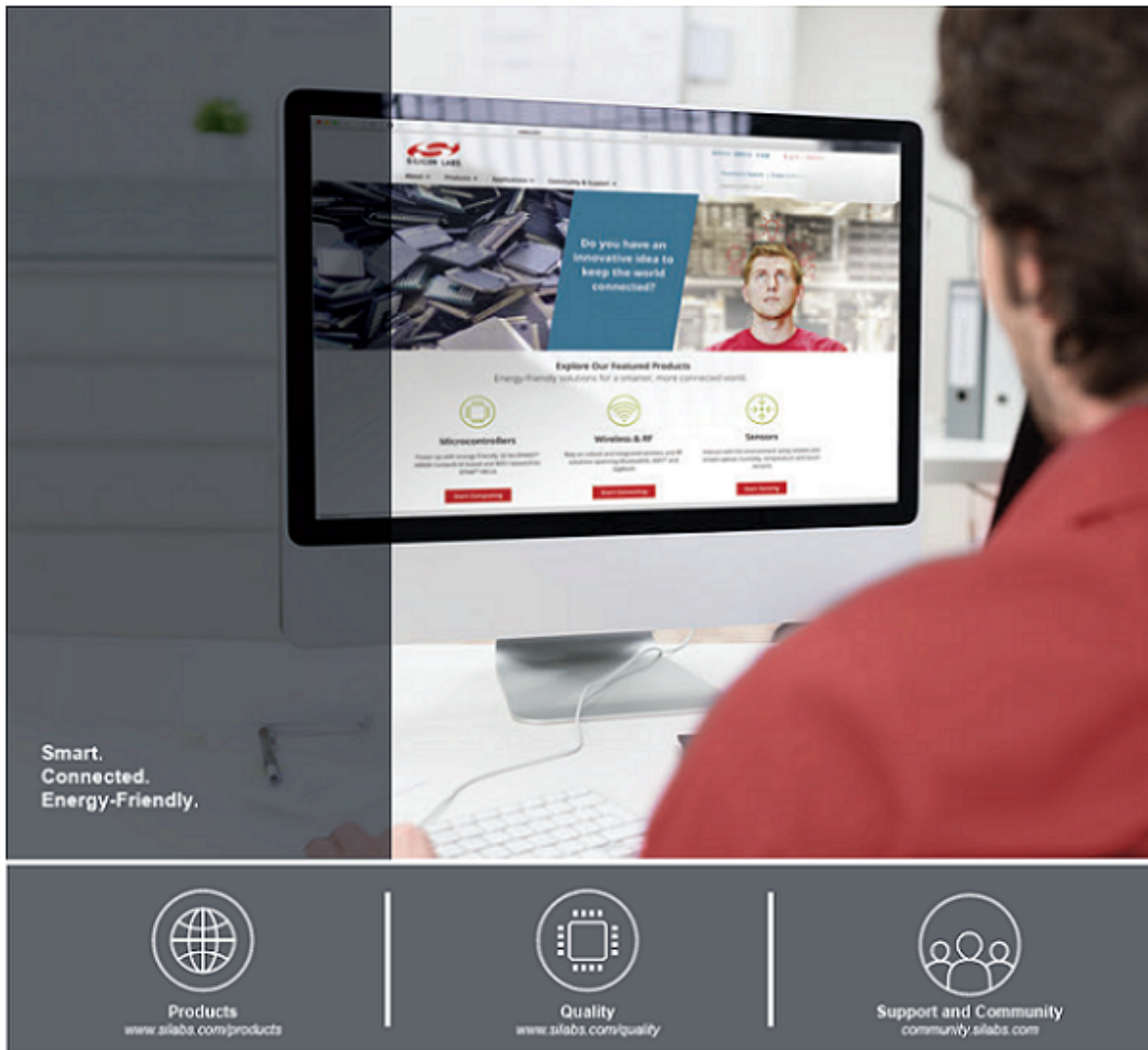
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Qualification Data

N/A



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