XinaBox Datasheet IX03 - UART to xBUS breakout

Contents

Overview

This xCHIP is based on the SC16IS750IBS chip-set, which is SC16IS750 is a slave I^2C-bus/SPI interface to a single-channel high performance UART. IX03 is a UART to ⊠BUS breakout designed for bread-boarding with UART (https://en.wikipedia.org/wiki/Universal_asynchronous_receiver-transmitter) and I^2C devices. The SC16IS750 supports SPI clock speeds up to 4 Mbit/s and has programmable I/O pins. The SC16IS740/750/760's internal register set is backward-compatible with the widely used and widely popular 16C450. This allows the software to be easily written or ported from another platform.

Product Highlights

- Based on SC16IS750IBS from NXP Semiconductors

Applications

- Factory automation and process control
- Portable and battery operated devices
- Cellular data devices

Specifications

- Baud rates up to 5 Mbit/s in 16⊠ clock mode
- Auto hardware flow control using RTS/CTS
- Auto software flow control with programmable Xon/Xoff characters
- Single or double Xon/Xoff characters
- Automatic RS-485 support (automatic slave address detection)
- Single full-duplex UART
- Selectable I2C-bus or SPI interface
- Industrial temperature range: ⊠40 ⊠C to +95 ⊠C
- 64 bytes FIFO (transmitter and receiver)

Pin List

- GND
- VCC
- GPIO0
- GPIO1
- DSR
- DTR
- CD
- RI
- A0
- A1
- TX
- RX
- CTS
- RTC

External Links

Datasheet

- SC16IS740 From NXP Semiconductors (https://www.nxp.com/docs/en/data-sheet/SC16IS740_750_760.pdf)

Shop

- Buy IX03 (https://xinabox.cc/products/IX03)

GitHub Libraries

- Arduino (https://github.com/xinabox/Arduino_IX03)

IX03 - UART to xBUS breakout (SC16IS750IBS/SC16IS750)

Front

Back
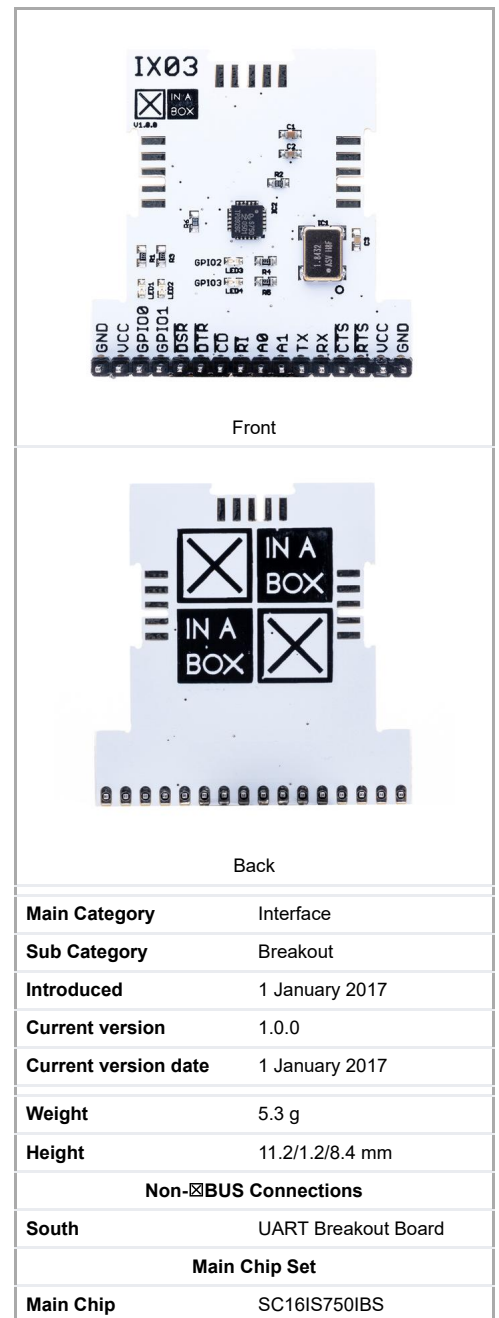
Main Category	Interface
Sub Category	Breakout
Introduced	1 January 2017
Current version	1.0.0
Current version date	1 January 2017
Weight	5.3 g
Height	11.2/1.2/8.4 mm
Non-⊠BUS Connections	
South	UART Breakout Board
Main Chip Set	
Main Chip	SC16IS750IBS