

# 190423523 Si5374/75/76 Adding 2nd Source Capacitor Supplier

PCN Issue Date: 4/23/2019

Effective Date: 7/29/2019

PCN Type: Assembly

#### **Description of Change**

Silicon Labs is pleased to announce the successful qualification of 2nd source capacitor supplier for Si5374/75/76 devices. There is no change in the dimension, specification or performance with the new capacitor. As of the effective date of the PCN, Silicon Labs will continue to fulfill Si5374/75/76 orders built with both capacitors. The package qualification report is attached.

#### **Reason for Change**

Capacity expansion.

#### Impact on Form, Fit, Function, Quality, Reliability

There is no change on form, fit, function, quality & reliability of this product

#### **Product Identification**

Existing Part # SI5374B-A-GL SI5374C-A-GL SI5375B-A-GL SI5376B-A-GL

#### Last Date of Unchanged Product: 7/29/2019

#### **Qualification Samples**

Available upon request

#### **Customer Response**

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at <a href="http://www.silabs.com">http://www.silabs.com</a>.

Customers may approve early PCN acceptance by emailing approval, along with PCN # to PCNEarlyAcceptance@silabs.com

#### **User Registration**

Register today to create your account on Silabs.com. Your personalized profile allows you to receive technical document updates, new product announcements, "how-to" and design documents, product change notices (PCN) and other valuable content available only to registered users. <u>http://www.silabs.com/profile</u>

### **Qualification Data**

Refer to Appendix.



SIS375 Qualification Report intended only for the Internal use of Silcon Labs. Any other use or reproduction of any part of this document is prohibited without Silcon Labs disclaims all warrantes concerning the accuracy of the Intornation contained in this document. This document is version controlled, printed or electronically saved versions of this documents may be obsolved. Any misuse of this document should be reported to DL. QualitySystems@silabs.com

Part Rev A, TSI	DL QualitySystems@sia MC Fabrication, SPIL Assem		ted				
Test Name	Test Condition	Qualification	Lot ID or Start	Fall/Pass or End	Notes	Summary	Statue
	ccelerated Environment Street	Tests					
HAST	JA110		Q035729	0/25	1,2		
	110°C, 85%RH	3 lots, N=>25	Q035617	0/25	1,2	3 lots	Pass
	Vcc=2.5V, 264 hours		Q035730	0/25	1,2	0/75	
UHAST	JA118		Q038165	0/25	1,2		
	130°C, 85%RH	3 lots, N+>25	Q038166	0/25	1,2	3 lots	Pass
	96 hours		Q038167	0/25	1,2	0/75	
Temp Cycle	JA104		Q043828	0/25	1		
	Cond C: -65*C to 150*C	3 lots, N=>25	Q043829	0/25	1	3 lots	Pass
	500 cycles		Q043830	0/25	1	0/75	
HTSL	JA103		Q038168	0/25	1,2		
	150°C, 1000hr	3 lots, N+>25	Q038169	0/25	1,2	3 lots	Pass
			Q038170	0/25	1, 2	0/75	
Test Group B - A	ccelerated Lifetime Simulation	Tests					
HTOL	JA108		Q030474	0/80	3		
	T, ≥ 125°C, Dynamic	3 lots, N=>77	Q025592	0/80	3	3 lots	Pass
	Voc-2.5V, 1000 hours		Q025904	0/80	3	0/240	
ELFR	JA108		Q030433	0/502	3		
	T, ≥ 125°C, Dynamic	3 lots, N=>500	Q025539	0/511	3	3 lots	Pass
	Voc=2.5V, 48 hours		Q026187	0/504	3	0/1517	
Test Group E – E	Jectrical Verification						
ESD-HBM	JS-001						
		1 lot, N=>3	Q029471		3	2.5 KV	Class 2
							(700V Cik In)
ESD-COM	JES022-C101						
		1 lot, N=>3	Q029472		3	250 V	Class II
							(150V Cik In)
Lalch Up	JESO78						
	a100mA	1 lot, N=>3	Q030317	25 °C	3		Pass
	Overvoltage = 4.2V						

1 silabs.com | 5/537x\_Qual\_Report

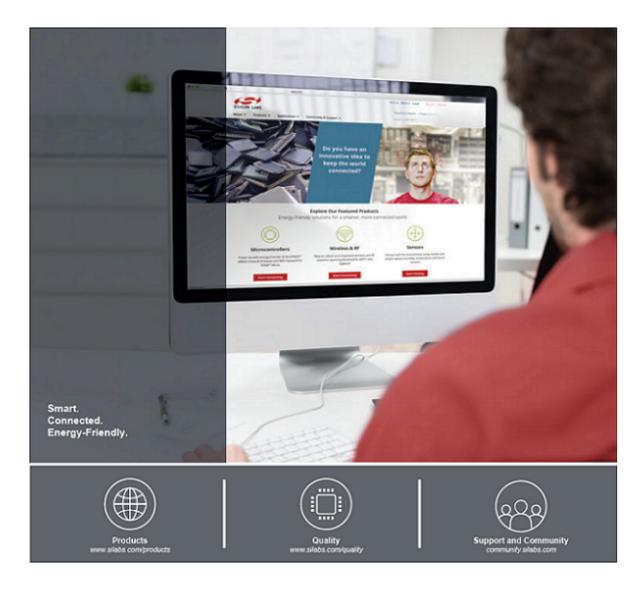
Prepared on: 06-Apr-2019 by N. Arguelo



SISTS Qualification Report SILICON LARS Intended only for the internal use of Silicon Labs. Any other use or reproduction of any part of this document is prohibited without Silicon Labs' written consent. Any use of this document is solery at the risk of the user. Silicon Labs disclaims all warrantes concoming the accuracy of the information contained in this document. This document is version controlled, printed or electronically saved versions of this documents may be obsolete. Any misuse of this document should be reported to DL Quality Systems@silabs.com

Test Name	Test Condition	Gualification St		Notes	Summary	Statue
iotes:			_			
	nditioned at MSL3/260°C					
Leveraged pack	age family qualification data					
Leveraged pack						
Leveraged pack	age family qualification data amily qualification data	and early is the follow				
Leveraged pack	age family qualification data amily qualification data	eport applies to the followi	ng part numbers:			
Leveraged pack	age family qualification data amily qualification data	eport applies to the follow: SI53758-A-BL	ng part numbers: SI5375	8-A-GL	51537	68-A-8L

#### **Qual Report**



#### Disclaimer

Silicon Labs intends to provide customers with the latest, accurate, and in-depth documentation of all peripherals and modules available for system and software implementers using or intending to use the Silicon Labs products. Characterization data, available modules and peripherals, memory sizes and memory addresses refer to each specific device, and "Typical" parameters provided can and do vary in different applications. Application examples described herein are for illustrative purposes only. Silicon Labs reserves the right to make changes without further notice and limitation to product information, specifications, and descriptions herein, and does not give warranties as to the accuracy or completeness of the included information. Silicon Labs shall have no liability for the consequences of use of the information supplied herein. This document does not imply or express copyright licenses granted hereunder to design or fabricate any integrated circuits. The products are not designed or authorized to be used within any Life Support System without the specific written consent of Silicon Labs. A "Life Support System" is any product or system intended to support or sustain life and/or health, which, if fails, can be reasonably expected to result in significant personal injury or death. Silicon Labs products are not designed or authorized to key set on the advector and designed or authorized to result in significant personal injury or death. Silicon Labs products are not designed or authorized to result in significant personal injury or death. Silicon Labs products are not designed or authorized to weapons, or missiles capable of delivering such weapons.

#### Trademark Information

Silicon Laboratories Inc.®, Silicon Laboratories®, Silicon Labs®, SiLabs® and the Silicon Labs logo®, Bluegiga®, Bluegiga Logo®, Clockbuilder®, CMEMS®, DSPLL®, EFM®, EFM32®, EFR, Ember®, Energy Micro, Energy Micro logo and combinations thereof, "the world's most energy friendly microcontrollers", Ember®, EZLink®, EZRadio®, EZRadioPRO®, Gecko®, ISOmodem®, Micrium, Precision32®, ProSLIC®, Simplicity Studio®, SiPHY®, Telegesis, the Telegesis Logo®, USBXpress®, Zentri and others are trademarks or registered trademarks of Silicon Labs. ARM, CORTEX, Cortex-M3 and THUMB are trademarks or registered trademarks of ARM Limited. All other products or brand names mentioned herein are trademarks of their respective holders.



Silicon Laboratories Inc. 400 West Cesar Chavez Austin, TX 78701

## http://www.silabs.com