190410521 Si5330x Revision 1.0 Datasheet Release

PCN Issue Date: 4/10/2019   **Effective Date:** 7/16/2019

PCN Type: Datasheet

Description of Change

Silicon Labs is pleased to announce the availability of revision 1.0 of the Si5330x datasheet. The new Si5330x revision 1.0 datasheet consolidates the 8 part numbers outlined below into a single datasheet.

Reason for Change

The 8 part numbers noted below share similar functionality and performance, consolidation into a single datasheet is more efficient. Typographical errors were noted in the 16-QFN POD and LPD, which have been corrected. Further characterization over PVT showed that certain AC/DC datasheet parameters have to be updated to reflect actual device performance.

Impact on Form, Fit, Function, Quality, Reliability

There is no impact to Fit, Form, Quality, or Reliability.

The following updates were made, which impact function:
1) Changed minimum operating frequency from DC to 1MHz
2) Removed support for gapped clocks
3) HCSL outputs are limited to 3.3V only (removed 2.5V support)
4) Removed 1.8V VDDO support on VREF with single ended input
5) Clarified AC/DC-coupled clock input requirements for glitchless, non-continuous clocks.

In addition to the above changes, specifications for thetaJB have been added to the package thermal tables.

Product Identification

```
Existing Part #
SI53301-B-GM
SI53301-B-GMR
SI53302-A-GM
SI53302-A-GMR
SI53302-B-GM
SI53302-B-GMR
SI53303-B-GM
SI53303-B-GMR
SI53304-B-GM
SI53304-B-GMR
SI53305-B-GM
SI53305-B-GMR
SI53306-B-GM
SI53306-B-GMR
SI53307-B-GM
SI53307-B-GMR
SI53308-B-GM
SI53308-B-GMR
```

Last Date of Unchanged Product: 7/16/2019

Qualification Samples

n/a

Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at http://www.silabs.com.
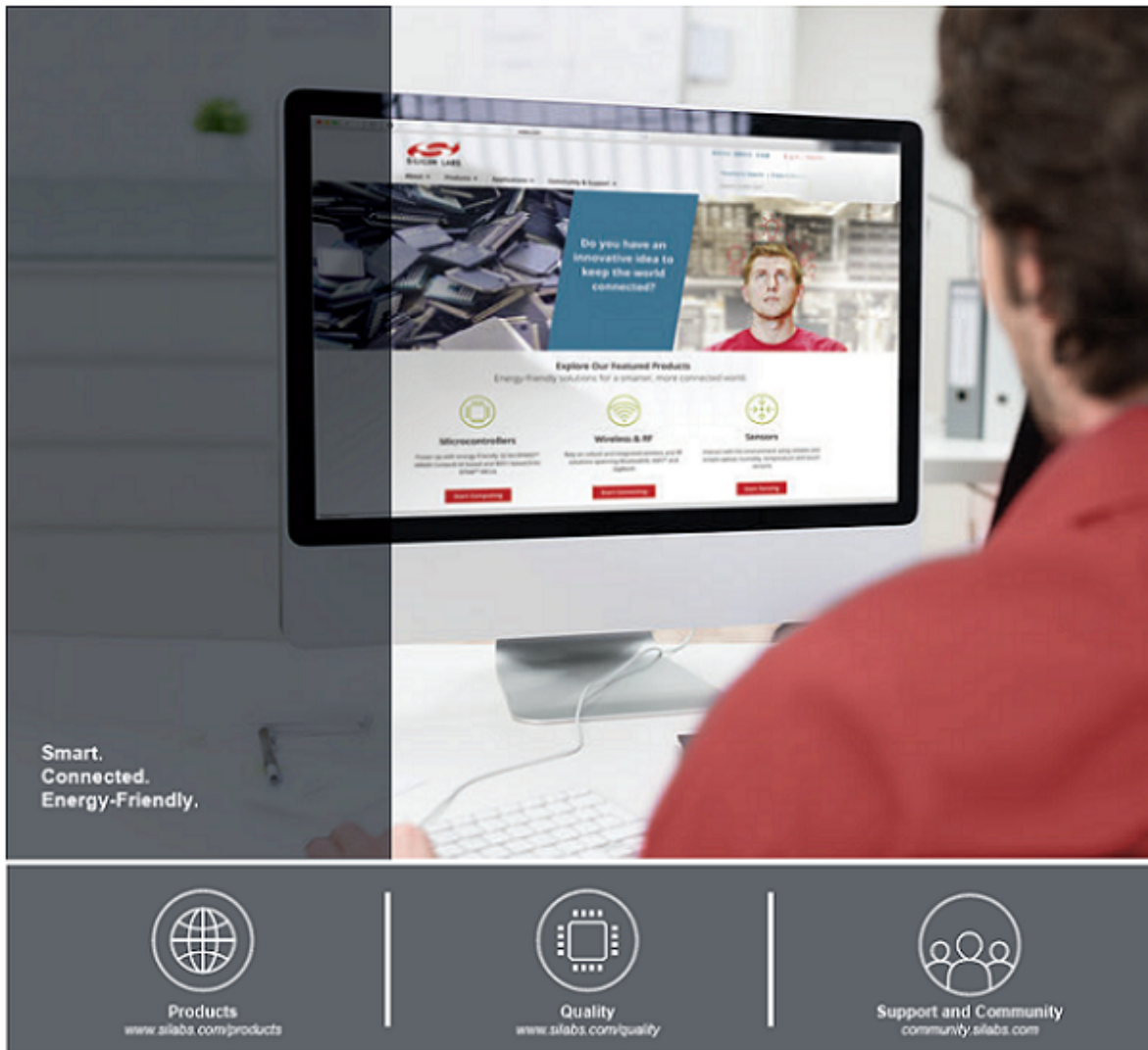
Customers may approve early PCN acceptance by emailing approval, along with PCN # to PCNEarlyAcceptance@silabs.com

User Registration

Register today to create your account on Silabs.com. Your personalized profile allows you to receive technical document updates, new product announcements, "how-to" and design documents, product change notices (PCN) and other valuable content available only to registered users. **http://www.silabs.com/profile**

Qualification Data

n/a

Smart.
Connected.
Energy-Friendly.

Products	Quality	Support and Community
www.silabs.com/products	www.silabs.com/quality	community.silabs.com

Disclaimer

Silicon Labs intends to provide customers with the latest, accurate, and in-depth documentation of all peripherals and modules available for system and software implementers using or intending to use the Silicon Labs products. Characterization data, available modules and peripherals, memory sizes and memory addresses refer to each specific device, and "Typical" parameters provided can and do vary in different applications. Application examples described herein are for illustrative purposes only. Silicon Labs reserves the right to make changes without further notice and limitation to product information, specifications, and descriptions herein, and does not give warranties as to the accuracy or completeness of the included information. Silicon Labs shall have no liability for the consequences of use of the information supplied herein. This document does not imply or express copyright licenses granted hereunder to design or fabricate any integrated circuits. The products are not designed or authorized to be used within any Life Support System without the specific written consent of Silicon Labs. A "Life Support System" is any product or system intended to support or sustain life and/or health, which, if it fails, can be reasonably expected to result in significant personal injury or death. Silicon Labs products are not designed or authorized for military applications. Silicon Labs products shall under no circumstances be used in weapons of mass destruction including (but not limited to) nuclear, biological or chemical weapons, or missiles capable of delivering such weapons.

Trademark Information

Silicon Laboratories Inc.® , Silicon Laboratories®, Silicon Labs®, SiLabs® and the Silicon Labs logo®, Bluegiga®, Bluegiga Logo®, Clockbuilder®, CMEMS®, DSPLL®, EFM®, EFM32®, EFR, Ember®, Energy Micro, Energy Micro logo and combinations thereof, "the world's most energy friendly microcontrollers", Ember®, EZLink®, EZRadio®, EZRadioPRO®, Gecko®, ISOmodem®, Micrium, Precision32®, ProSLIC®, Simplicity Studio®, SiPHY®, Telegesis, the Telegesis Logo®, USBXpress®, Zentri and others are trademarks or registered trademarks of Silicon Labs. ARM, CORTEX, Cortex-M3 and THUMB are trademarks or registered trademarks of ARM Holdings. Keil is a registered trademark of ARM Limited. All other products or brand names mentioned herein are trademarks of their respective holders.

Silicon Laboratories Inc.
400 West Cesar Chavez
Austin, TX 78701

http://www.silabs.com