

Voltage transducer DVC 1000-P

 $V_{PN} = 1000 \text{ V}$

For the electronic measurement of voltage: DC, AC, pulsed..., with galvanic separation between the primary and the secondary circuit.





Features

- Bipolar and insulated measurement up to 1500 V
- Voltage output
- PCB mount
- 8 pins
- 5 V unipolar power supply.

Advantages

- Low consumption and low losses
- Compact design
- Very low sensitivity to common mode voltage variations
- Excellent accuracy (offset, sensitivity, linearity)
- · Low temperature drift
- High immunity to external interferences.

Applications

- Single or three phase inverters
- AC variable speed drives and servo motor drives
- · Static converters for DC motor drives
- Uninterruptible Power Supplies (UPS)
- Power supplies for welding applications.
- Auxiliary converters
- Substations.

Standards

- EN 50155: 2007
- EN 50121-3-2: 2015
- EN 50124-1: 2001
- IEC 61010-1: 2010
- IEC 61800-1: 1997IEC 61800-2: 2015
- IEC 61800-2: 2017
- IEC 61800-5-1: 2007
- IEC 62109-1: 2010.

Application Domain

- Traction (trackside and onboard)
- Industrial.

N° 97.Q9.60.000.0



Absolute maximum ratings

Parameter	Symbol	Unit	Value
Maximum supply voltage ($V_p = 0 \text{ V}, 0.1 \text{ s}$)	$U_{\rm C\; max}$	V	6
Maximum supply voltage (working) (-40 85 °C)	$U_{\rm C\; max}$	V	5.25
Maximum primary voltage (−40 85 °C)	$V_{_{\mathrm{P}\mathrm{max}}}$	V	1500
Maximum steady state primary voltage (-40 85 °C)	V_{PNmax}	V	1000

Absolute maximum ratings apply at 25 °C unless otherwise noted.

Stresses above these ratings may cause permanent damage.

Exposure to absolute maximum ratings for extended periods may degrade reliability.

Insulation coordination

Parameter	Symbol	Unit	Value	Comment
RMS voltage for AC insulation test, 50 Hz, 1 min	U_{d}	kV	4.2	100 % tested in production
Impulse withstand voltage 1.2/50 µs	\hat{U}_{W}	kV	7.84	
Partial discharge RMS test voltage ($q_{\rm m}$ < 10 pC)	U_{t}	V	1500	
Insulation resistance	R_{INS}	ΜΩ	200	
Clearance (pri sec.)	d_{CI}	mm	See dimensions	Shortest distance through air
Creepage distance (pri sec.)	d_{Cp}	mm	drawing on page 9	Shortest path along device body
Case material	-	-	V0	According to UL 94
Comparative tracking index	CTI	V	600	
Maximum DC common mode voltage	$\frac{V_{\mathrm{HV+}} + V_{\mathrm{HV-}}}{\mathrm{and}\; V_{\mathrm{HV+}} - V_{\mathrm{HV-}} }$	kV	≤ 1.5 ≤ V _{PM}	

Environmental and mechanical characteristics

Parameter	Symbol	Unit	Min	Тур	Max
Ambient operating temperature	T_{A}	°C	-40		85
Ambient storage temperature	T_{S}	°C	-50		90
Mass	m	g		22	



Electrical data

At $T_{\rm A}$ = 25 °C, + $U_{\rm C}$ = +5 V, $R_{\rm M}$ = 2 k Ω , unless otherwise noted.

Lines with a * in the conditions column apply over the −40 ... 85 °C ambient temperature range.

Parameter	Symbol	Unit	Min	Тур	Max		Conditions
Primary nominal RMS voltage	V_{PN}	V	101111	1000	Max	*	Sonditions
, ,			4500	1000	4500	*	
Primary voltage, measuring range	V_{PM}	V	-1500		1500		
Measuring resistance	R_{M}	Ω	2000			*	
Secondary nominal RMS voltage	$V_{ m SN}$	V		3.83		*	
Secondary voltage	$V_{\mathtt{S}}$	V	+0.5		+4.5	*	
Supply voltage	$U_{\mathrm{C}\mathrm{DC}}$	V	4.75	5	5.25	*	5 V ±5 % DC
Rise time of $U_{\rm C}$ (10-90 %)	$t_{ m rise}$	ms			100		
Current consumption @ $U_{\rm C}$ = +5 V at $V_{\rm P}$ = 0 V	I_{C}	mA		35			See temperature variation on figure 4
Offset voltage	V_{o}	V	2.492	2.5	2.508		100 % tested in production
Temperature variation of $V_{\rm O}$	V_{OT}	mV	-25		25	*	−40 85 °C
Sensitivity	G	mV/V		1.333			2 V for primary 1500 V
Sensitivity error	$oldsymbol{arepsilon}_{G}$	%	-0.8		0.8		$\varepsilon_{_G}$ @ $V_{_{\mathrm{P}\mathrm{N}}}$
Thermal drift of sensitivity	$arepsilon_{G\ T}$	%	-0.4		0.4	*	
Linearity error	$arepsilon_{L}$	% of $V_{\rm PM}$	-0.2		0.2		±1500 V range
			-1		1		25 °C; 100 % tested in
Overall accuracy	X_G	% of $V_{\rm PN}$	-1.5		1.5	*	production; −40 85 °C
Output RMS noise voltage	V_{no}	μV		600			10 Hz to 100 kHz
Reaction time @ 10 % of $V_{\rm PN}$	$t_{\rm ra}$	μs		1.7			
Response time @ 90 % of $V_{\rm PN}$	$t_{\rm r}$	μs		10			0 to 1000 V step, 6 kV/μs
Frequency bandwidth	BW	kHz		47 26			−3 dB −1 dB
Start-up time	$t_{ m start}$	ms		20		*	
Resistance of primary	$R_{\rm p}$	ΜΩ		12.6		*	
Total primary power loss @ $V_{\scriptscriptstyle \sf PN}$	P_{P}	W		0.080		*	

Definition of typical, minimum and maximum values

Minimum and maximum values for specified limiting and safety conditions have to be understood as such as well as values shown in "typical" graphs.

On the other hand, measured values are part of a statistical distribution that can be specified by an interval with upper and lower limits and a probability for measured values to lie within this interval.

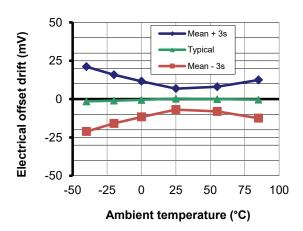
Unless otherwise stated (e.g. "100 % tested"), the LEM definition for such intervals designated with "min" and "max" is that the probability for values of samples to lie in this interval is 99.73 %.

For a normal (Gaussian) distribution, this corresponds to an interval between -3 sigma and +3 sigma. If "typical" values are not obviously mean or average values, those values are defined to delimit intervals with a probability of 68.27 %, corresponding to an interval between -sigma and +sigma for a normal distribution.

Typical, maximal and minimal values are determined during the initial characterization of a product.

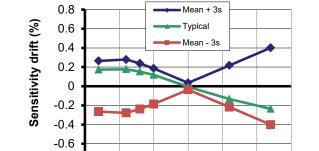


Typical performance characteristics



1.5 Overall accuracy (%) 0.5 0 Typical -0.5 Mean - 3s -1.5 -50 -25 0 25 50 75 100 Ambient temperature (°C)

Figure 1: Electrical offset thermal drift



0

Figure 2: Overall accuracy in temperature

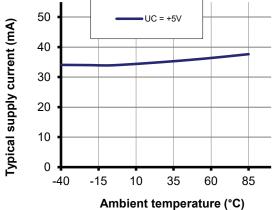
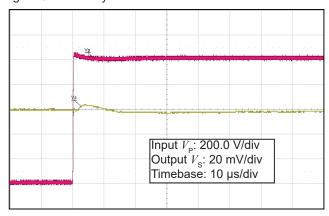


Figure 3: Sensitivity thermal drift

-25

-0.8

-50



25

Ambient temperature (°C)

50

75

100

Figure 4: Supply current function of temperature

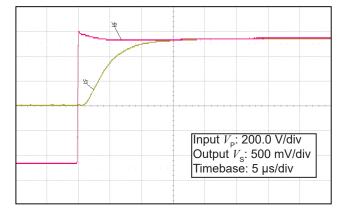
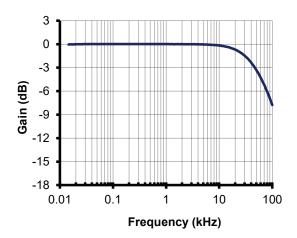


Figure 5: Detail of typical common mode perturbation (1000 V step with 6 kV/ μ s, $R_{\rm M}$ = 2 k Ω)

Figure 6: Typical step response (0 to 1000 V)



Typical performance characteristics



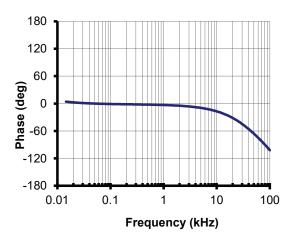
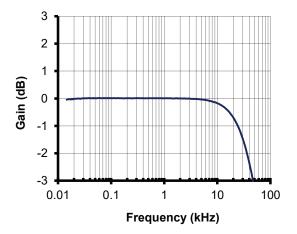


Figure 7: Typical frequency and phase response



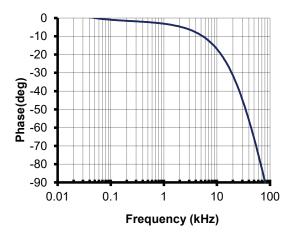


Figure 8: Typical frequency and phase response (detail)



Typical performance characteristics continued

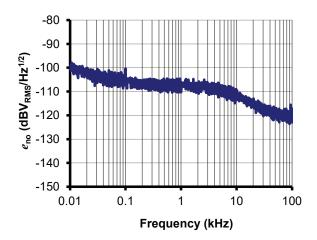


Figure 9: : Typical output noise voltage spectral density $e_{\mbox{\tiny no}}$ with $R_{\rm M} = 2 \text{ k}\Omega$

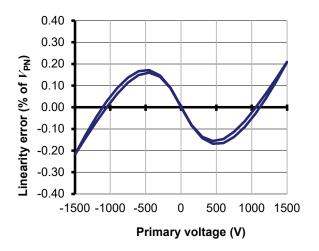


Figure 11: Typical linearity error at 25 °C

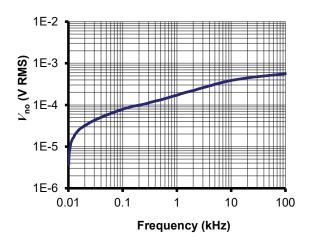


Figure 10: Typical total output RMS noise voltage with $R_{\rm M} = 2 \text{ k}\Omega$

Figure 9 (output noise voltage spectral density) shows that there are no significant discrete frequencies in the output. Figure 10 confirms the absence of steps in the total output RMS noise voltage that would indicate discrete frequencies. To calculate the noise in a frequency band f1 to f2, the formula is:

$$V_{\text{no}}(f1 \text{ to } f2) = \sqrt{V_{\text{no}}(f2)^2 - V_{\text{no}}(f1)^2}$$

with $V_{no}(f)$ read from figure 10 (typical, RMS value).

Example:

What is the noise from 100 to 1 kHz? Figure 10 gives:

 $V_{\rm no}(100~{\rm Hz})$ = 78 $\mu{\rm V}$ and $V_{\rm no}(1~{\rm kHz})$ = 171 $\mu{\rm V}$. The output RMS noise current is therefore.

$$\sqrt{(171 \times 10^{-6})^2 - (78 \times 10^{-6})^2} = 152 \,\mu\text{V}$$



Performance parameters definition

The schematic used to measure all electrical parameters is:

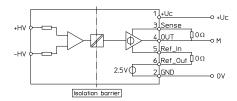


Figure 12: Standard characterization schematic for voltage output transducers

Transducer simplified model

The static model of the transducer at temperature $T_{\rm A}$ is: $V_{\rm S} = G {\cdot} V_{\rm P} + \varepsilon$ In which

$$\varepsilon = V_{\mathsf{OE}} + V_{\mathsf{OT}}(T_{\mathsf{A}}) + \varepsilon_{\mathsf{G}} \cdot G \cdot V_{\mathsf{P}} + \varepsilon_{\mathsf{GT}}(T_{\mathsf{A}}) \cdot G \cdot V_{\mathsf{P}} + \varepsilon_{\mathsf{L}} \cdot G \cdot V_{\mathsf{PM}}$$

: secondary voltage (V)

: sensitivity of the transducer (V/V)

: primary voltage (V)

: primary voltage, measuring range (V)

: ambient operating temperature (°C)

: electrical offset voltage (V)

 $V_{\cap T}(T_{\Delta})$: temperature variation of $V_{\rm O}$ at

temperature $T_{A}(V)$

: sensitivity error at 25 °C $\varepsilon_{_{G\,T}}\left(T_{_{\mathsf{A}}}\right)$: thermal drift of sensitivity at

temperature $T_{\scriptscriptstyle A}$

: linearity error

This is the absolute maximum error. As all errors are independent, a more realistic way to calculate the error would be to use the following formula:

$$\varepsilon = \sqrt{\sum_{i=1}^{N} \varepsilon_i^2}$$

Sensitivity and linearity

To measure sensitivity and linearity, the primary voltage (DC) is cycled from 0 to $V_{\rm P\,M}$, then to $-V_{\rm P\,M}$ and back to 0 (equally spaced $V_{\rm PM}/10$ steps).

The sensitivity G is defined as the slope of the linear regression

line for a cycle between $V_{\rm PM}$. The linearity error $\varepsilon_{\rm L}$ is the maximum positive or negative difference between the measured points and the linear regression line, expressed in % of the maximum measured value.

Offset

The offset voltage ${\cal V}_{\rm O}$ is the residual output voltage when the input voltage is zero.

The temperature variation $V_{\text{O},T}$ of the offset voltage V_{O} is the variation of the offset from 25 °C to the considered temperature.

Overall accuracy

The overall accuracy X_G is the error at V_{PN} , relative to the rated value $V_{\rm P\,N}.$ It includes all errors mentioned above.

Response and reaction times

The response time $t_{\rm r}$ and the reaction time $t_{\rm ra}$ are shown in the

Both depend on the primary voltage dv/dt. They are measured at nominal voltage.

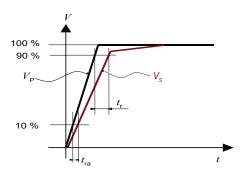
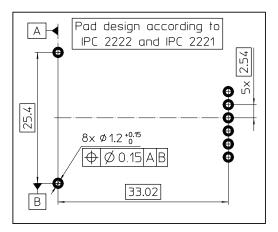


Figure 13: response time t_r and reaction time t_{ra}



PCB Footprint in mm.



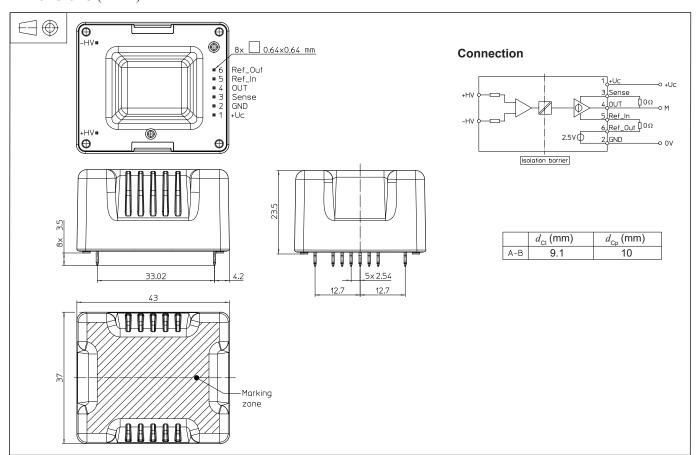
Recommended Customer PCB Layout Opposite Transducer side

Assembly on PCB

- Recommended PCB hole diameter
- Maximum PCB thickness
- 1.2 mm for primary pin
- 1.2 mm for secondary pin
- 2.4 mm



Dimensions (in mm)



Mechanical characteristics

General tolerance

0.5 mm

Transducer fastening

8 holes Ø 0.64 mm

Remarks

- ullet V_{S} is positive when a positive voltage is applied on +HV.
- The transducer is directly connected to the primary voltage.
- Care must be taken to maintain the creepage and clearance distances with the board design using proper PCB techniques.
- Installation of the transducer is to be done without primary or secondary voltage present
- Installation of the transducer must be done unless otherwise specified on the datasheet, according to LEM Transducer Generic Mounting Rules. Please refer to LEM document N°ANE120504 available on our Web site: Products/Product Documentation.
- This is a standard model. For different versions (supply voltages, sensitivity, unidirectional measurements...), please contact us.

Safety

This transducer must be used in limited-energy secondary circuits according to IEC 61010-1.



Caution

This transducer must be used in electric/electronic equipment with respect to applicable standards and safety requirements in accordance with the manufacturer's operating instructions.



Caution, risk of electrical shock

When operating the transducer, certain parts of the module can carry hazardous voltage (e.g. primary connections, power supply). Ignoring this warning can lead to injury and/ or cause serious damage. This transducer is a build-in device, whose conducting parts must be inaccessible after installation. A protective housing or additional shield could be used. Main supply must be able to be disconnected. All installations, maintenance and servicing operations must be carried out by trained operators. Failure to observe the installation instructions may compromise the protection provided by the unit.