Darlington Transistor TO-3

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Description:

Darlington complementary silicon power transistors Designed for general-purpose amplifier and low frequency switching applications

Features:

- High DC current gain h_{FE} = 3,500 (typical) at I_C = 5A DC
- Collector-emitter sustaining voltage at 100mA
- V_{CEO} (sus) = 100V DC (min.)
- Monolithic construction with built-in-base-emitter shunt resistors
- Pb-free device

Maximum Ratings

Rating	Symbol	Value	Unit	
Collector-Emitter Voltage	V _{CEO}	100		
Collector-Base Voltage	V _{CB}	100	V DC	
Emitter-Base Voltage	V _{EB}	5		
Collector Current -Continuous -Peak	۱ _C	12 20	A DC	
Base Current	Ι _Β	0.2		
Total Power Dissipation at T _C = 25°C Derate above 25°C	P _D	150 0.857	W W/°C	
Operating and Storage Junction Temperature Range	T _J , T _{STG}	-65 to +200	°C	

Thermal Characteristics

Characteristic	Symbol	Max.	Unit
Thermal Resistance Junction to Case	R _{ejc}	1.17	°C/W

Stresses exceeding maximum ratings may damage the device. Maximum ratings are stress ratings only. Functional operation above the recommended operating conditions is not implied. Extended exposure to stresses above the recommended operating conditions may affect device reliability.

1. Indicates JEDEC registered data.



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Electrical Characteristics (TC = 25°C unless otherwise noted) (Note 2)

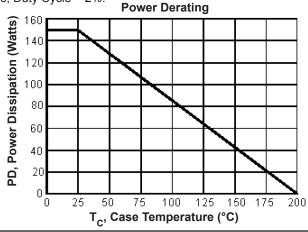
Characteristic	Symbol	Min.	Max.	Unit
Off Characteristics				
Collector-Emitter Sustaining Voltage (Note 3) ($I_C = 100$ mA DC, $I_B = 0$)	V _{CEO (sus)}	100	-	V DC
Collector Cut off Current (V_{CE} = 50V DC, I_B = 0)	I _{CEO}	-	1	mA DC
Collector Cut off Current (V_{CE} = Rated V_{CEO} , $V_{BE (off)}$ = 1.5V DC) (V_{CE} = Rated V_{CEO} , $V_{BE (off)}$ = 1.5V DC, T_C = 150°C)	I _{CEX}	-	0.5 5	
Emitter Cut off Current (V_{BE} = 5V DC, I_{C} = 0)	I _{EBO}	-	2	
On Characteristics (Note 3)				
DC Current Gain ($I_C = 6A DC$, $V_{CE} = 3V DC$) ($I_C = 12A DC$, $V_{CE} = 3V DC$)	h _{FE}	750 100	18,000 -	-
Collector-Emitter Saturation Voltage ($I_C = 6A DC$, $I_B = 24mA DC$) ($I_C = 12A DC$, $I_B = 120mA DC$)	V _{CE (sat)}	-	2 3	V DC
Base-Emitter Saturation Voltage ($I_C = 12A DC$, $I_B = 120mA DC$)	V _{BE (sat)}	-	4	
Base-Emitter On Voltage (I _C = 6A DC, V _{CE} = 3V DC)	V _{BE (on)}	-	2.8	
Dynamic Characteristics				
Magnitude of Common-Emitter Small-Signal Short-Circuit Forward Current Transfer Ratio (I_C = 5A DC, V_{CE} = 3V DC, f = 1MHz)	h _{fe}	4	-	MHz
Output Capacitance (V_{CB} = 10V DC, I_E = 0, f = 0.1MHz)	C _{ob}	-	500 300	pF
Small-Signal Current Gain (I _C = 5A DC, V _{CE} = 3V DC, f = 1kHz)	h _{fe}	300	-	-

 $(I_C = 5A DC, V_{CE} = 3V DC, f = 1kHz)$

Note:

(2) Indicates JEDEC Registered Data.

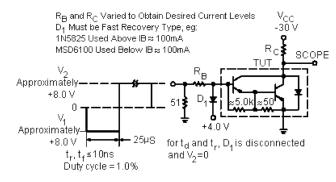
(3) Pulse test: Pulse Width = 300µs, Duty Cycle = 2%.



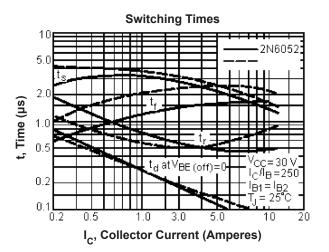


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Switching Times Test Circuit

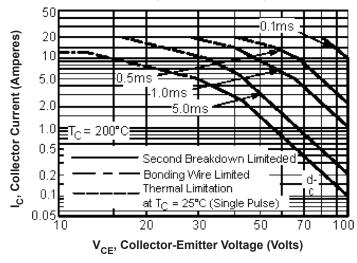


For NPN Test Circuit Reverse Diode and Voltage Polarities



Thermal Response 1.0Effective Transient Thermal 0.7 0.5 Resistance (Normalized) 0.3 0.2 0.2 P_(pk) $R\theta_{JC}(t) = r(t) R\theta_{JC}$ 0.1 0.0 Rejc = 1.17°C/V Maximum 0.07 D Curves Apply for Power Pulse Train Shown Read Time at t₁ 0.05 0.03 Тј (pk) - Т_С = Р(pk)^өЈС ^(†) Duty cycle, D = t₁ Sinale 0.02 r (t), Pulse 0.011.0 0.01 0.02 0.030.05 0.1 0.2 0.3 0.5 2.0 3.0 5.020 30 50 100 200 300 500 10 1000 t, Time (ms)

Active-Region Safe Operating Area



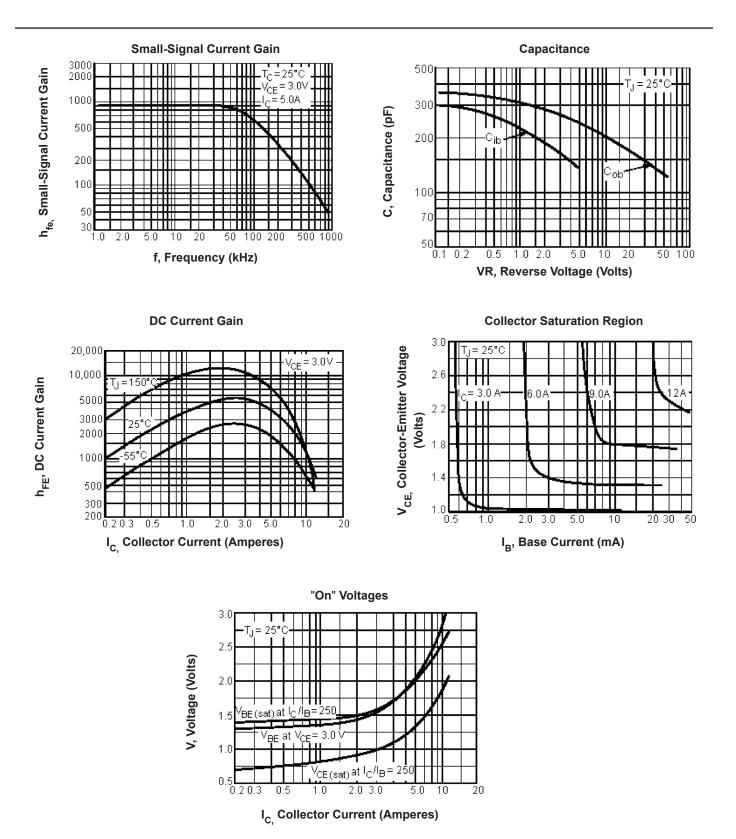
Newark.com/multicomp-pro Farnell.com/multicomp-pro Element14.com/multicomp-pro There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data is based on $T_{J(pk)} = 200^{\circ}C$; T_C is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} \le 200^{\circ}C$; $T_{J(pk)}$ may be calculated from the data. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.



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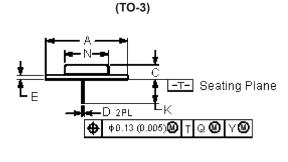
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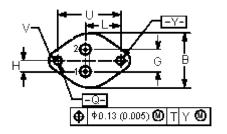




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Dimensions





A 1.55 (39.37) Reference В 1.05 (26.67) -С 0.25 (6.35) 0.335 (8.51) D 0.038 (0.97) 0.043 (1.09) Е 0.055 (1.4) 0.07 (1.77) G 0.43 (10.92) BSC Н 0.215 (5.46) BSC Κ 0.44 (11.18) 0.48 (12.19) L 0.665 (16.89) BSC Ν 0.83 (21.08) Q 0.151 (3.84) 0.165 (4.19) U 1.187 (30.15) BSC V 0.131 (3.33) 0.188 (4.77)

Min.

Max.

Dimensions

Pin Configuration Pin 1. Base

2. Emitter Collector (Case)

Dimensions : Inches (Millimetres)

Part Number Table

Description	Part Number	
Darlington Transistor, TO-3	2N6052	

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