

**EB13E2D2H-50.000M TR** [Click part number to visit Part Number Details page](#)
**REGULATORY COMPLIANCE** (Data Sheet downloaded on Jan 8, 2020)

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**ITEM DESCRIPTION**

 Quartz Crystal Clock Oscillators XO (SPXO) LVCMOS (CMOS) 3.3Vdc 4 Pad 2.5mm x 3.2mm Ceramic Surface Mount (SMD) 50.000MHz  $\pm 50$ ppm over -20°C to +70°C

**ELECTRICAL SPECIFICATIONS**

<b>Nominal Frequency</b>	50.000MHz
<b>Frequency Tolerance/Stability</b>	$\pm 50$ ppm Maximum over -20°C to +70°C (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°C, Shock, and Vibration)
<b>Supply Voltage</b>	3.3Vdc $\pm 5\%$
<b>Input Current</b>	6mA Maximum
<b>Output Voltage Logic High (Voh)</b>	90% of Vdd Minimum (IOH= -4mA)
<b>Output Voltage Logic Low (Vol)</b>	10% of Vdd Maximum (IOL= +4mA)
<b>Rise/Fall Time</b>	4nSec Maximum (Measured at 20% to 80% of waveform)
<b>Duty Cycle</b>	50 $\pm 5$ (%) (Measured at 50% of waveform)
<b>Load Drive Capability</b>	15pF Maximum
<b>Output Logic Type</b>	CMOS
<b>Pin 1 Connection</b>	Tri-State (High Impedance)
<b>Tri-State Input Voltage (Vih and Vil)</b>	80% of Vdd Minimum or No Connect to Enable Output, 20% of Vdd Maximum to Disable Output (High Impedance)
<b>Standby Current</b>	10 $\mu$ A Maximum (Disabled Output: High Impedance)
<b>RMS Phase Jitter</b>	1pSec Maximum (Fj = 12kHz to 20MHz)
<b>Start Up Time</b>	10mSec Maximum
<b>Storage Temperature Range</b>	-55°C to +125°C

**ENVIRONMENTAL & MECHANICAL SPECIFICATIONS**

<b>ESD Susceptibility</b>	MIL-STD-883, Method 3015, Class 1, HBM: 1500V
<b>Fine Leak Test</b>	MIL-STD-883, Method 1014, Condition A
<b>Flammability</b>	UL94-V0
<b>Gross Leak Test</b>	MIL-STD-883, Method 1014, Condition C
<b>Mechanical Shock</b>	MIL-STD-883, Method 2002, Condition B
<b>Moisture Resistance</b>	MIL-STD-883, Method 1004
<b>Moisture Sensitivity</b>	J-STD-020, MSL 1
<b>Resistance to Soldering Heat</b>	MIL-STD-202, Method 210, Condition K
<b>Resistance to Solvents</b>	MIL-STD-202, Method 215
<b>Solderability</b>	MIL-STD-883, Method 2003
<b>Temperature Cycling</b>	MIL-STD-883, Method 1010, Condition B
<b>Vibration</b>	MIL-STD-883, Method 2007, Condition A

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### MECHANICAL DIMENSIONS (all dimensions in millimeters)



PIN	CONNECTION
1	Tri-State
2	Case/Ground
3	Output
4	Supply Voltage

LINE	MARKING
1	<b>E50.0</b> E=Ecliptek Designator
2	<b>XXXXX</b> XXXXX=Ecliptek Manufacturing Identifier

### Suggested Solder Pad Layout

All Dimensions in Millimeters



All Tolerances are  $\pm 0.1$

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## OUTPUT WAVEFORM & TIMING DIAGRAM



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## Test Circuit for CMOS Output



Note 1: An external 0.01 $\mu$ F ceramic bypass capacitor in parallel with a 0.1 $\mu$ F high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.

Note 3: Capacitance value C<sub>L</sub> includes sum of all probe and fixture capacitance.

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## Tape & Reel Dimensions

Quantity Per Reel: 1,000 units

All Dimensions in Millimeters

Compliant to EIA-481



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## Recommended Solder Reflow Methods



### High Temperature Infrared/Convection

<b><math>T_S</math> MAX to <math>T_L</math> (Ramp-up Rate)</b>	3°C/Second Maximum
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#### Preheat

- Temperature Minimum ( $T_S$ MIN)	150°C
- Temperature Typical ( $T_S$ TYP)	175°C
- Temperature Maximum ( $T_S$ MAX)	200°C
- Time ( $t_s$ MIN)	60 - 180 Seconds

<b>Ramp-up Rate (<math>T_L</math> to <math>T_P</math>)</b>	3°C/Second Maximum
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#### Time Maintained Above:

- Temperature ( $T_L$ )	217°C
- Time ( $t_L$ )	60 - 150 Seconds

<b>Peak Temperature (<math>T_P</math>)</b>	260°C Maximum for 10 Seconds Maximum
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<b>Target Peak Temperature (<math>T_P</math> Target)</b>	250°C +0/-5°C
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<b>Time within 5°C of actual peak (<math>t_p</math>)</b>	20 - 40 Seconds
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<b>Ramp-down Rate</b>	6°C/Second Maximum
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<b>Time 25°C to Peak Temperature (t)</b>	8 Minutes Maximum
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<b>Moisture Sensitivity Level</b>	Level 1
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## Recommended Solder Reflow Methods



### Low Temperature Infrared/Convection 240°C

Ts MAX to TL (Ramp-up Rate)	5°C/Second Maximum
<b>Preheat</b>	
- Temperature Minimum (Ts MIN)	N/A
- Temperature Typical (Ts TYP)	150°C
- Temperature Maximum (Ts MAX)	N/A
- Time (ts MIN)	60 - 120 Seconds
Ramp-up Rate (TL to TP)	5°C/Second Maximum
<b>Time Maintained Above:</b>	
- Temperature (TL)	150°C
- Time (tL)	200 Seconds Maximum
Peak Temperature (TP)	240°C Maximum
Target Peak Temperature (TP Target)	240°C Maximum 2 Times / 230°C Maximum 1 Time
Time within 5°C of actual peak (tp)	10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time
Ramp-down Rate	5°C/Second Maximum
Time 25°C to Peak Temperature (t)	N/A
Moisture Sensitivity Level	Level 1

### Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum.

### High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum.