



# 200630799 EFR32MG22 EFR32BG22 Full Production Data Sheet Rev 1.0 and Errata Rev 0.4

**PCN Issue Date:** 6/30/2020

**Effective Date:** 10/6/2020

**PCN Type:** Datasheet; Errata

## Description of Change

Silicon Labs is pleased to announce available of Data Sheet Rev 1.0 for the EFR32MG22 and EFR32BG22 Wireless Gecko SoC families. All OPNs have achieved Full Production (FP). The Errata have been updated to Rev 0.4.

Notable updates in the Data sheet:

- Finalized remaining MIN / MAX specifications according to characterization and qualification results.
- Table 4.30 GPIO Pins (3V GPIO pins): Relaxed 3.8 V, 125 °C GPIO maximum leakage specification on PA00, PA03, PB00, PC03, PC04, and PD00 from 400 nA to 550 nA.
- Updated DCDC lifetime condition guidance in 4.4.1 DC-DC Operating Limits.
- Expanded IADC descriptions to include information at higher oversampling ratios and added typical performance curve
- Corrected tolerance of dimension "L" in Table 8.1 TQFN32 Package Dimensions

Notable new Errata:

- RADIO\_E303 – RAIL packet filters work incorrectly when header is enabled
- USART\_E302 — Additional SCLK Pulses are generated in the USART Synchronous Mode

## Reason for Change

Updated Data Sheet and Errata

## Impact on Form, Fit, Function, Quality, Reliability

Change in function (Relaxed 3.8 V, 125 °C GPIO maximum leakage specification on specific GPIO pins)

## Product Identification

Existing Part #  
EFR32MG22C224F512GN32-C  
EFR32MG22C224F512IM32-C  
EFR32MG22C224F512IM40-C  
EFR32BG22C222F352GM32-C  
EFR32BG22C222F352GM40-C  
EFR32BG22C222F352GN32-C  
EFR32BG22C224F512GM32-C  
EFR32BG22C224F512GM40-C  
EFR32BG22C224F512GN32-C  
EFR32BG22C224F512IM32-C  
EFR32BG22C224F512IM40-C

**Last Date of Unchanged Product:** 10/6/2020

## Qualification Samples

Available upon request.

## Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at <http://www.silabs.com>.

Customers may approve early PCN acceptance by emailing approval, along with PCN # to [PCNEarlyAcceptance@silabs.com](mailto:PCNEarlyAcceptance@silabs.com)

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## Qualification Data

Available upon request.



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**Silicon Laboratories Inc.**  
**400 West Cesar Chavez**  
**Austin, TX 78701**

<http://www.silabs.com>