



Customer Information Notification

2020100171

Issue Date: 08-Nov-2020

Effective Date: 09-Nov-2020

Dear *Emma Tempest*,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from NXP.

For detailed information we invite you to [view this notification online](#)

This notice is NXP Company Proprietary.



Change Category

<input type="checkbox"/> Wafer Fab Process	<input type="checkbox"/> Assembly Process	<input type="checkbox"/> Product Marking	<input type="checkbox"/> Test Location	<input type="checkbox"/> Design
<input type="checkbox"/> Wafer Fab Materials	<input type="checkbox"/> Assembly Materials	<input type="checkbox"/> Mechanical Specification	<input type="checkbox"/> Test Process	<input checked="" type="checkbox"/> Errata
<input type="checkbox"/> Wafer Fab Location	<input type="checkbox"/> Assembly Location	<input type="checkbox"/> Packing/Shipping/Labeling	<input type="checkbox"/> Test Equipment	<input type="checkbox"/> Electrical spec./Test coverage
<input type="checkbox"/> Firmware	<input type="checkbox"/> Other			

KL03Z(1N86K) Errata Update to Rev.10

Description

NXP Semiconductors announces that the KL03Z(1N86K) Errata have been updated to new revisions: KL03Z_1N86K Rev. 10.

There provides a detailed description of the changes:

Added following errata:

1. ERR008992, AWIC: Early NMI wakeup not detected upon entry to stop mode from VLPR mode
2. ERR008777, I2C: Address match wake-up from low-power mode cannot receive data
3. ERR009308, I2C: I2C does not hold bus between byte transfers in receive and may result in lost data
4. ERR009457, Kinetis Flashloader/ ROM Bootloader: The peripheral auto-detect code in bootloader can falsely detect presence of SPI host causing non-responsive bootloader
5. ERR008010, LLWU: CMP flag in LLWU_Fx register cleared by multiple CMP out toggles when exiting LLSx or VLLSx modes.
6. ERR010527, LPUART: Setting and immediately clearing SBK bit can result in transmission of two break characters

The updated Errata for KL03Z(1N86K) can be found at:

https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/general-purpose-mcus/kl-series-cortex-m0-plus/kinetis-kl0x-48-mhz-entry-level-ultra-low-power-microcontrollers-mcus-based-on-arm-cortex-m0-plus-core:KL0x?fpsp=1&tab=Documentation_Tab

Reason

The Errata have been updated to provide additional technical clarification on some device features.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

Additional information

Affected products and sales history information: see attached file

Additional documents: [view online](#)

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name Andrew Su
Position Systems Engineer
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At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards.

Customer Focus, Passion to Win.

NXP Quality Management Team.

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