



Product Change Notification / SYST-26OEII040

Date:

03-Mar-2021

Product Category:

8-bit Microcontrollers

PCN Type:

Silicon Die Revision

Notification Subject:

ERRATA - PIC16F15213/14/23/24/43/44 Silicon Errata and Data Sheet Clarifications Silicon Die Revision Addition

Affected CPNs:

[SYST-26OEII040_Affected_CPN_03032021.pdf](#)

[SYST-26OEII040_Affected_CPN_03032021.csv](#)

Notification Text:

SYST-26OEII040

Microchip has released a new Product Documents for the PIC16F15213/14/23/24/43/44 Silicon Errata and Data Sheet Clarifications of devices. If you are using one of these devices please read the document located at [PIC16F15213/14/23/24/43/44 Silicon Errata and Data Sheet Clarifications](#).

Notification Status: Final

Description of Change: 1. Adding silicon revision A6.

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Estimated First Ship Date: 03 Mar 2021

Markings to Distinguish Revised from Unrevised Devices: Traceability Code

Attachments:

[PIC16F15213/14/23/24/43/44 Silicon Errata and Data Sheet Clarifications](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

PIC16F15213-E/MF
PIC16F15213-E/P
PIC16F15213-E/SN
PIC16F15213-I/MF
PIC16F15213-I/P
PIC16F15213-I/SN
PIC16F15213T-I/MF
PIC16F15213T-I/SN
PIC16F15214-E/MF
PIC16F15214-E/P
PIC16F15214-E/SN
PIC16F15214-I/MF
PIC16F15214-I/P
PIC16F15214-I/SN
PIC16F15214T-I/MF
PIC16F15214T-I/SN
PIC16F15223-E/MG
PIC16F15223-E/P
PIC16F15223-E/SL
PIC16F15223-E/ST
PIC16F15223-I/MG
PIC16F15223-I/P
PIC16F15223-I/SL
PIC16F15223-I/ST
PIC16F15223T-I/MG
PIC16F15223T-I/SL
PIC16F15223T-I/ST
PIC16F15224-E/MG
PIC16F15224-E/P
PIC16F15224-E/SL
PIC16F15224-E/ST
PIC16F15224-I/MG
PIC16F15224-I/P
PIC16F15224-I/SL
PIC16F15224-I/ST
PIC16F15224T-I/MG
PIC16F15224T-I/SL
PIC16F15224T-I/ST
PIC16F15243-E/P
PIC16F15243-E/REB
PIC16F15243-E/SO
PIC16F15243-E/SS
PIC16F15243-I/P
PIC16F15243-I/REB
PIC16F15243-I/SO
PIC16F15243-I/SS

PIC16F15243T-I/REB
PIC16F15243T-I/SO
PIC16F15243T-I/SS
PIC16F15244-E/P
PIC16F15244-E/REB
PIC16F15244-E/SO
PIC16F15244-E/SS
PIC16F15244-I/P
PIC16F15244-I/REB
PIC16F15244-I/SO
PIC16F15244-I/SS
PIC16F15244T-I/REB
PIC16F15244T-I/SO
PIC16F15244T-I/SS



PIC16F15213/14/23/24/43/44

PIC16F15213/14/23/24/43/44 Silicon Errata and Data Sheet Clarifications

The PIC16F15213/14/23/24/43/44 devices that you have received conform functionally to the current device data sheet (DS40002195B), except for the anomalies described in this document.

The silicon issues discussed in the following pages are for silicon revisions with the Device and Revision IDs listed in the table below.

The errata described in this document will be addressed in future revisions of the PIC16F15213/14/23/24/43/44 silicon.

Note: This document summarizes all silicon errata issues from all revisions of silicon, previous as well as current.

Table 1. Silicon Device Identification

Part Number	Device ID	Revision ID		
		A3	A5	A6
PIC16F15213	0x30E3	0x2003	0x2005	0x2006
PIC16F15214	0x30E6	0x2003	0x2005	0x2006
PIC16F15223	0x30E4	0x2003	0x2005	0x2006
PIC16F15224	0x30E7	0x2003	0x2005	0x2006
PIC16F15243	0x30E5	0x2003	0x2005	0x2006
PIC16F15244	0x30E8	0x2003	0x2005	0x2006



Important: Refer to the **Device/Revision ID** section in the current “**PIC16F152xx Family Programming Specification**” (DS40002149) for more detailed information on Device Identification and Revision IDs for your specific device.

Table 2. Silicon Issue Summary

Module	Feature	Item No.	Issue Summary	Affected Revisions		
				A3	A5	A6
Nonvolatile Memory (NVM)	Self-Write Operation	1.1.1	Self-write operation may not work above 85°C with V _{DD} above 5.3V	X		
Note: Only those issues indicated in the last column apply to the current silicon revision.						

1. Silicon Errata Issues



Notice: This document summarizes all silicon errata issues from all revisions of silicon, previous as well as current. Only the issues indicated by the bold font in the following tables apply to the current silicon revision.

1.1 Module: Nonvolatile Memory (NVM)

1.1.1 Self-Write Operation

When performing self-write operations at 85°C or above, the writes may not occur at V_{DD} voltages above 5.3V.

Work around

Perform self-write operations at a temperature below 85°C while operating at V_{DD} voltages above 5.3V.

Affected Silicon Revisions

A3	A5	A6					
X							

2. Data Sheet Clarifications

The following typographic corrections and clarifications are to be noted for the latest version of the device data sheet (40002195B):

Note:

Corrections are shown in **bold**. Where possible, the original bold text formatting has been removed for clarity.

2.1 None

There are no known data sheet clarifications as of this publication date.

3. Appendix A: Revision History

Doc. Rev.	Date	Comments
C	02/2021	Adding silicon revision A6.
B	10/2020	Adding silicon revision A5.
A	09/2020	Initial document release.

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