



Customer Information Notification



Customer Information Notification

202102036I : Power Management IC Data Sheet Updates for Output Accuracy Spec Improvement and Clarifications

Note: This notice is NXP Company Proprietary.

Issue Date: Feb 26, 2021 **Effective date:**Feb 27, 2021



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Change Category

- Wafer Fab Process
- Assembly Process
- Product Marking
- Test Process
- Design
- Wafer Fab Materials
- Assembly Materials
- Mechanical Specification
- Test Equipment
- Errata
- Wafer Fab Location
- Assembly Location
- Packing/Shipping/Labeling
- Test Location
- Electrical spec./Test coverage
- Firmware
- Other: Data Sheet Update (Specification Improvement and Clarifications)

PCN Overview

Description

NXP Semiconductors announces data sheet updates for the PF8100_PF8200, PF8101_PF8201 and PF8121 family devices, including all applicable derivative products, associated with this notification.

The new data sheet revisions provide updated specification information and clarifications, including the tightening of the output accuracy specification on all Type 1 switching regulators, as well as increasing the storage temperature range.

The revision history included in the updated documents provides a detailed description of the changes. Changes are summarized below.

PF8100_PF8200, PF8101_PF8201, and PF8121 Data Sheet Change Summary:

1. Output accuracy of the Type 1 Buck regulators (SW1 - SW6) is split in four operating ranges to

allow tighter accuracy at higher output voltage configuration. Output voltages from 0.8V to 1.0V provide a specification improvement from +/-2.0% to +/-1.5% output accuracy.

2. The minimum storage temperature is extended from -40C to -55C.
3. Figure 21 is corrected to depict the proper behavior of the XFAILB when the PWRON pin is still asserted low.

** Documentation changes only for tighter (better) switching regulator output accuracy, storage temperature, and technical clarifications, corrections - absolutely no changes to the device / product **

PF8100_PF8200 new revision 11.0 data sheet is attached to this notice and is available at:

https://www.nxp.com/docs/en/data-sheet/PF8100_PF8200.pdf

PF8101_PF8201 new revision 4.0 data sheet is attached to this notice and is available at:

https://www.nxp.com/docs/en/data-sheet/PF8101_PF8201.pdf

PF8121 new revision 5.0 data sheet is attached to this notice and is available at:

<https://www.nxp.com/docs/en/data-sheet/PF8121.pdf>

Corresponding ZVEI Delta Qualification Matrix ID: SEM-DS-02

Reason

Switching regulator accuracy specification is tightened to provide specification improvement on output voltages from 0.8 to 1.0V. Storage temperature range is increased to provide improvement on the minimum temperature. XFAILB figure is corrected.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Data Sheet Revision

A new datasheet will be issued

Additional information

Additional documents: [view online](#)

Contact and Support

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For specific questions on this notice or the products affected please contact our specialist directly:

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: MC56F84xxx Chip Errata Rev Dec 2020 Update

Note: This notice is NXP Company
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Issue Date: Feb 25, 2021 **Effective date:**Feb 26, 2021

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<input type="checkbox"/> Wafer Fab Materials	<input type="checkbox"/> Assembly Materials	<input type="checkbox"/> Mechanical Specification	<input type="checkbox"/> Test Equipment	<input checked="" type="checkbox"/> Errata
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<input type="checkbox"/> Firmware <input type="checkbox"/> Other				

PCN Overview

Description

NXP Semiconductors announces chip errata update to revision Dec 2020 for MC56F84xxx. The revision history included in the updated document provides a detailed description of the changes. Changes are summarized below.

MC56F84xxx Chip Errata has the following changes:

? Updated workaround description in the following errata:

? e11484: POR: Residual voltage on VDD may cause POR unsuccessful

The MC56F84xxx errata is attached to this notice, and can be found at:

https://www.nxp.com/products/processors-and-microcontrollers/additional-mpu-mcus-architectures/digital-signal-controllers/32-bit-56f8xxx-families/digital-signal-controllers:MC56F84xxx?tab=Documentation_Tab&linkline=Errata

Reason

The errata was modified to provide more accurate workaround to avoid the errata issue.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Additional information

Additional documents: [view online](#)

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